UM2502
User manual

STLINK-V3MODS and STLINK-V3MINI debugger/programmer tiny probes for STM32 microcontrollers

Introduction

STLINK-V3MODS and STLINK-V3MINI are stand-alone debugging and programming tiny probes for STM32 microcontrollers. These products are designed in a very low form factor and both offer high performance without any compromise to functions. They support the JTAG/SWD interfaces for communication with any STM32 microcontroller located on an application board.

They provide a Virtual COM port interface allowing the host PC to communicate with the target microcontroller through one UART. The STLINK-V3MODS also provides bridge interfaces to several communication protocols allowing for instance the programming of the target through the bootloader.

The STLINK-V3MODS and STLINK-V3MINI are both proposed for different uses. The STLINK-V3MODS may be directly soldered on a host PCB including an STM32 application-based with its 2 x 16-pin castellated vias connection, while the STLINK-V3MINI offers STDC14 connectivity with an included STDC14 to STDC14 flat cable.

Note: STLINK-V3MINI is obsolete and has been replaced by STLINK-V3MINIE. For more information, refer to the corresponding product pages at st.com.

Figure 1. STLINK-V3MODS product top view

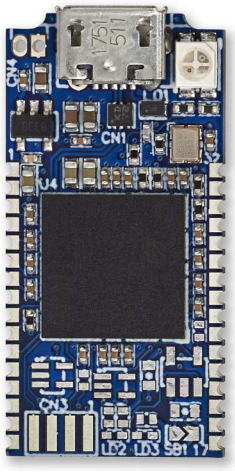
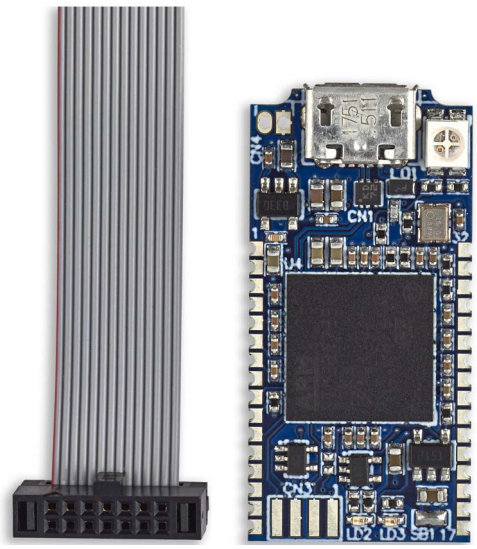


Figure 2. STLINK-V3MINI product top view



Pictures are not contractual.

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1 Features

Common features

- Tiny 15 mm x 30 mm standalone debugging and programming probes for STM32 microcontrollers
- Self-powered through a USB Micro-B connector
- USB 2.0 high-speed interface
- Probe firmware update through USB
- Optional drag-and-drop flash memory programming of binary files
- Two-color LEDs: communication, power
- JTAG communication support up to 21 MHz
- SWD (Serial Wire Debug) and SWV (Serial Wire Viewer) communication support up to 24 MHz
- Virtual COM port (VCP) up to 16 Mbps
- 3.0 to 3.6 V application voltage support and 5 V tolerant inputs

STLINK-V3MODS features

- Direct-to-PCB implementation by 2 x 16-pin 1.27 mm edge castellated vias with all signals available in a minimum PCB required surface
- Multipath bridge USB to SPI/UART/I^2C/CAN/GPIOs

STLINK-V3MINI features

- 1.27 mm pitch STDC14 debug connector with STDC14 to STDC14 flat cable
- STDC14 signals protection

Note: The STLINK-V3MINI tiny probe does not provide any power supply to the target application.

2 Ordering information

To order the STLINK-V3MODS or STLINK-V3MINI tiny probe, refer to *Table 1*.

Table 1. Ordering information

Order code	Content and references	Description	Differentiating feature
STLINK-V3MODS	– MB1467(1)	STLINK-V3 in-circuit debugger and programmer for STM32 microcontrollers	– 2 x 16-pin castellated vias
STLINK-V3MINI	– MB1467(1) – FFC(2)		– STDC14 connector – Flexible flat cable

1. Tiny probe board
2. Flexible flat cable

3 Development environment

The STLINK-V3MODS and STLINK-V3MINI tiny probes run with an STM32 32-bit microcontroller based on the Arm®(a) Cortex®-M core.

3.1 System requirements

- Multi-OS support: Windows® 10 or 11, Linux® 64-bit, or macOS®(b)(c)(d)
- USB Type-A or USB Type-C® to Micro-B cable

3.2 Development toolchains

- IAR Systems® - IAR Embedded Workbench®(e)
- Keil® - MDK-ARM(e)
- STMicroelectronics - STM32CubeIDE

3.3 Firmware upgrade

The STLINK-V3MODS and STLINK-V3MINI tiny probes embed firmware which needs to be frequently updated from the *www.st.com* website to benefit from new functionality or corrections. Refer to the technical note *Overview of ST-LINK derivatives* (TN1235) for details.

a. Arm is a registered trademark of Arm Limited (or its subsidiaries) in the US and or elsewhere.

b. macOS® is a trademark of Apple Inc. registered in the U.S. and other countries.

c. Linux® is a registered trademark of Linus Torvalds.

d. All other trademarks are the property of their respective owners.

e. On Windows® only.

4 Quick start

This section describes how to start development quickly using the STLINK-V3MODS and STLINK-V3MINI.

Before installing and using these products, accept the Evaluation Product License Agreement from the www.st.com/epla web page.

The STLINK-V3MODS and STLINK-V3MINI are standalone debugging and programming probes for STM32 microcontrollers.

- They support protocols JTAG and SWD to communicate with any STM32 microcontroller.
- They provide a Virtual COM port interface allowing the host PC to communicate with the target microcontroller through one UART
- The STLINK-V3MODS provides bridge interfaces to several communication protocols allowing, for instance, the programming of the target through the bootloader.

To start using STLINK-V3MINI, follow the steps below:

1. Check that the STDC14 to STDC14 flat cable is present inside the box.
2. Install/update the IDE/STM32CubeProgrammer to support the STLINK-V3MINI (drivers).
3. Connect the flat cable between the STLINK-V3MINI and the application.
4. Connect a USB Type-A to Micro-B cable between the STLINK-V3MINI and the PC.
5. Check that the PWR LED is green and the COM LED is red.
6. Open the development toolchain or STM32CubeProgrammer software utility. For more details, refer to the www.st.com/stlink-v3mini website.

Using STLINK-V3MODS requires to be firstly soldered onto the destination application including the targeted STM32 microcontroller. Some recommendations are given here:

1. Reserve in the design the necessary PCB area under the STLINK-V3MODS by using the recommended PCB land pattern.
2. Apply the recommended reflow soldering profile, from *Soldering recommendations and package information for lead-free ECOPACK microcontrollers* (AN2639), and verify that contacts between host board and module meet the IPC Specification (see IPC-A-610-F Acceptability of Electronic Assemblies).
3. Install/update the IDE/STM32CubeProgrammer to support the STLINK-V3MODS (drivers).
4. Power supply the application board.
5. Connect a USB Type-A to Micro-B cable between the STLINK-V3MODS and the PC.
6. Check that the PWR LED is green and the COM LED is red.
7. Open the development toolchain or STM32CubeProgrammer software utility. For more details, refer to the www.st.com/stlink-v3mods website.

5 STLINK-V3MODS and STLINK-V3MINI functional description

5.1 STLINK-V3MODS and STLINK-V3MINI overview

STLINK-V3MODS and STLINK-V3MINI are standalone debugging and programming tiny probes for STM32 microcontrollers. These products support many functions and protocols for debugging, programming, or communicating with one or several targets.

These modules are fully powered by the PC. If the COM LED blinks red, refer to the technical note *Overview of ST-LINK derivatives* (TN1235) for details.

5.2 STLINK-V3MODS power scheme

5.2.1 Powering the board using the STLINK-V3MODS USB connector

It is possible to power the entire board using the STLINK-V3MODS USB connector. This is the preferred powering method when the target side of the board requires less than 1 W (200 mA at 5 V). It is similar to the STMicroelectronics Nucleo and Discovery boards embedding an STLINK-V3.

The power is provided by the STLINK-V3MODS USB connector, supplying both the STLINK-V3MODS microcontroller and target through pin 22 of the right-edge pin connector (5 V, up to 200 mA).

Note: To comply with USB constraints during board initialization, the power to the target is cut during the USB enumeration and during the ST-LINK firmware upgrade. It may also be cut temporarily after "drag-and-drop" programming of a new application through the mass storage interface, in case of a readout-protected target (to allow the new application to run).

Despite not being part of the initial use cases for STLINK-V3MODS, if the ST-LINK is connected to a USB port without a data connection (such as a USB charger), the power is provided to the target on pin 22 of the right-edge pin connector after a two-second timeout. STLINK-V3MODS hardware was not designed to detect a charger.

5.2.2 Debugging an externally powered target with STLINK-V3MODS

If another power source is available on the board for the target part, it is not necessary to use pin 22 of the right-edge pin connector to power it.

In this case, it is still possible to use STLINK-V3MODS for debugging, but the following must be considered:

- STLINK-V3MODS is powered only by its USB connector. During phases when STLINK-V3MODS is not powered, the state of its outputs is unpredictable, and voltage injection to its GPIOs may cause unexpected behavior in the microcontroller. If this situation is expected on the board, it must include a mechanism to isolate all signals between the ST-LINK and the target.
- The ST-LINK firmware samples the signal on pin 22 of the right-edge pin connector during its startup. A high level at this time indicates that the target is externally powered. In this case, the ST-LINK firmware does not provide any power to this pin afterwards (standard behavior since firmware V3J6). A low level is interpreted as a

request to activate the power supply to the target on this pin before attempting communication.

If the target is never (or no longer) expected to be powered by the ST-LINK, either do not connect anything to pin 22 of the right-edge pin connector, or remove SB1 to avoid any risk of conflict on the signal, even in the case of a firmware bug.

5.3 STLINK-V3MODS and STLINK-V3MINI frequency selection

The STLINK-V3MODS and STLINK-V3MINI run internally at three different frequencies:

- high-performance frequency
- standard frequency, compromising between performance and consumption
- low-consumption frequency

By default, the STLINK-V3MODS and STLINK-V3MINI start in high-performance frequency. It is the responsibility of the toolchain provider to propose or not the frequency selection at the user level.

5.4 High-performance modules

The STLINK-V3MODS and STLINK-V3MINI support high-performance configuration for STM32 microcontrollers. The working voltage range is from 3.0 to 3.6 V.

The protocols and functions supported are:

- SWD (up to 24 MHz) with SWO (up to 16 MHz)
- JTAG (up to 21 MHz)
- VCP (from 732 bps to 16 Mbps)

Figure 3. STLINK-V3MINI connections

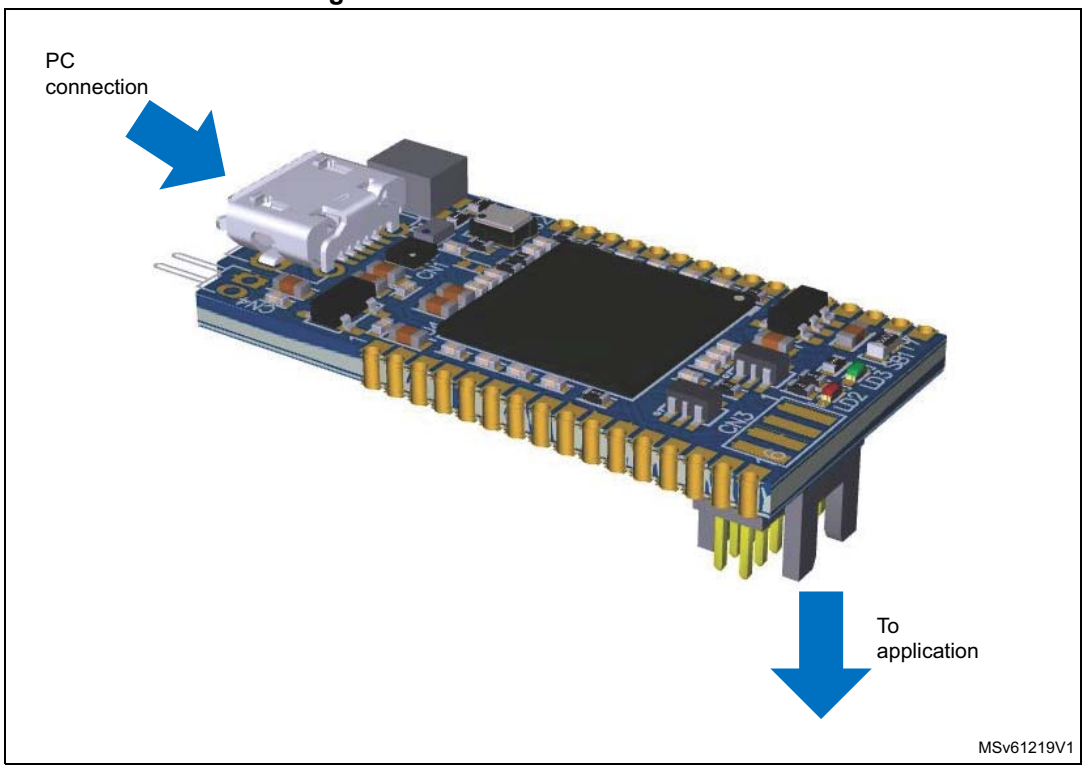
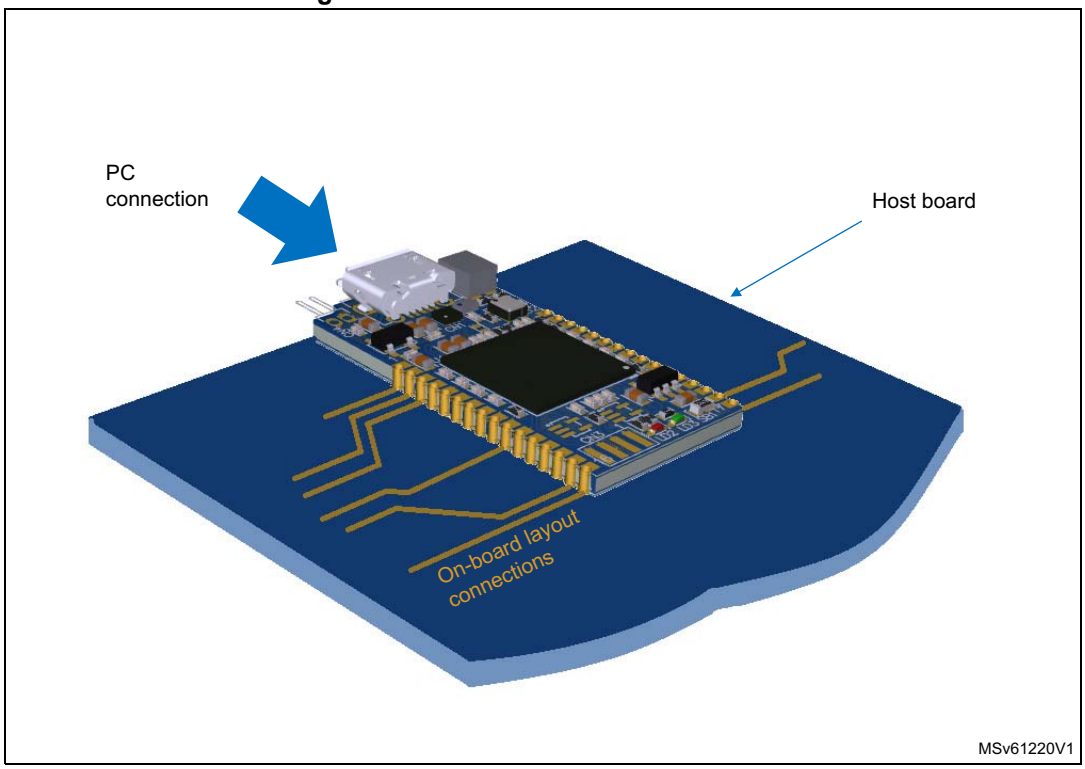


Figure 4. STLINK-V3MODS connections



For the STLINK-V3MODS, the connections are done with the host board by tracks.

5.5 Hardware layout

The STLINK-V3MODS and STLINK-V3MINI products are designed around the STM32F723 microcontroller (176-pin in UFBGA package). *Figure 5* shows the STLINK-V3MODS and *Figure 6* the STLINK-V3MINI. *Figure 7* and *Figure 8* show the top and bottom layouts for MB1467, which is the common board reference for STLINK-V3MODS and STLINK-V3MINI.

Figure 5. Hardware board STLINK-V3MODS

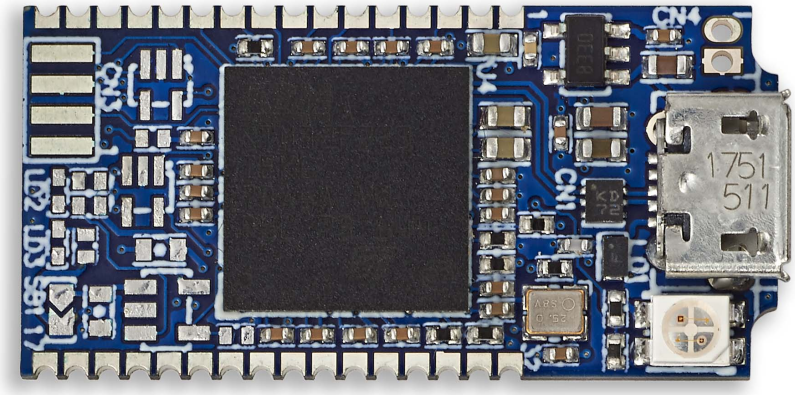


Figure 6. Hardware board STLINK-V3MINI

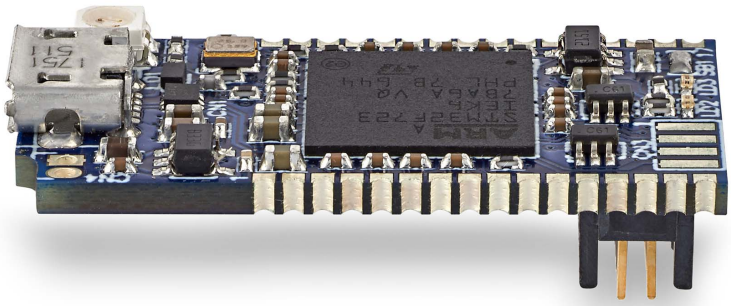


Figure 7. MB1467 top layout

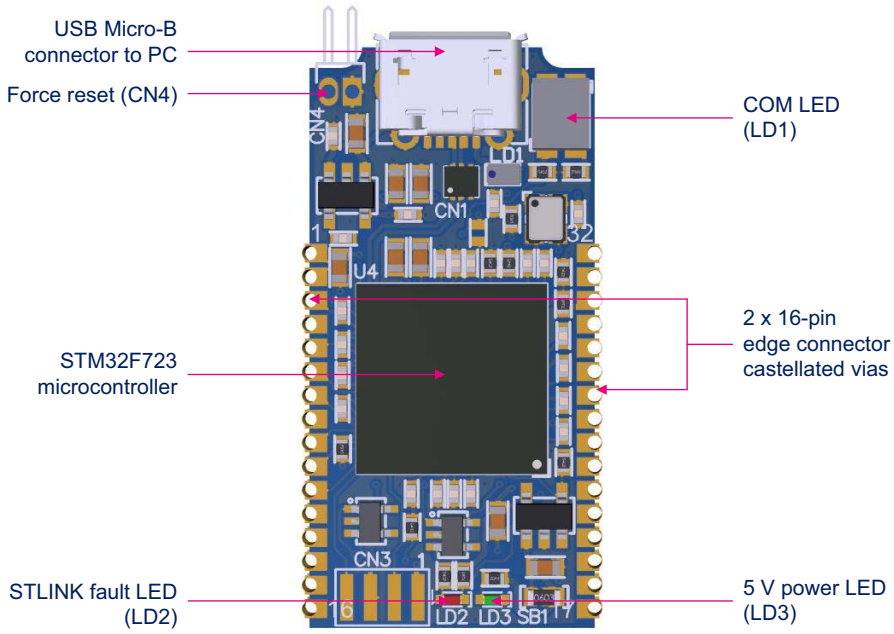


Figure 8. MB1467 bottom layout

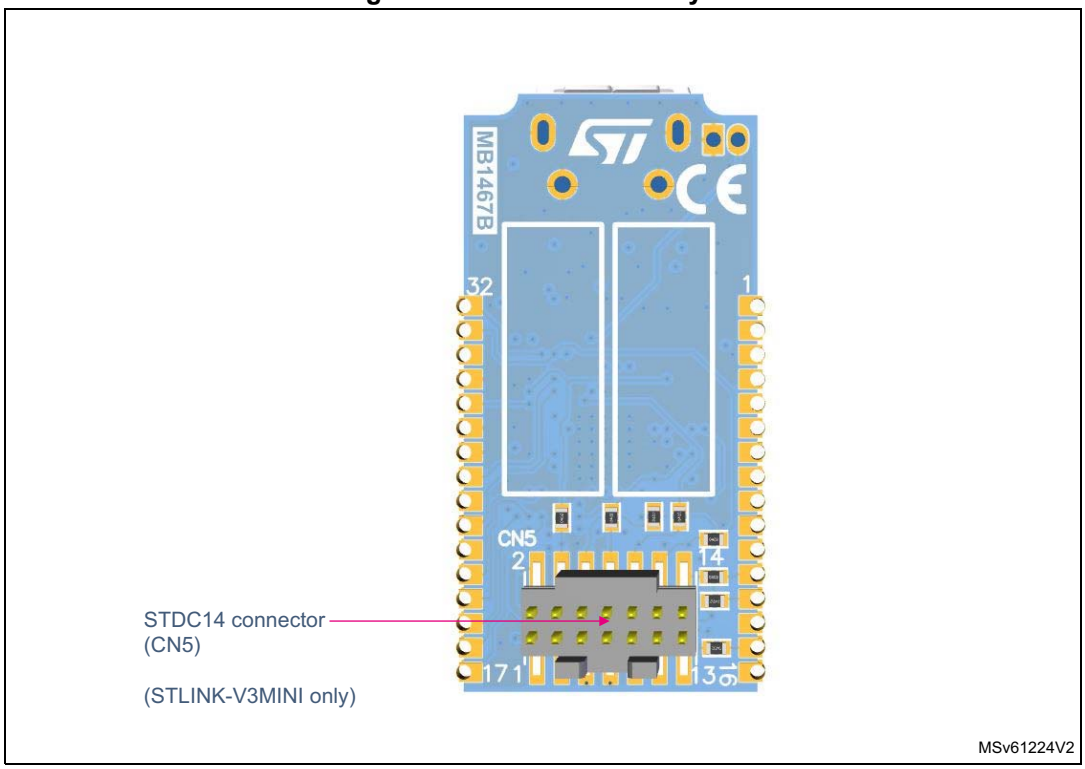
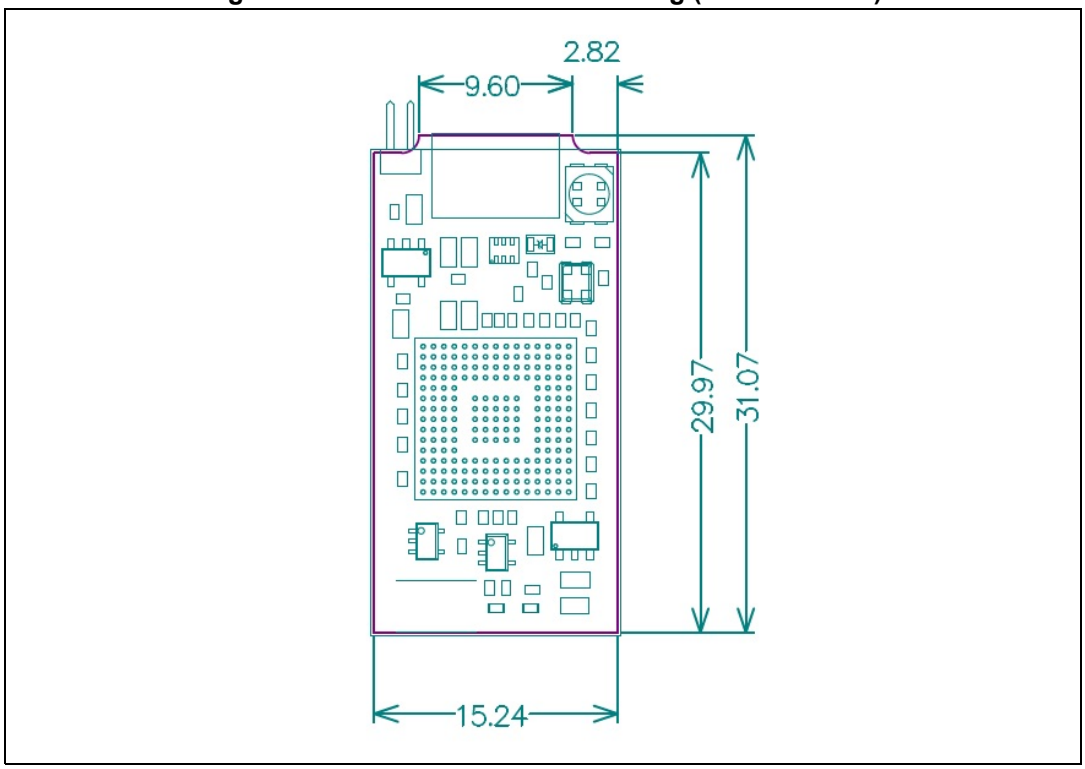


Figure 9. MB1467 mechanical drawing (in millimeters)



5.6 STLINK-V3MODS and STLINK-V3MINI functions

All functions are designed for high performance: all signals are 3.3 V compatible. The following description concerns STLINK-V3MODS and STLINK-V3MINI except for some mentioned sections and indicates where to find the functions on the boards and connectors.

5.6.1 SWD with SWV

SWD protocol is a Debug/Program protocol used for STM32 microcontrollers with SWV as a trace. The signals are 3.3 V compatible and may perform up to 24 MHz. This function is available on CN2 (STLINK-V3MODS) and CN5 (STLINK-V3MINI).

5.6.2 JTAG

The JTAG protocol is a Debug/Program protocol used for STM32 microcontrollers. The signals are 3.3 V compatible and may perform up to 21 MHz. This function is available on CN2 (STLINK-V3MODS) and CN5 (STLINK-V3MINI).

5.6.3 Virtual COM port (VCP)

The serial interface VCP is directly available as a Virtual COM port of the PC, connected to STLINK-V3MODS and STLINK-V3MINI USB connector CN5. This function may be used for STM32 microcontrollers. The signals are 3.3 V compatible and may perform from 732 bps to 16 Mbps. This function is available on CN2 (STLINK-V3MODS) and CN5 (STLINK-V3MINI).

A second Virtual COM port may be activated on STLINK-V3MODS, as detailed later in *Section 5.6.5* (Bridge UART).

For details regarding baud rates, refer to *section 14.2*. of the user manual *STLINK-V3SET debugger/programmer for STM8 and STM32* (UM2448).

5.6.4 Mass storage interface

The STLINK-V3MODS and STLINK-V3MINI tiny probes implement a virtual mass storage interface allowing the programming of an STM32 target flash memory with drag-and-drop action of a binary file from a file explorer. This ability requires the STLINK-V3MODS or STLINK-V3MINI tiny probe to identify the connected target before enumerating it on the USB host. As a consequence, this functionality is available only if the target is connected to the STLINK-V3MODS or STLINK-V3MINI tiny probe when it powers up.

The mass storage interface may be disabled or enabled again by reprogramming the ST-LINK firmware. This can be performed with the *STLinkUpgrade* application. Activate the *<change type> checkbox* then select the firmware with or without mass storage and launch the update. The action is reversible.

5.6.5 Bridge functions (STLINK-V3MODS only)

The STLINK-V3MODS tiny probe provides a proprietary USB interface allowing the communication with an STM32 target with several protocols: SPI, I^2C, CAN, UART and GPIOs. This interface may be used to communicate with the target bootloader, but may also be used for customized needs through its public software interface. All bridge signals are accessible on CN2.

Bridge SPI

SPI signals are available on CN2 pins 7, 14, 23, and 25.

Bridge I²C

I²C signals are available on CN2 pins 16 and 17. It is necessary to add external 680 Ω pull-up resistors on the host application board.

Bridge CAN

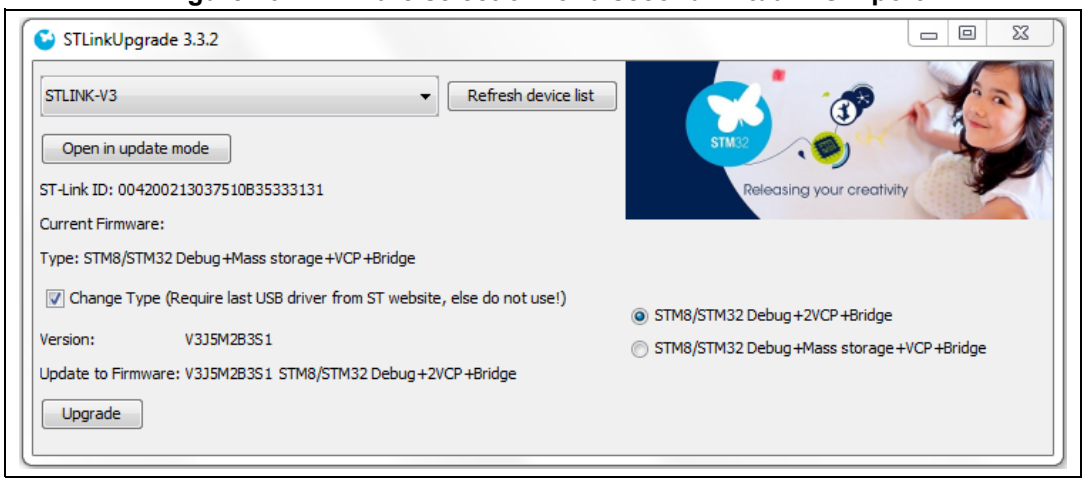
CAN logic signals (Rx-Tx) are available on CN2 pins 9 and 10, they may be used as input for an external CAN transceiver.

Bridge UART

UART signals with hardware flow control (CTS/RTS) are available on MB1467 CN2 pins 1, 2, 3, and 11. These signals require dedicated firmware to be programmed on the main module before use. With this firmware, a second Virtual COM port becomes available and the mass-storage interface (used for drag-and-drop flash memory programming) is disabled. The firmware selection is reversible and is performed using the STLinkUpgrade application, as shown in *Figure 10*.

The hardware flow control can be activated by physically connecting the UART_RTS and UART_CTS signals to the target. If not connected, the second Virtual COM port operates without hardware flow control. Note that the hardware flow control activation/deactivation cannot be configured by software from the host side on a Virtual COM port. Consequently, configuring a parameter related to this on the host application does not affect the system behavior.

Figure 10. Firmware selection for a second Virtual COM port



Bridge GPIOs

Four GPIO signals are available on CN2 pins 18 and 21. Basic management is provided by the public ST bridge software interface.

5.6.6 LEDs

Power LED: The red light indicates that 5 V is enabled.

COM LED: Refer to the technical note *Overview of ST-LINK derivatives* (TN1235) for details.

Fault LED: Indicates USB overcurrent request.

6 Board connectors

6.1 Connectors

Per convention, refer to *Table 2* for I/O type definitions:

Table 2. I/O type definitions

Type	Definition
S	Supply pin
I	Input only pin
O	Output only pin
I/O	Input/Output pin

6.1.1 USB Micro-B

The CN5 USB connector is used to connect the embedded STLINK-V3MODS or STLINK-V3MINI to the PC.

6.1.2 32-pin edge connector for STLINK-V3MODS (STM32 JTAG/SWD, VCP and bridges)

Table 3. 32-pin edge connector for STLINK-V3MODS

Side	Pin #	Pin description	Type
LEFT	1	Bridge UART RX(1)	I
	2	Bridge UART CTS	I
	3	Bridge UART RTS	O
	4	T_JTMS/T_SWDIO	I/O
	5	GNDDetect	O
	6	T_JTDO/T_SWO(2)	I
	7	Bridge SPI CLK	I/O
	8	GND	S
	9	Bridge CAN RX(1)	I
	10	Bridge CAN TX(3)	O
	11	Bridge UART TX(3)	O
	12	T_VCP_TX	I
	13	T_JCLK/T_SWCLK	O
	14	Bridge SPI NSS	I/O
	15	T_VCP_RX	O
	16	Bridge I2C SCL	O

Table 3. 32-pin edge connector for STLINK-V3MODS (continued)

Side	Pin #	Pin description	Type
RIGHT	17	Bridge I2C SDA	I/O
	18	Bridge GPIO0	I/O
	19	Bridge GPIO1	I/O
	20	Bridge GPIO2	I/O
	21	Bridge GPIO3	I/O
	22	5 V(4)	O
	23	Bridge SPI MISO	I/O
	24	GND	S
	25	Bridge SPI MOSI	I/O
	26	GND	S
	27	GND	S
	28	T_JTDI/NC(5)	O
	29	GND	S
	30	T_VCC(6)	I
	31	T_NRST	O
	32	T_SW_DIR	O

1. Rx signals are inputs for STLINK-V3MODS, outputs for the target.
2. SWO is optional, required only for Serial Wire Viewer (SWV) trace.
3. TX signals are outputs for STLINK-V3MODS, inputs for the target.
4. Can drive 5 V +/- 5% with a 200 mA maximum current.
5. NC means not required for SWD connection.
6. Input for STLINK-V3MODS.

5 V source output capability

Pin 22 of the right edge pin connector can drive a 5 V target with a maximum output current of 200 mA. The 5 V is guaranteed to be in the +/- 5% range if the driven current does not exceed 200 mA. Exceeding this maximum output current over 200 mA may damage the device or cause communication issues with the host PC (A PC standard USB connector cannot provide more than 500 mA by the USB port and STLINK-V3MODS can request dynamic current surges during target devices programming).

6.1.3 STDC14 for STLINK-V3MINI (STM32 JTAG/SWD and VCP)

The CN5 STDC14 connector allows the connection to an STM32 target using the JTAG or SWD protocol, respecting (from pin 3 to pin 12) the ARM10 pinout (Arm® Cortex® debug connector). But it also advantageously provides two UART signals for the Virtual COM port. The related pinout for the STDC14 connector is listed in *Table 4*.

Figure 11. CN5 STDC14 connector (Top view)

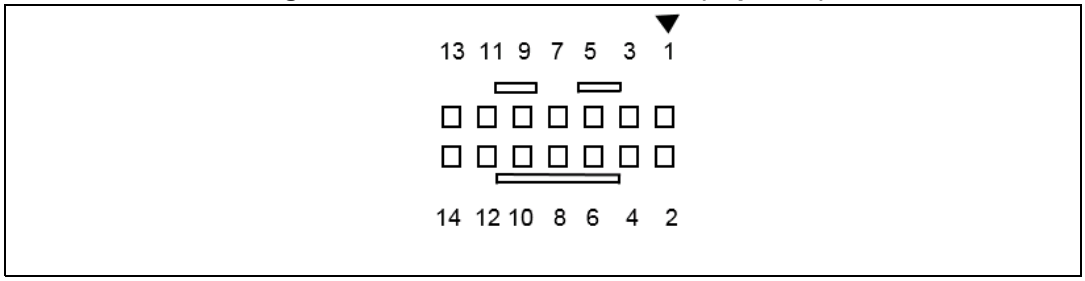


Table 4. CN5 STDC14 connector pinout

STDC14 Pin #	ARM10 Pin #	Pin description	Type
1	-	Reserved(1)	-
2	-	Reserved(1)	-
3	1	T_VCC(2)	I
4	2	T_JTMS/T_SWDIO	I/O
5	3	GND	S
6	4	T_JCLK/T_SWCLK	O
7	5	GND	S
8	6	T_JTDO/T_SWO(3)	I
9	7	T_JCLK	O
10	8	T_JTDI/NC(4)	O
11	9	GNDDetect	O
12	10	T_NRST	O
13	-	T_VCP_RX	O
14	-	T_VCP_TX	I

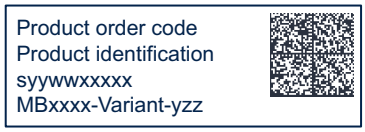
1. Do not connect on target.
2. T_VCC is an input for STLINK-V3MINI.
3. SWO is optional, required only for Serial Wire Viewer (SWV) trace.
4. NC means not required for SWD connection.

7 Product information

7.1 Product marking

The product and each board composing the product are identified with one or several stickers. The stickers, located on the top or bottom side of each PCB, provide product information:

- Main board featuring the target device: product order code, product identification, serial number, and board reference with revision.
  Single-sticker example:

  Dual-sticker example:

- Other boards if any: board reference with revision and serial number.
  Examples:

On the main board sticker, the first line provides the product order code, and the second line the product identification.
On all board stickers, the line formatted as *"MBxxxx-Variant-yzz"* shows the board reference *"MBxxxx"*, the mounting variant *"Variant"* when several exist (optional), the PCB revision *"y"*, and the assembly revision *"zz"*, for example B01. The other line shows the board serial number used for traceability.
Products and parts labeled as *"ES"* or *"E"* are not yet qualified or feature devices that are not yet qualified. STMicroelectronics disclaims any responsibility for consequences arising from their use. Under no circumstances will STMicroelectronics be liable for the customer's use of these engineering samples. Before deciding to use these engineering samples for qualification activities, contact STMicroelectronics' quality department.

"ES" or *"E"* marking examples of location:

- On the targeted STM32 that is soldered on the board (for an illustration of STM32 marking, refer to the STM32 datasheet *Package information* paragraph at the *www.st.com* website).
- Next to the evaluation tool ordering part number that is stuck or silk-screen printed on the board.
- Some boards feature a specific STM32 device version, which allows the operation of any bundled commercial stack/library available. This STM32 device shows a *"U"* marking option at the end of the standard part number and is not available for sales. To use the same commercial stack in their applications, the developers might need to purchase a part number specific to this stack/library. The price of those part numbers includes the stack/library royalties.

7.2 Product history

Table 5. Product history

Order code	Product identification	Product details	Product change description	Product limitations
STLINK-V3MODS	LKV3MODS$AT1	Board: – MB1467-V3MODS-B01 (Tiny probe board)	Initial revision	No limitation
	LKV3MODS$AT2	Board: – MB1467-V3MODS-B02 (Tiny probe board)	Tiny probe board revision changed	No limitation
	LKV3MODS$RT1	Board: – MB1467-V3MODS-B03 (Tiny probe board)	– Tiny probe board revision changed – Board sticker format changed	No limitation
STLINK-V3MINI	LKV3MINI$AT1	Board: – MB1467-V3MINI-B01 (Tiny probe board)	Initial revision	No limitation
	LKV3MINI$AT2	Board: – MB1467-V3MINI-B02 (Tiny probe board)	Tiny probe board revision changed	No limitation

7.3 Board revision history

Table 6. Board revision history

Board reference	Board variant and revision	Board change description	Board limitations
MB1467 (Tiny probe board)	V3MINI-B01 V3MODS-B01	Initial revision	No limitation
	V3MINI-B02 V3MODS-B02	R3 is fitted on the board	No limitation
	V3MODS-B03	Several part references updated due to obsolescence	No limitation

8 Safety certification information

The product must be powered by a power supply unit or auxiliary equipment complying with standard EN 62368-1:2014+A1:2017 or the standard replacing it, and must be safety extralow voltage (SELV) with limited power capability.

9 Federal Communications Commission (FCC) and Innovation, Science and Economic Development Canada (ISED) Compliance Statements

9.1 FCC Compliance Statement

Part 15.19

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Part 15.21

Any changes or modifications to this equipment not expressly approved by STMicroelectronics may cause harmful interference and void the user's authority to operate this equipment.

Part 15.105

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception which can be determined by turning the equipment off and on, the user is encouraged to try to correct interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

Note: Use a shielded USB cable with a length of less than 0.5 m and ferrite on the computer side, like WURTH ELEKTRONIK 742 711 12.

Responsible party (in the USA)

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9.2 ISED Compliance Statement

ISED Canada ICES-003 Compliance Label: CAN ICES-3 (B) / NMB-3 (B).

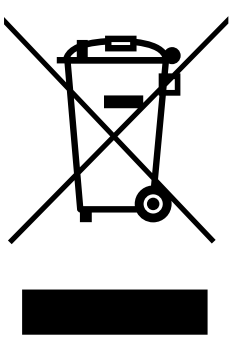
Étiquette de conformité à la NMB-003 d'ISDE Canada : CAN ICES-3 (B) / NMB-3 (B).

Note: Use a shielded USB cable with a length of less than 0.5 m and ferrite on the computer side, like WURTH ELEKTRONIK 742 711 12.

10 Product disposal

Disposal of this product: WEEE (Waste Electrical and Electronic Equipment)

(Applicable in Europe)



This symbol on the product, accessories, or accompanying documents indicates that the product and its electronic accessories should not be disposed of with household waste at the end of their working life.

To prevent possible harm to the environment and human health from uncontrolled waste disposal, please separate these items from other type of waste and recycle them responsibly to the designated collection point to promote the sustainable reuse of material resources.

Household users:

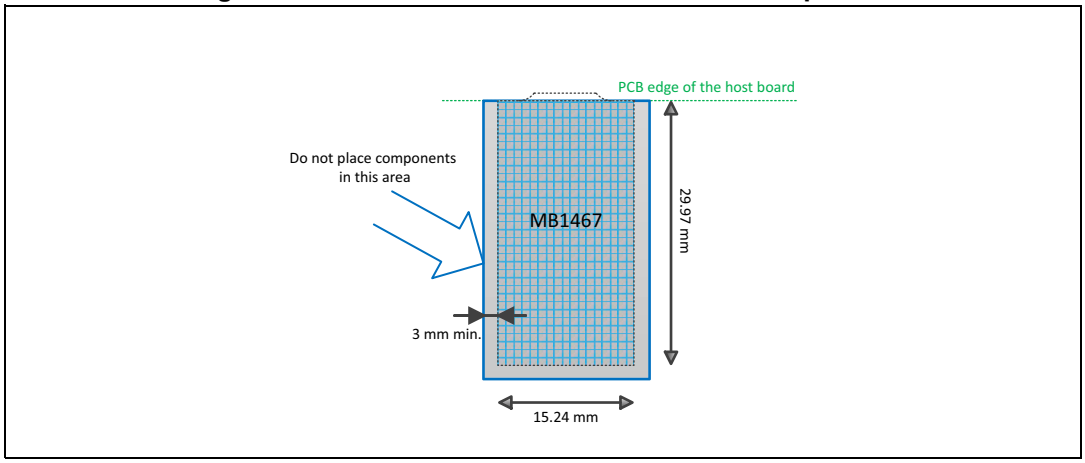
You should contact either the retailer where you buy the product or your local authority for further details of your nearest designated collection point.

Business users:

You should contact your dealer or supplier for further information.

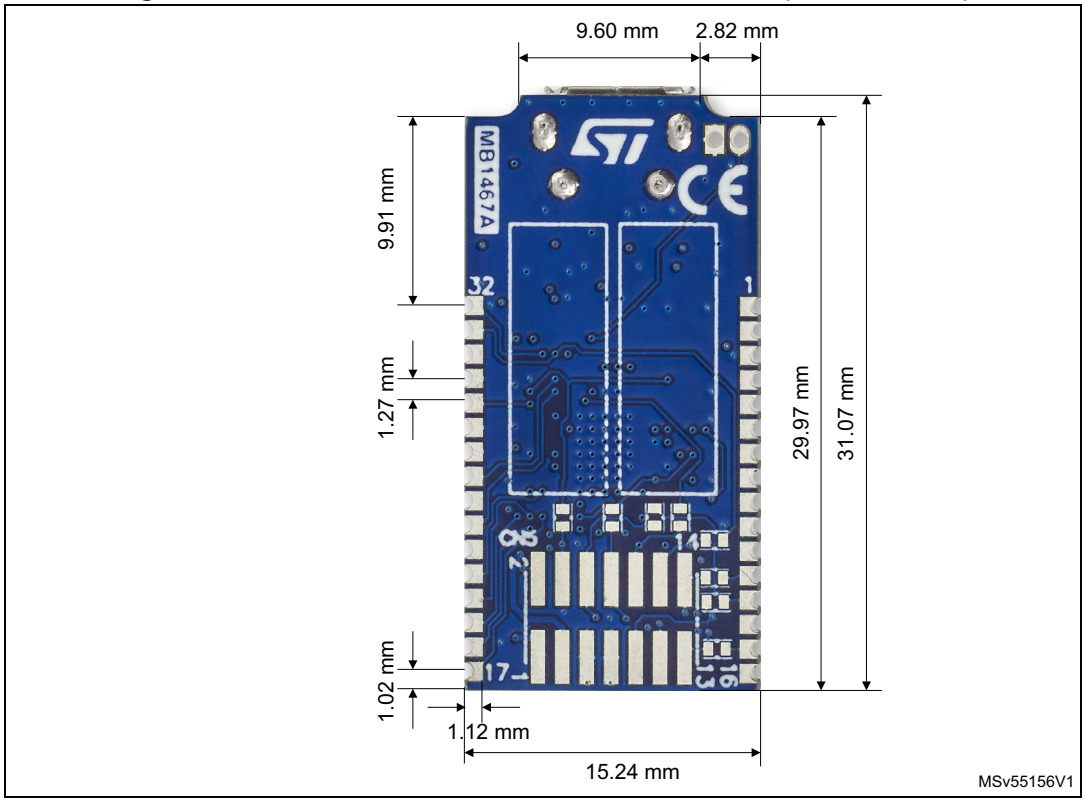
Appendix A STLINK-V3MODS recommended land pattern

Figure 12. STLINK-V3MODS recommended land pattern



Appendix B STLINK-V3MODS board dimensions

Figure 13. STLINK-V3MODS mechanical dimensions (in millimeters)



Appendix C STLINK-V3MODS reference design for voltage adapter

A document describing how to connect a voltage adapter to the STLINK-V3MODS is available on the www.st.com/stlink-v3mods website under the CAD resources tab. This document is given as a guidance and reference design helping customers how to realize a 1.65 to 3.60 V voltage adapter to the STLINK-V3MODS compact in-circuit debugger and programmer for STM32.

Revision history

Table 7. Document revision history

Date	Revision	Changes
11-Apr-2019	1	Initial release.
14-Dec-2021	2	Reshuffle of the document to align with latest standards: – *Introduction* to *Quick start* reordering – Former *Software configuration* subsections spread between *Development environment* and *STLINK-V3MODS and STLINK-V3MINI functional description* Added: – *5 V source output capability* subsection – *STLINK-V3MODS board dimensions* with new *Figure 13* – *STLINK-V3MODS reference design for voltage adapter* Updated: – Title, *Features*, *Figure 7*, *Figure 8*, *Table 3* and *Table 4* – *Virtual COM port (VCP)* and *Bridge functions (STLINK-V3MODS only)* – *Product information* and *Federal Communications Commission (FCC) and Innovation, Science and Economic Development Canada (ISED) Compliance Statements*
16-Jun-2025	3	Updated: – *Introduction* – *Bridge UART* – *Section 7.1: Product marking* – *Section 7.2: Product history* – *Section 7.3: Board revision history* – *Section 9.1: FCC Compliance Statement* – *Section 9.2: ISED Compliance Statement* Added: – *Section 5.2: STLINK-V3MODS power scheme* – *Section 8: Safety certification information* – *Section 10: Product disposal*

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