

Reference device marking schematics for STM32 microcontrollers and microprocessors

Introduction

This document provides the most probable⁽¹⁾ device marking example for every package code⁽²⁾ encountered in STM32 general-purpose microcontrollers or microprocessors.

The package code is a unique package identifier that can be found:

- in this document, between parenthesis, at the end of each section title
- in this document, as a fragment of the index located at each figure bottom-right corner
- in the product datasheet, between parenthesis, at the end of each section title, or as a fragment of the index located at the bottom-right corner of package outline figures.
- (1): Some slight package marking variations may be encountered between different products using the same package, and even between two identical products (in case of multiple assembly plants for instance). Nevertheless, the pin 1 / ball A1 localisation instructions provided in this document apply invariably.
- (2): Except wafer level chip scale packages (WLCSP) that remain described in the product datasheet.



1 General information

This document applies to STM32 Arm®-based general-purpose microcontrollers or microprocessors.

Note: Arm is a registered trademark of Arm Limited (or its subsidiaries) in the US and/or elsewhere.

arm

TN1433 - Rev 7 page 2/84



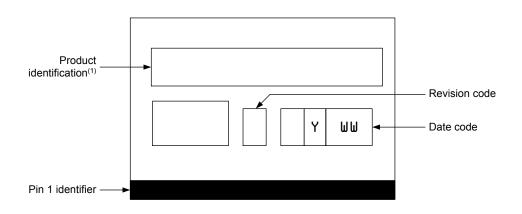
2 Device markings for SO packages

2.1 Device marking for SO8N 4.9 × 6 mm (O7)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 1. SO8N marking example (package top view)



07_S08N_DM_V5

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

TN1433 - Rev 7 page 3/84



3 Device markings for TSSOP packages

3.1 Device marking for TSSOP14 5 × 4.4 mm (6R)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

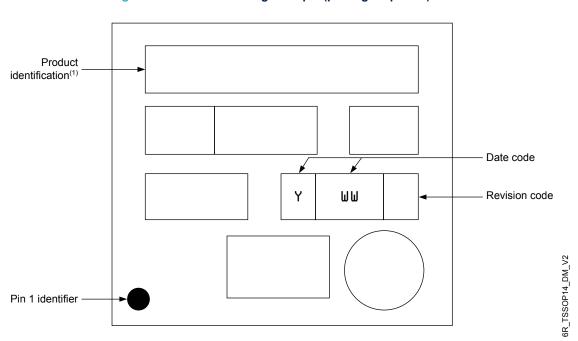


Figure 2. TSSOP14 marking example (package top view)

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

3.2 Device marking for TSSOP20 6.5 × 4.4 mm (YA)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 4/84



Product identification⁽¹⁾

Pin 1 identifier

Y WW Revision code

Date code

Figure 3. TSSOP20 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

TN1433 - Rev 7 page 5/84

5V_LQFP32_DM_V3



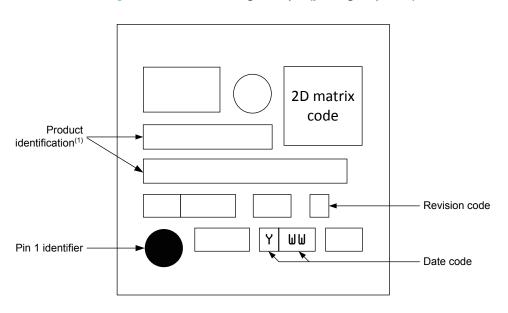
4 Device markings for LQFP packages

4.1 Device marking for LQFP32 7 × 7 mm (5V)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 4. LQFP32 marking example (package top view)



1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

4.2 Device marking for LQFP44 10 × 10 mm (4Y)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 6/84

4Y_LQFP44_DM_V3

Product identification (1)

2D matrix code

Revision code

Pin 1 identifier

Figure 5. LQFP44 marking example (package top view)

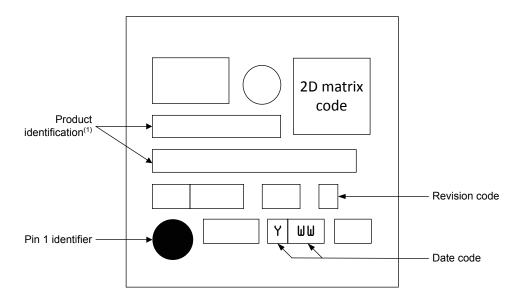
 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

4.3 Device marking for LQFP48 7 × 7 mm (5B)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 6. LQFP48 marking example (package top view)



5B_LQFP48_DM_V3

TN1433 - Rev 7

5W_LQFP64_DM_V3



 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

4.4 Device marking for LQFP64 10 × 10 mm (5W)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

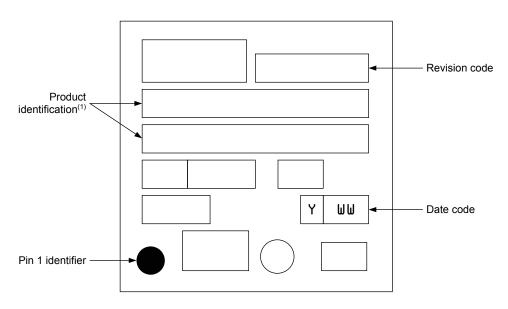


Figure 7. LQFP64 marking example (package top view)

Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified
and therefore not approved for use in production. ST is not responsible for any consequences resulting from
such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's
Quality department must be contacted prior to any decision to use these engineering samples to run a
qualification activity.

4.5 Device marking for LQFP64 14 × 14 mm (1R)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 8/84

IR_LQFP64_DM_V3

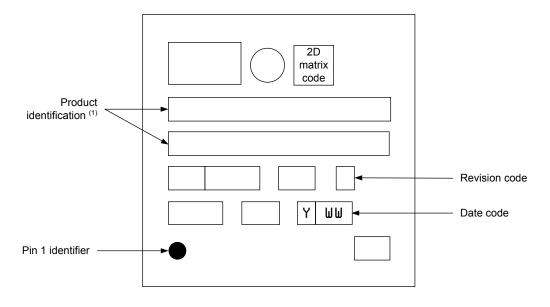


Figure 8. LQFP64 marking example (package top view)

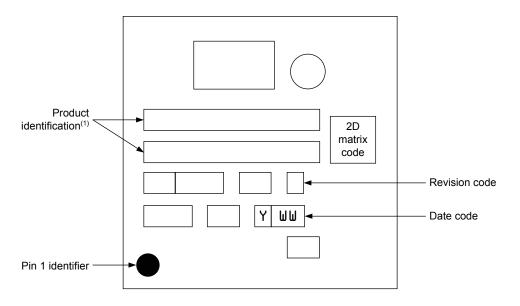
 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

4.6 Device marking for LQFP80 12 × 12 mm (9X)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 9. LQFP80 marking example (package top view)



9X_LQFP80_DM_V3

TN1433 - Rev 7

203F_LQFP80_DM_V1



 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

4.7 Device marking for LQFP80 12 × 12 mm (C03F)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

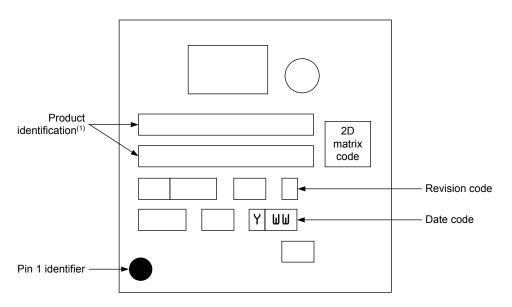


Figure 10. LQFP80 marking example (package top view)

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

4.8 Device marking for LQFP80 14 × 14 mm (1S)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 10/84

IS_LQFP80_DM_V2

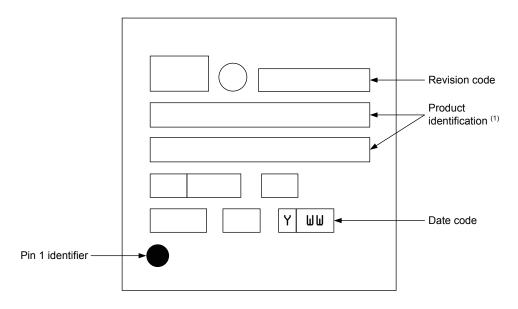


Figure 11. LQFP80 marking example (package top view)

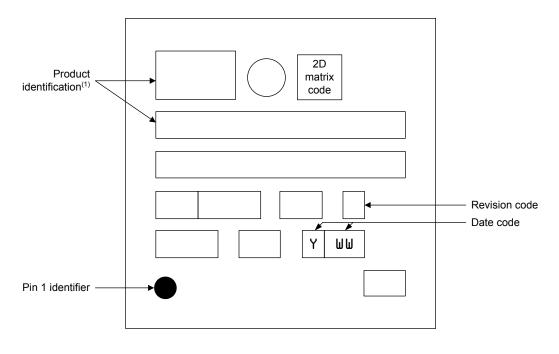
 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

4.9 Device marking for LQFP100 14 × 14 mm (1L)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 12. LQFP100 marking example (package top view)



1L_LQFP100_DM_V3

OS LQFP100 DM V1



 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

4.10 Device marking for LQFP100 14 × 14 mm (OS)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

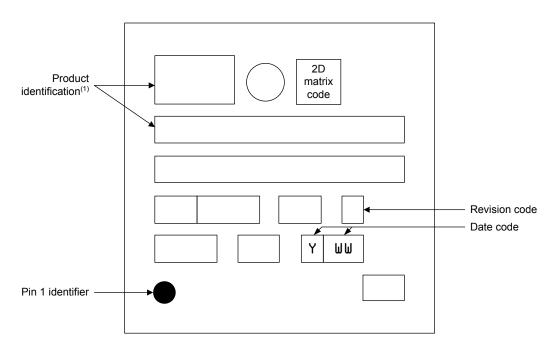


Figure 13. LQFP100 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

4.11 Device marking for LQFP128 14 × 14 mm (TC)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 12/84

TC_LQFP128_DM_V5

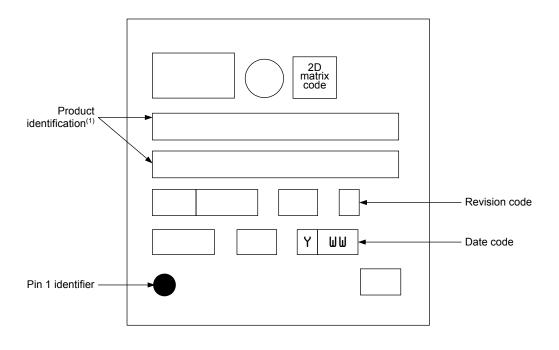


Figure 14. LQFP128 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

4.12 Device marking for LQFP128 14 × 14 mm (MT)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 13/84

MT_LQFP128_DM_V1

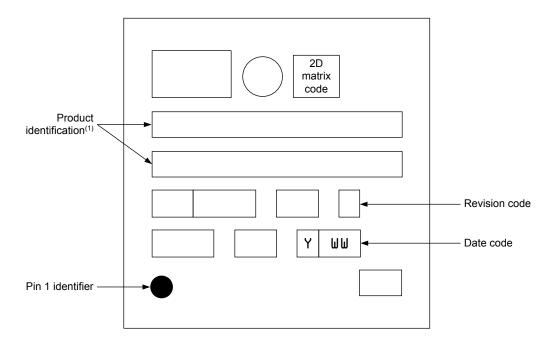


Figure 15. LQFP128 marking example (package top view)

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

4.13 Device marking for LQFP144 20 × 20 mm (1A)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 14/84

1A_LQFP144_DM_V2

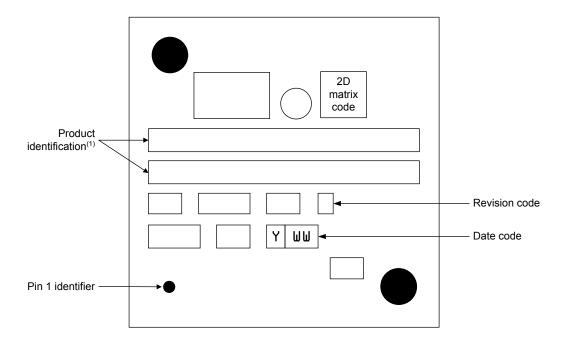


Figure 16. LQFP144 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

4.14 Device marking for LQFP176 24 × 24 mm (1T)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 15/84

1T_LQFP176_DM_V2

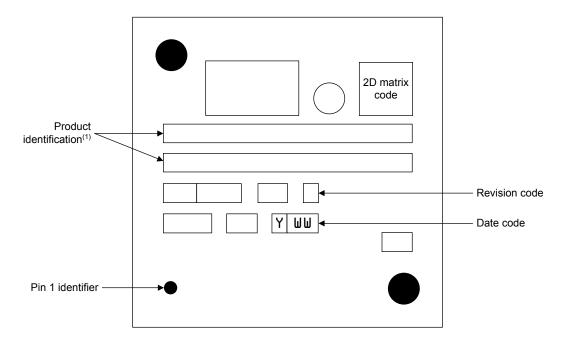


Figure 17. LQFP176 marking example (package top view)

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

4.15 Device marking for LQFP208 28 × 28 mm (UH)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 16/84

UH_LQFP208_DM_V3



Product identification⁽¹⁾
Revision code
Pin 1 identifier

Figure 18. LQFP208 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

TN1433 - Rev 7 page 17/84

ES_TQFP48_DM_V1



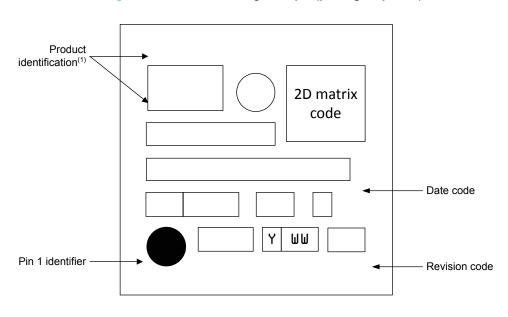
5 Device markings for TQFP packages

5.1 Device marking for TQFP48 7 × 7 mm (ES)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 19. TQFP48 marking example (package top view)



1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

5.2 Device marking for TQFP64 10 × 10 mm (9I)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 18/84

91_TQFP64_DM_V1

Product identification⁽¹⁾

2D matrix code

Revision code

Pin 1 identifier

Figure 20. TQFP64 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

TN1433 - Rev 7 page 19/84



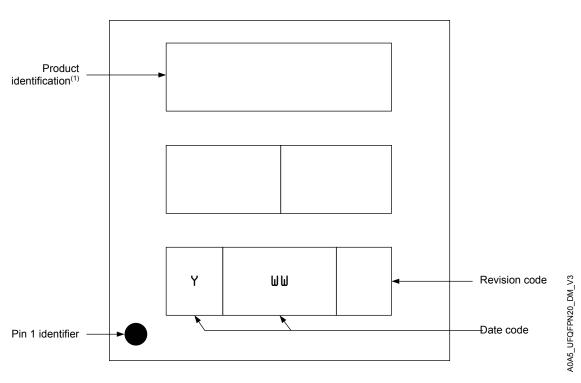
6 Device markings for UFQFPN packages

6.1 Device marking for UFQFPN20 3 × 3 mm (A0A5)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 21. UFQFPN20 marking example (package top view)



 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

6.2 Device marking for UFQFPN24 4 × 4 mm (998Z)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 20/84

D806_UFQFPN24_DM_V1



Product identification⁽¹⁾

Date code

Pin 1 identifier

Figure 22. UFQFPN24 marking example (package top view)

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

6.3 Device marking for UFQFPN28 4 × 4 mm (A0B0)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 21/84



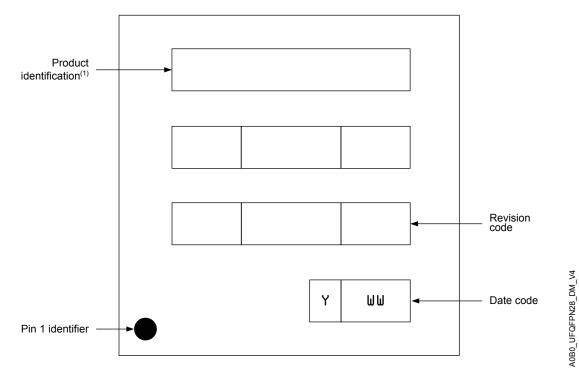


Figure 23. UFQFPN28 marking example (package top view)

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

6.4 Device marking for UFQFPN32 5 × 5 mm (A09E)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 22/84



Product identification⁽¹⁾

Pin 1 identifier

Product

Y

W

Revision code

Pin 1 identifier

Figure 24. UFQFPN32 marking example (package top view)

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

6.5 Device marking for UFQFPN32 5 × 5 mm (A0B8)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 23/84

A0B8_UFQFPN32_DM_V2



Product identification⁽¹⁾

Date code
Revision code

Pin 1 identifier

Figure 25. UFQFPN32 marking example (package top view)

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

6.6 Device marking for UFQFPN48 6 × 6 mm (A0F2)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 24/84

A0F2_UFQFPN48_DM_V1

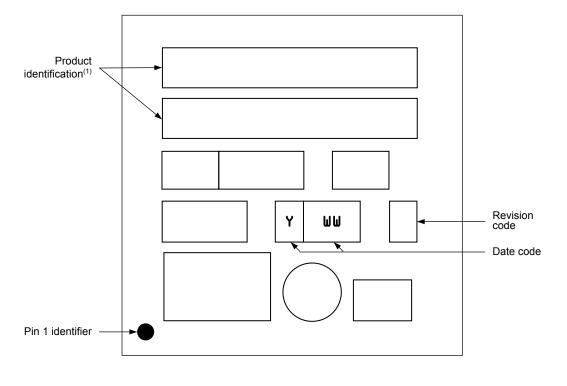


Figure 26. UFQFPN48 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

6.7 Device marking for UFQFPN48 7 × 7 mm (A0B9)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 25/84

A0B9_UFQFPN48_DM_V4



Product identification⁽¹⁾

Y

W

Pate code

Revision code

Figure 27. UFQFPN48 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

TN1433 - Rev 7 page 26/84



Device markings for VFQFPN packages

7.1 Device marking for VFQFPN32 5 × 5 mm (42)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

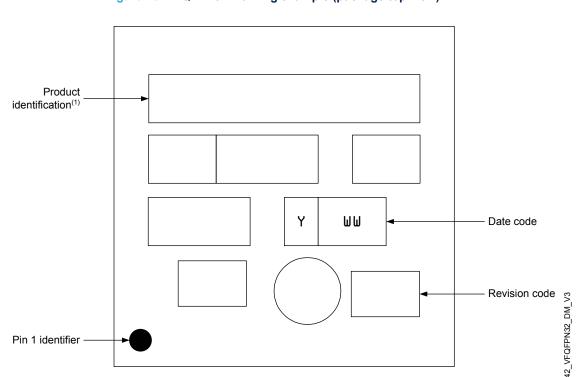


Figure 28. VFQFPN32 marking example (package top view)

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

7.2 Device marking for VFQFPN36 6 × 6 mm (ZR)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 27/84



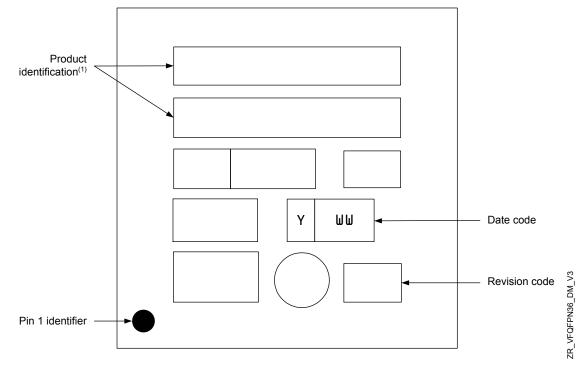


Figure 29. VFQFPN36 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

7.3 Device marking for VFQFPN48 6 × 6 mm (A0BE)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 28/84



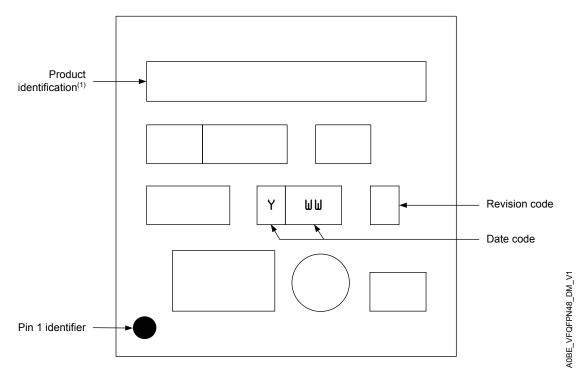


Figure 30. VFQFPN48 marking example (package top view)

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

7.4 Device marking for VFQFPN68 8 × 8 mm (B029)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 29/84



Product identification⁽¹⁾

Revision code

Y

U

Date code

Pin 1 identifier

Figure 31. VFQFPN68 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

TN1433 - Rev 7 page 30/84

AL_LFBGA36_DM_V2



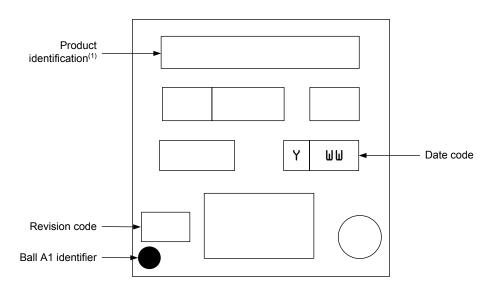
8 Device markings for LFBGA packages

8.1 Device marking for LFBGA36 6 × 6 mm (AL)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 32. LFBGA36 marking example (package top view)



 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8.2 Device marking for LFBGA100 10 × 10 mm (H0)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 31/84

HO_LFBGA100_DM_V3

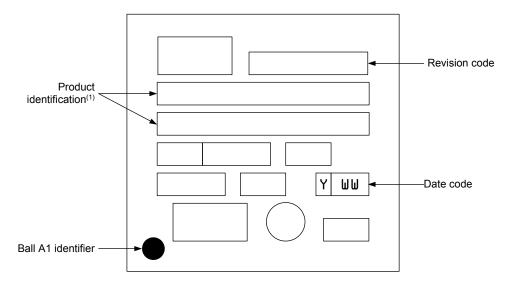


Figure 33. LFBGA100 marking example (package top view)

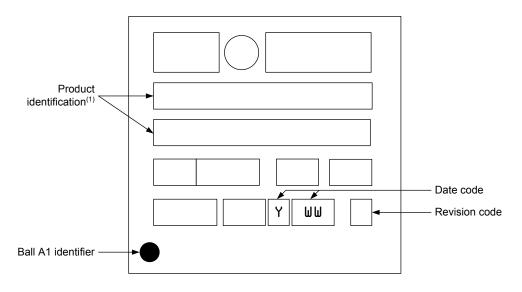
1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8.3 Device marking for LFBGA144 10 × 10 mm (X3)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 34. LFBGA144 marking example (package top view)



X3_LFBGA144_DM_V3

TN1433 - Rev 7



1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8.4 Device marking for LFBGA289 14 × 14 mm (B0ED)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

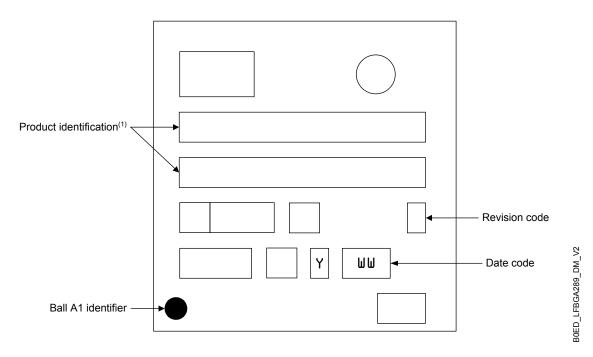


Figure 35. LFBGA289 marking example (package top view)

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8.5 Device marking for LFBGA354 16 × 16 mm (B02Z)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 33/84



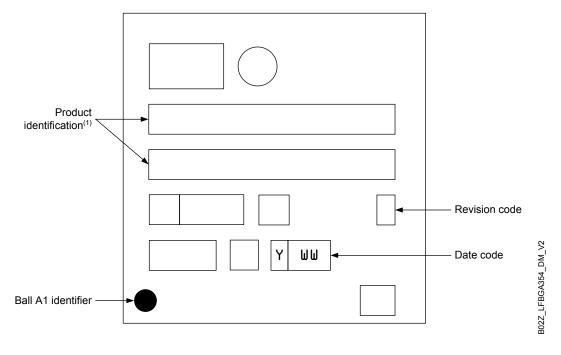


Figure 36. LFBGA354 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8.6 Device marking for LFBGA448 18 × 18 mm (B032)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 34/84



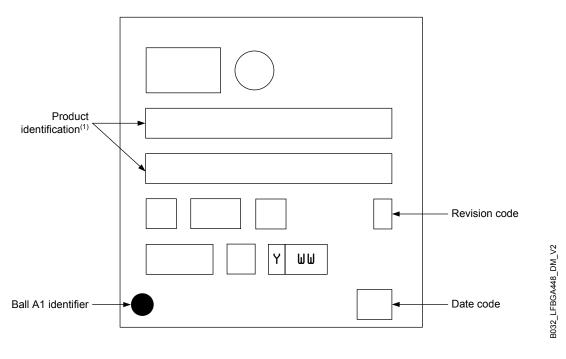


Figure 37. LFBGA448 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

TN1433 - Rev 7 page 35/84



9 Device markings for TFBGA packages

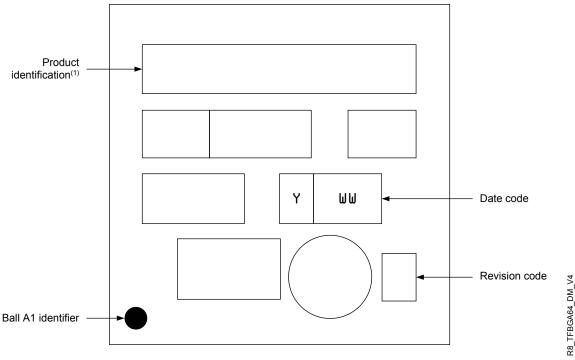
9.1 Device marking for TFBGA64 5 × 5 mm (R8)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

Figure 38. TFBGA64 marking example (package top view)

With the device text markings oriented as shown below, ball A1 is always located at bottom left.





1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

9.2 Device marking for TFBGA100 8 × 8 mm (A08Q)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 36/84



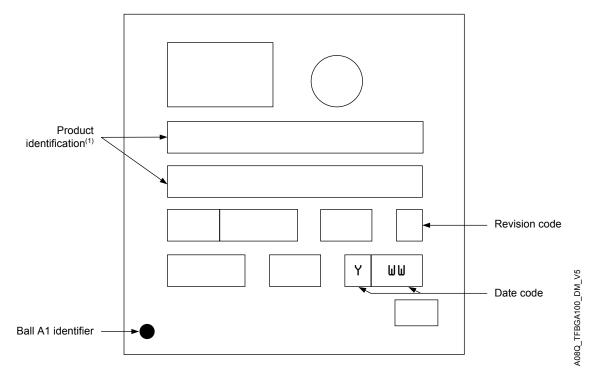


Figure 39. TFBGA100 marking example (package top view)

9.3 Device marking for TFBGA169 7 × 7 mm (B0MA)

The following figure gives an example of the locations and orientation of the marking areas versus ball 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball 1 is always located at bottom left.

TN1433 - Rev 7 page 37/84



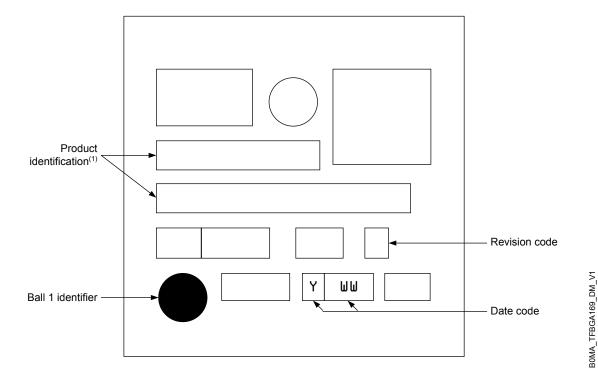


Figure 40. TFBGA169 marking example (package top view)

9.4 Device marking for TFBGA216 13 × 13 mm (A0L2)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 38/84



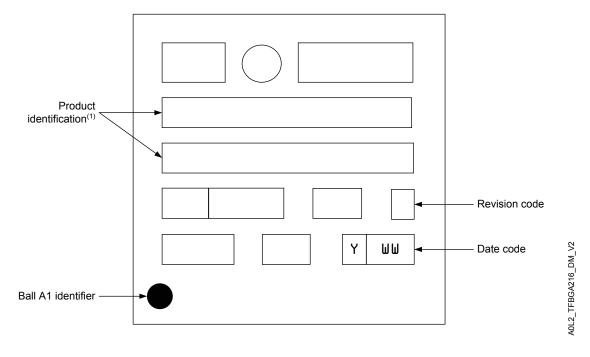


Figure 41. TFBGA216 marking example (package top view)

9.5 Device marking for TFBGA225 13 × 13 mm (B04V)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 39/84



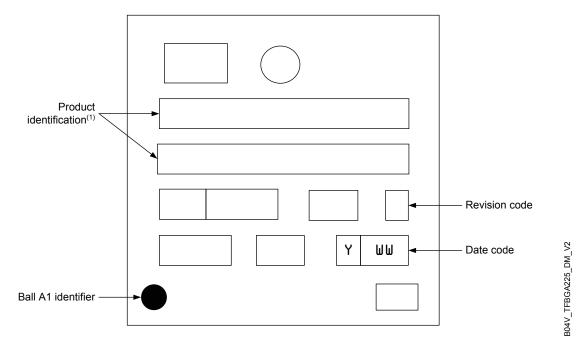


Figure 42. TFBGA225 marking example (package top view)

9.6 Device marking for TFBGA257 10 × 10 mm (B02Y)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 40/84



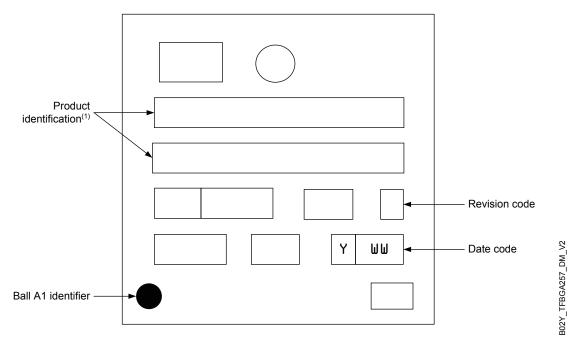


Figure 43. TFBGA257 marking example (package top view)

9.7 Device marking for TFBGA240+25 14 × 14 mm (A07U)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 41/84



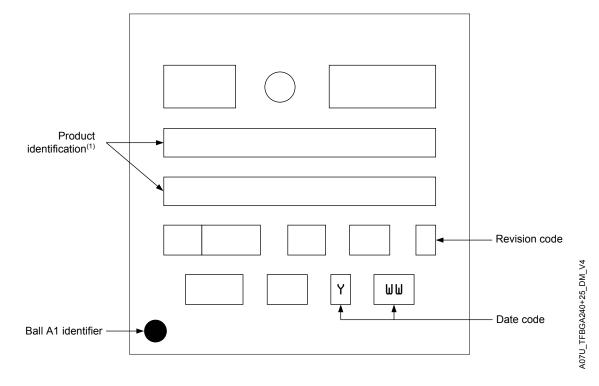


Figure 44. TFBGA240+25 marking example (package top view)

9.8 Device marking for TFBGA289 9 × 9 mm (B0EB)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 42/84



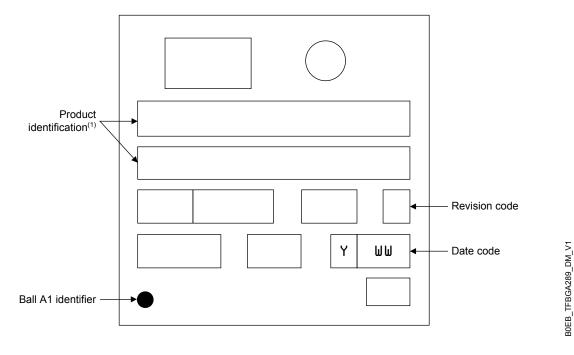


Figure 45. TFBGA289 marking example (package top view)

9.9 Device marking for TFBGA289 14 × 14 mm (B0NY)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 43/84

BONY_TFBGA289_DM_V1



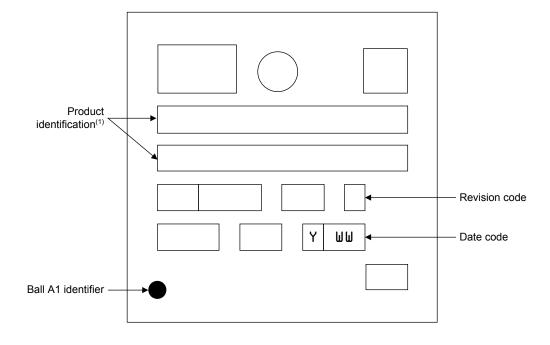


Figure 46. TFBGA289 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

9.10 Device marking for TFBGA320 11 × 11 mm (B0EC)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 44/84



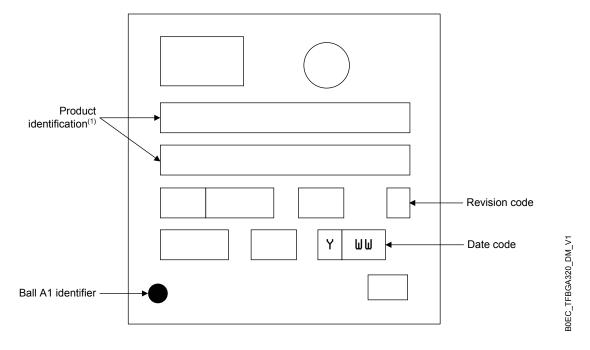


Figure 47. TFBGA320 marking example (package top view)

9.11 Device marking for TFBGA361 12 × 12 mm (B031)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 45/84

B031_TFBGA361_DM_V1



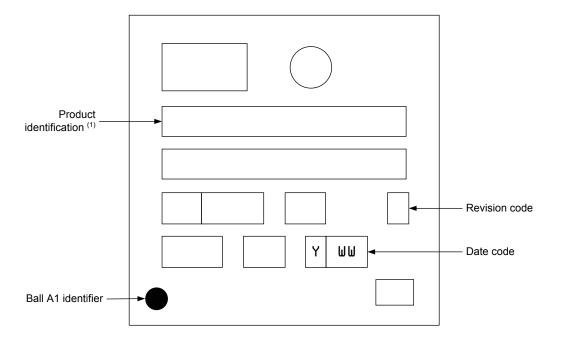


Figure 48. TFBGA361 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

9.12 Device marking for TFBGA361 16 × 16 mm (B0N8)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 46/84

BON8_TFBGA361_DM_V1



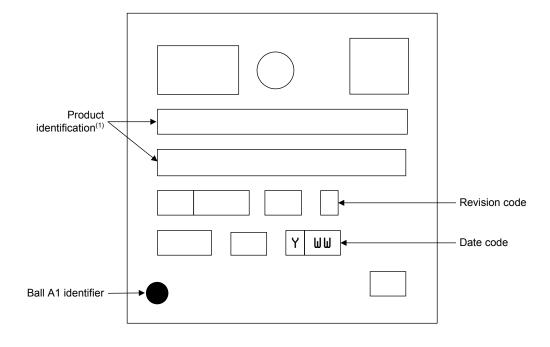


Figure 49. TFBGA361 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

9.13 Device marking for TFBGA436 18 × 18 mm (B0D1)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 47/84

B0D1_TFBGA436_DM_V2



Product identification (1)

Revision code

Y WW

Date code

Figure 50. TFBGA436 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

TN1433 - Rev 7 page 48/84



10 Device markings for UFBGA packages

10.1 Device marking for UFBGA59 5 × 5 mm (B0FS)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

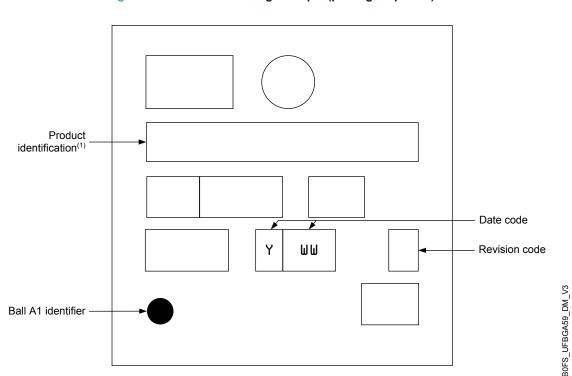


Figure 51. UFBGA59 marking example (package top view)

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

10.2 Device marking for UFBGA64 5 × 5 mm (A019)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 49/84



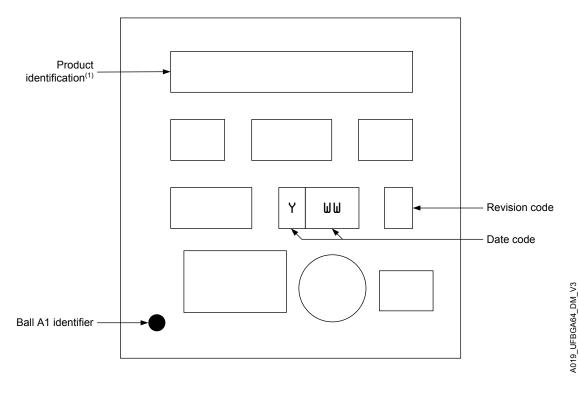


Figure 52. UFBGA64 marking example (package top view)

10.3 Device marking for UFBGA73 5 × 5 mm (B08E)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 50/84



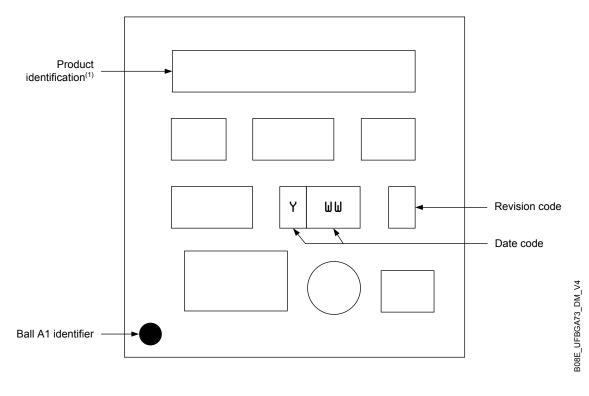


Figure 53. UFBGA73 marking example (package top view)

10.4 Device marking for UFBGA81 5 × 5 mm (B0B8)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 51/84



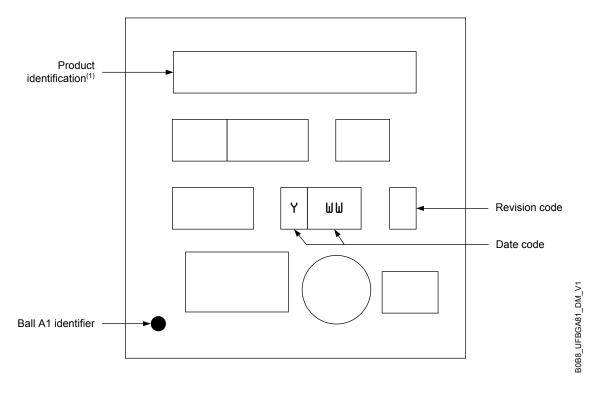


Figure 54. UFBGA81 marking example (package top view)

10.5 Device marking for UFBGA100 7 × 7 mm (A0C2)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 52/84

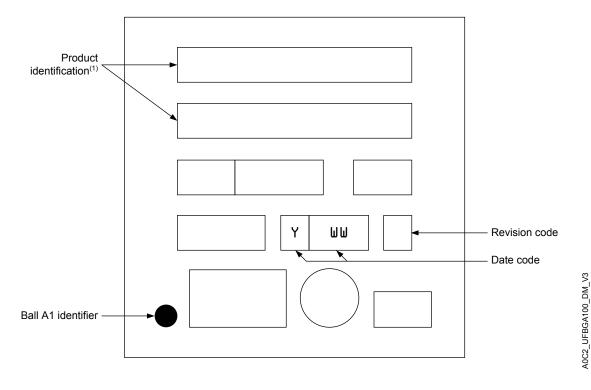


Figure 55. UFBGA100 marking example (package top view)

10.6 Device marking for UFBGA121 6 × 6 mm (B0CU)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 53/84



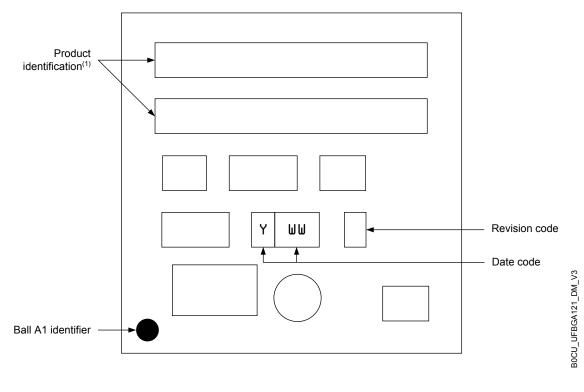


Figure 56. UFBGA121 marking example (package top view)

10.7 Device marking for UFBGA129 7 × 7 mm (B09R)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 54/84



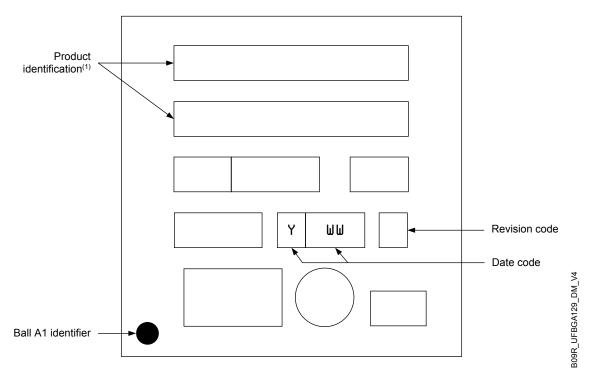


Figure 57. UFBGA129 marking example (package top view)

10.8 Device marking for UFBGA132 7 × 7 mm (A0G8)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 55/84



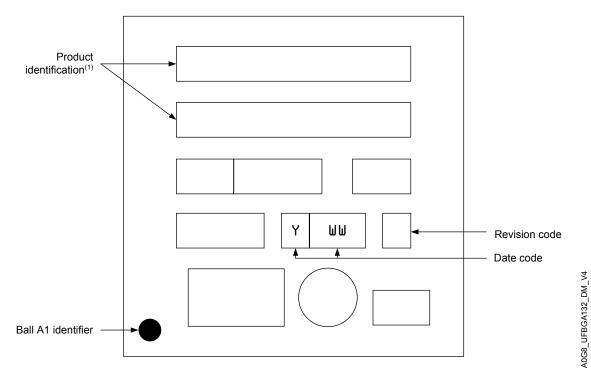


Figure 58. UFBGA132 marking example (package top view)

10.9 Device marking for UFBGA144 7 × 7 mm (A0AS)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 56/84



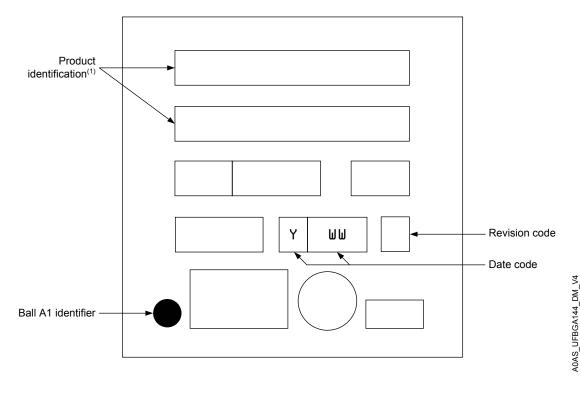


Figure 59. UFBGA144 marking example (package top view)

10.10 Device marking for UFBGA144 10 × 10 mm (A02Y)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 57/84



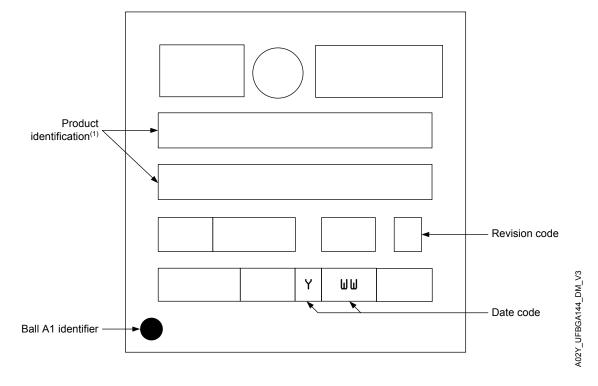


Figure 60. UFBGA144 marking example (package top view)

10.11 Device marking for UFBGA169 7 × 7 mm (A0YV)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at top left.

TN1433 - Rev 7 page 58/84

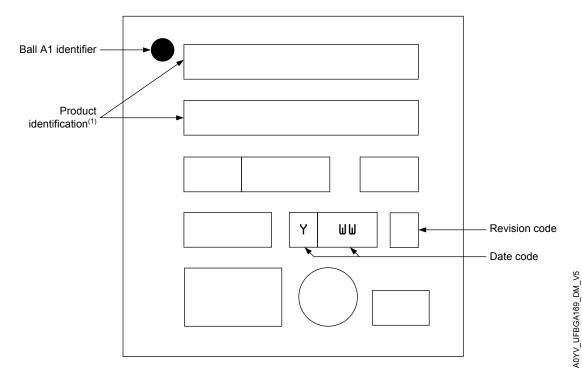


Figure 61. UFBGA169 marking example (package top view)

10.12 Device marking for UFBGA176+25 10 × 10 mm (A0E7)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 59/84

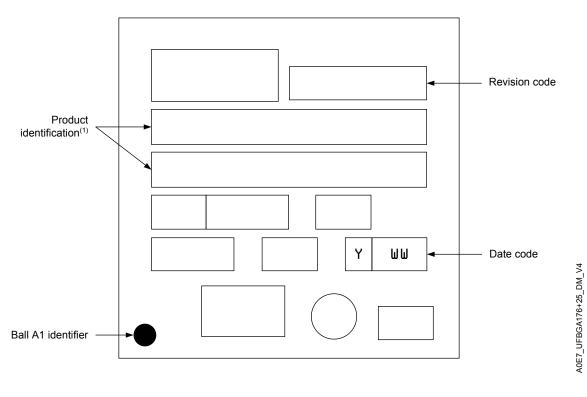


Figure 62. UFBGA176+25 marking example (package top view)

TN1433 - Rev 7 page 60/84

B0GM_VFBGA142_DM_V1



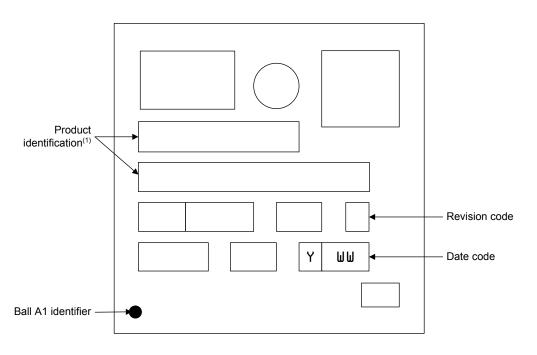
11 Device markings for VFBGA packages

11.1 Device marking for VFBGA142 8 × 8 mm (B0GM)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.





1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

11.2 Device marking for VFBGA169 6 × 6 mm (B0LA)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 61/84

BOLA_VFBGA169_DM_V1

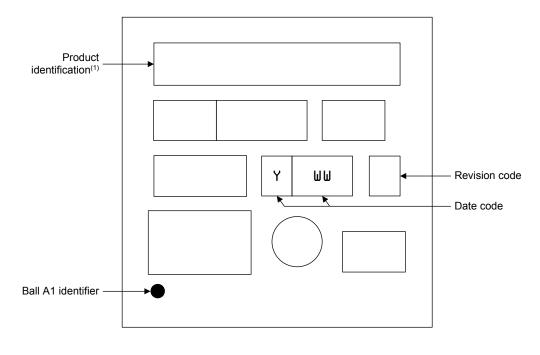


Figure 64. VFBGA169 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

11.3 Device marking for VFBGA178 12 × 12 mm (B0GL)

The following figure gives an example of the locations and orientation of the marking areas versus ball 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball 1 is always located at bottom left.

TN1433 - Rev 7 page 62/84

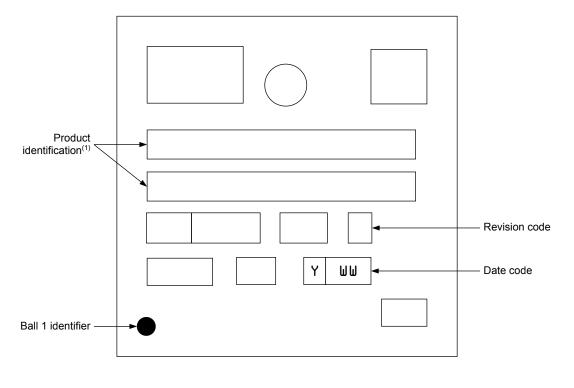


Figure 65. VFBGA178 marking example (package top view)

11.4 Device marking for VFBGA198 10 × 10 mm (B0GJ)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 63/84

BOGJ_VFBGA198_DM_V1

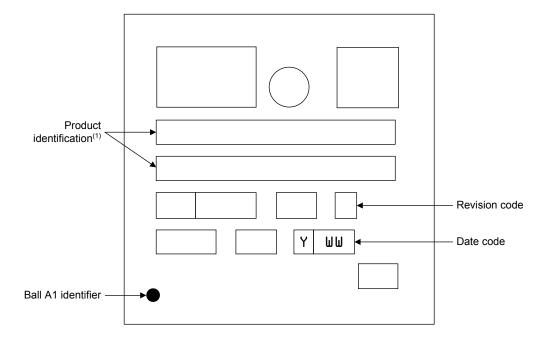


Figure 66. VFBGA198 marking example (package top view)

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

11.5 Device marking for VFBGA223 10 × 10 mm (B0GK)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 64/84



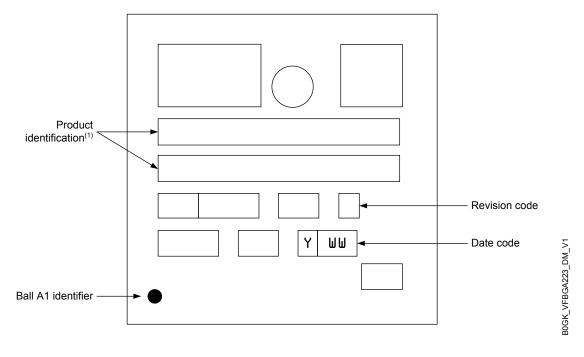


Figure 67. VFBGA223 marking example (package top view)

11.6 Device marking for VFBGA225 8 × 8 mm (B0P6)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 65/84



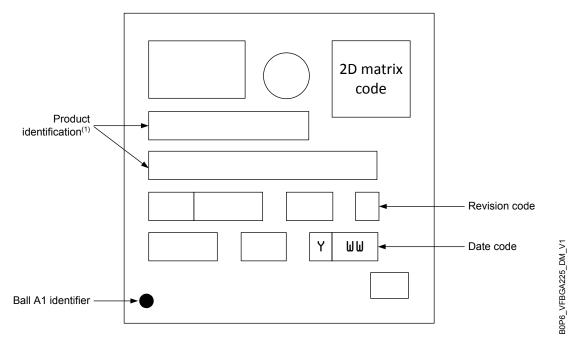


Figure 68. VFBGA225 marking example (package top view)

11.7 Device marking for VFBGA264 14 × 14 mm (B0GH)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 66/84



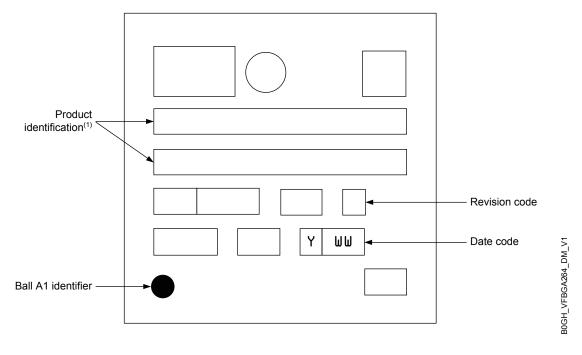


Figure 69. VFBGA264 marking example (package top view)

11.8 Device marking for VFBGA273 11 × 11 mm (B0P7)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 67/84

B0P7_VFBGA273_DM_V2



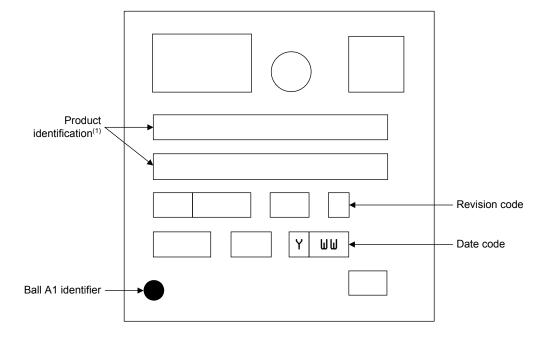


Figure 70. VFBGA273 marking example (package top view)

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

11.9 Device marking for VFBGA361 10 × 10 mm (B09U)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 68/84



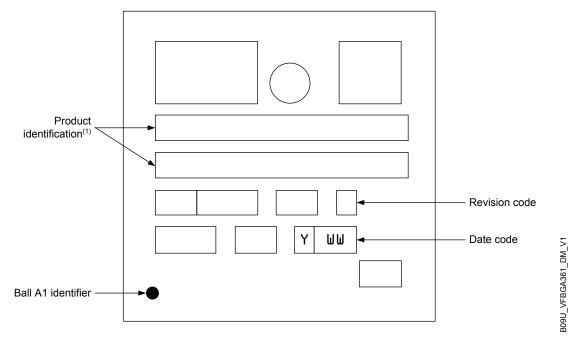


Figure 71. VFBGA361 marking example (package top view)

11.10 Device marking for VFBGA424 14 × 14 mm (B0MP)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 69/84



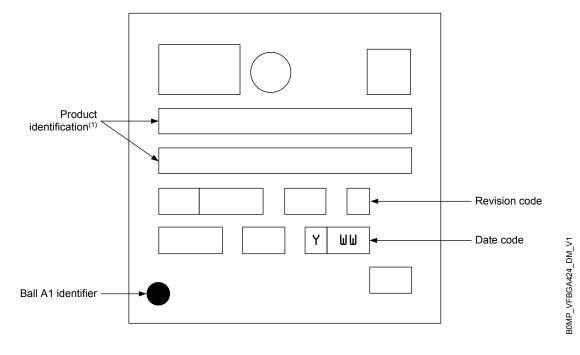


Figure 72. VFBGA424 marking example (package top view)

11.11 Device marking for VFBGA436 18 × 18 mm (B0MS)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

TN1433 - Rev 7 page 70/84



Product identification⁽¹⁾

Revision code

Pall A1 identifier

Figure 73. VFBGA436 marking example (package top view)

TN1433 - Rev 7 page 71/84



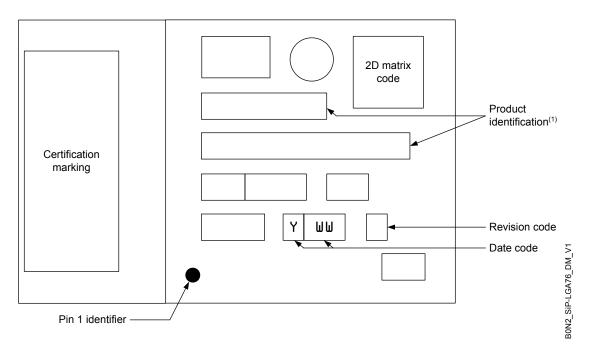
12 Device markings for SiP-LGA packages

12.1 Device marking for SiP-LGA76 8 × 10.5 mm (B0N2)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 74. SiP-LGA76 marking example (package top view)



 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

12.2 Device marking for SiP-LGA77 6.5 × 10 mm (B0HQ)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 72/84



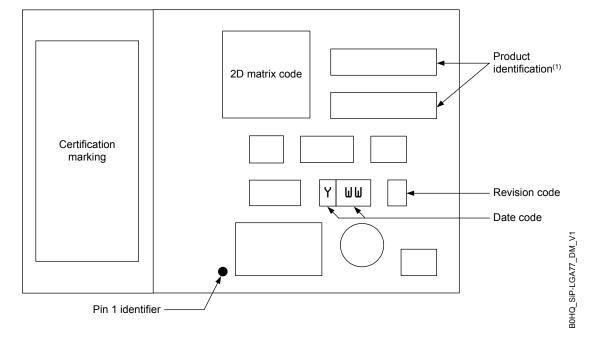


Figure 75. SiP-LGA77 marking example (package top view)

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

12.3 Device marking for SiP-LGA86 7.3 × 11 mm (B0CQ)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 73/84

BOCQ_SIP-LGA86_DM_V1



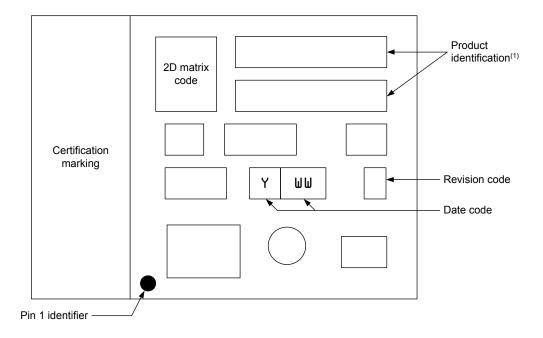


Figure 76. SiP-LGA86 marking example (package top view)

 Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

12.4 Device marking for SiP-LGA92 10 × 10 mm (B0HB)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 74/84

BOHB_SIP-LGA92_DM_V1



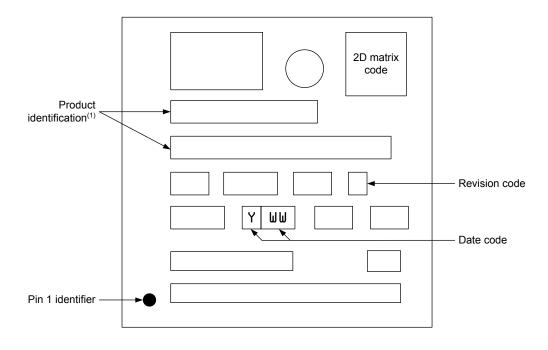


Figure 77. SiP-LGA92 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

12.5 Device marking for SiP-LGA141 11.1 × 13.4 mm (B0TS)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

TN1433 - Rev 7 page 75/84

Certification marking

Product identification(1)

Revision code

Date code

Pin 1 identifier

Figure 78. SiP-LGA141 marking example (package top view)

1. Parts marked as "ES", "E" or accompanied by an engineering sample notification letter, are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

BOTS_SiP-LGA141_DM_V1

TN1433 - Rev 7 page 76/84



Revision history

Table 1. Document revision history

Date	Version	Changes
13-Jan-2023	1	Initial release.
29-Jun-2023	2	The whole document has been updated with the exception of the figures that kept the same index (same vertical text in bottom right corner) as in Rev 1 of the document.
		Updated Section Introduction .
19-Sep-2023	3	Grouped similar package types into corresponding level 1 sections. Added following device marking sections: Section 6.6: Device marking for UFQFPN48 6 × 6 mm (A0F2) Section 7.3: Device marking for VFQFPN48 6 × 6 mm (A0BE) Section 10.4: Device marking for UFBGA81 5 × 5 mm (B0B8) Section 11.3: Device marking for VFBGA178 12 × 12 mm (B0GL) Section 11.5: Device marking for VFBGA223 10 × 10 mm (B0GK) Section 11.7: Device marking for VFBGA264 14 × 14 mm (B0GH) Section 12.2: Device marking for SiP-LGA77 6.5 × 10 mm (B0HQ)
22-Apr-2024	4	Added following device marking sections: Section 11.9: Device marking for VFBGA361 10 × 10 mm (B09U) Section 11.10: Device marking for VFBGA424 14 × 14 mm (B0MP) Section 11.11: Device marking for VFBGA436 18 × 18 mm (B0MS)
16-Jul-2024	5	Added following device marking sections: Section 9.9: Device marking for TFBGA289 14 × 14 mm (B0NY) Section 9.12: Device marking for TFBGA361 16 × 16 mm (B0N8) Section 11.2: Device marking for VFBGA169 6 × 6 mm (B0LA) Section 11.4: Device marking for VFBGA198 10 × 10 mm (B0GJ) Section 11.8: Device marking for VFBGA273 11 × 11 mm (B0P7)
26-Sep-2025	6	Added following device marking sections: Section 4.7: Device marking for LQFP80 12 × 12 mm (C03F) Section 4.10: Device marking for LQFP100 14 × 14 mm (OS) Section 4.12: Device marking for LQFP128 14 × 14 mm (MT) Section 5.1: Device marking for TQFP48 7 × 7 mm (ES) Section 5.2: Device marking for TQFP64 10 × 10 mm (9I) Section 6.2: Device marking for UFQFPN24 4 × 4 mm (998Z) Section 11.6: Device marking for VFBGA225 8 × 8 mm (B0P6) Section 12.1: Device marking for SiP-LGA76 8 × 10.5 mm (B0N2) Section 12.5: Device marking for SiP-LGA141 11.1 × 13.4 mm (B0TS)
01-Dec-2025	7	Updated following device marking sections: Section 4.1: Device marking for LQFP32 7 × 7 mm (5V) Section 4.2: Device marking for LQFP44 10 × 10 mm (4Y) Section 4.3: Device marking for LQFP48 7 × 7 mm (5B) Section 4.5: Device marking for LQFP64 14 × 14 mm (1R) Section 4.6: Device marking for LQFP80 12 × 12 mm (9X) Section 4.9: Device marking for LQFP100 14 × 14 mm (1L) Section 4.11: Device marking for LQFP128 14 × 14 mm (TC) Section 4.13: Device marking for LQFP144 20 × 20 mm (1A) Section 4.14: Device marking for LQFP176 24 × 24 mm (1T) Section 4.15: Device marking for LQFP208 28 × 28 mm (UH)

TN1433 - Rev 7 page 77/84



Contents

1	Gene	eral information	2
2	Devi	ce markings for SO packages	3
	2.1	Device marking for SO8N 4.9 × 6 mm (O7)	3
3	Devi	ce markings for TSSOP packages	4
	3.1	Device marking for TSSOP14 5 × 4.4 mm (6R)	4
	3.2	Device marking for TSSOP20 6.5 × 4.4 mm (YA)	4
4	Devi	ce markings for LQFP packages	6
	4.1	Device marking for LQFP32 7 × 7 mm (5V)	6
	4.2	Device marking for LQFP44 10 × 10 mm (4Y)	6
	4.3	Device marking for LQFP48 7 × 7 mm (5B)	7
	4.4	Device marking for LQFP64 10 × 10 mm (5W)	8
	4.5	Device marking for LQFP64 14 × 14 mm (1R)	8
	4.6	Device marking for LQFP80 12 × 12 mm (9X)	9
	4.7	Device marking for LQFP80 12 × 12 mm (C03F)	. 10
	4.8	Device marking for LQFP80 14 × 14 mm (1S)	. 10
	4.9	Device marking for LQFP100 14 × 14 mm (1L)	. 11
	4.10	Device marking for LQFP100 14 × 14 mm (OS)	. 12
	4.11	Device marking for LQFP128 14 × 14 mm (TC)	. 12
	4.12	Device marking for LQFP128 14 × 14 mm (MT)	. 13
	4.13	Device marking for LQFP144 20 × 20 mm (1A)	. 14
	4.14	Device marking for LQFP176 24 × 24 mm (1T)	. 15
	4.15	Device marking for LQFP208 28 × 28 mm (UH)	. 16
5	Devi	ce markings for TQFP packages	.18
	5.1	Device marking for TQFP48 7 × 7 mm (ES)	. 18
	5.2	Device marking for TQFP64 10 × 10 mm (9I)	. 18
6	Devi	ce markings for UFQFPN packages	.20
	6.1	Device marking for UFQFPN20 3 × 3 mm (A0A5)	. 20
	6.2	Device marking for UFQFPN24 4 × 4 mm (998Z)	. 20
	6.3	Device marking for UFQFPN28 4 × 4 mm (A0B0)	.21
	6.4	Device marking for UFQFPN32 5 × 5 mm (A09E)	. 22
	6.5	Device marking for UFQFPN32 5 × 5 mm (A0B8)	. 23
	6.6	Device marking for UFQFPN48 6 × 6 mm (A0F2)	. 24
	6.7	Device marking for UFQFPN48 7 × 7 mm (A0B9)	. 25
7	Devi	ce markings for VFQFPN packages	.27



	7.1	Device marking for VFQFPN32 5 × 5 mm (42)	. 27
	7.2	Device marking for VFQFPN36 6 × 6 mm (ZR)	. 27
	7.3	Device marking for VFQFPN48 6 × 6 mm (A0BE)	. 28
	7.4	Device marking for VFQFPN68 8 × 8 mm (B029)	. 29
8	Devic	e markings for LFBGA packages	.31
	8.1	Device marking for LFBGA36 6 × 6 mm (AL)	. 31
	8.2	Device marking for LFBGA100 10 × 10 mm (H0)	. 31
	8.3	Device marking for LFBGA144 10 × 10 mm (X3)	. 32
	8.4	Device marking for LFBGA289 14 × 14 mm (B0ED)	. 33
	8.5	Device marking for LFBGA354 16 × 16 mm (B02Z)	. 33
	8.6	Device marking for LFBGA448 18 × 18 mm (B032)	. 34
9	Devic	ce markings for TFBGA packages	.36
	9.1	Device marking for TFBGA64 5 × 5 mm (R8)	. 36
	9.2	Device marking for TFBGA100 8 × 8 mm (A08Q)	. 36
	9.3	Device marking for TFBGA169 7 × 7 mm (B0MA)	. 37
	9.4	Device marking for TFBGA216 13 × 13 mm (A0L2)	. 38
	9.5	Device marking for TFBGA225 13 × 13 mm (B04V)	. 39
	9.6	Device marking for TFBGA257 10 × 10 mm (B02Y)	. 40
	9.7	Device marking for TFBGA240+25 14 × 14 mm (A07U)	.41
	9.8	Device marking for TFBGA289 9 × 9 mm (B0EB)	. 42
	9.9	Device marking for TFBGA289 14 × 14 mm (B0NY)	. 43
	9.10	Device marking for TFBGA320 11 × 11 mm (B0EC)	
	9.11	Device marking for TFBGA361 12 × 12 mm (B031)	. 45
	9.12	Device marking for TFBGA361 16 × 16 mm (B0N8)	. 46
	9.13	Device marking for TFBGA436 18 × 18 mm (B0D1)	. 47
10	Devic	e markings for UFBGA packages	.49
	10.1	Device marking for UFBGA59 5 × 5 mm (B0FS)	. 49
	10.2	Device marking for UFBGA64 5 × 5 mm (A019)	. 49
	10.3	Device marking for UFBGA73 5 × 5 mm (B08E)	. 50
	10.4	Device marking for UFBGA81 5 × 5 mm (B0B8)	. 51
	10.5	Device marking for UFBGA100 7 × 7 mm (A0C2)	. 52
	10.6	Device marking for UFBGA121 6 × 6 mm (B0CU)	. 53
	10.7	Device marking for UFBGA129 7 × 7 mm (B09R)	. 54
	10.8	Device marking for UFBGA132 7 × 7 mm (A0G8)	. 55
	10.9	Device marking for UFBGA144 7 × 7 mm (A0AS)	
	10.10	Device marking for UFBGA144 10 × 10 mm (A02Y)	. 57

TN1433 - Rev 7



	10.11	Device marking for UFBGA169 7 × 7 mm (A0YV)	. 58
	10.12	Device marking for UFBGA176+25 10 × 10 mm (A0E7)	. 59
11	Devic	e markings for VFBGA packages	.61
	11.1	Device marking for VFBGA142 8 × 8 mm (B0GM)	. 61
	11.2	Device marking for VFBGA169 6 × 6 mm (B0LA)	. 61
	11.3	Device marking for VFBGA178 12 × 12 mm (B0GL)	. 62
	11.4	Device marking for VFBGA198 10 × 10 mm (B0GJ)	. 63
	11.5	Device marking for VFBGA223 10 × 10 mm (B0GK)	. 64
	11.6	Device marking for VFBGA225 8 × 8 mm (B0P6)	. 65
	11.7	Device marking for VFBGA264 14 × 14 mm (B0GH)	. 66
	11.8	Device marking for VFBGA273 11 × 11 mm (B0P7)	. 67
	11.9	Device marking for VFBGA361 10 × 10 mm (B09U)	. 68
	11.10	Device marking for VFBGA424 14 × 14 mm (B0MP)	. 69
	11.11	Device marking for VFBGA436 18 × 18 mm (B0MS)	. 70
12	Devic	e markings for SiP-LGA packages	.72
	12.1	Device marking for SiP-LGA76 8 × 10.5 mm (B0N2)	. 72
	12.2	Device marking for SiP-LGA77 6.5 × 10 mm (B0HQ)	. 72
	12.3	Device marking for SiP-LGA86 7.3 × 11 mm (B0CQ)	. 73
	12.4	Device marking for SiP-LGA92 10 × 10 mm (B0HB)	. 74
	12.5	Device marking for SiP-LGA141 11.1 × 13.4 mm (B0TS)	. 75
Revi	sion h	nistory	.77
List	of tab	les	.81
List	of figu	ıres	.82

TN1433 - Rev 7 page 80/84





List of tables

able 1.	ocument revision history	 77	7

TN1433 - Rev 7 page 81/84



List of figures

rigure i.		. ა
Figure 2.	TSSOP14 marking example (package top view)	. 4
Figure 3.	TSSOP20 marking example (package top view)	. 5
Figure 4.	LQFP32 marking example (package top view)	. 6
Figure 5.	LQFP44 marking example (package top view)	. 7
Figure 6.	LQFP48 marking example (package top view)	. 7
Figure 7.	LQFP64 marking example (package top view)	. 8
Figure 8.	LQFP64 marking example (package top view)	. 9
Figure 9.	LQFP80 marking example (package top view)	. 9
Figure 10.	LQFP80 marking example (package top view)	10
Figure 11.	LQFP80 marking example (package top view)	11
Figure 12.	LQFP100 marking example (package top view)	11
Figure 13.	LQFP100 marking example (package top view)	12
Figure 14.	LQFP128 marking example (package top view)	
Figure 15.	LQFP128 marking example (package top view)	14
Figure 16.	LQFP144 marking example (package top view)	15
Figure 17.	LQFP176 marking example (package top view)	16
Figure 18.	LQFP208 marking example (package top view)	17
Figure 19.	TQFP48 marking example (package top view)	18
Figure 20.	TQFP64 marking example (package top view)	19
Figure 21.	UFQFPN20 marking example (package top view)	20
Figure 22.	UFQFPN24 marking example (package top view)	21
Figure 23.	UFQFPN28 marking example (package top view)	22
Figure 24.	UFQFPN32 marking example (package top view)	23
Figure 25.	UFQFPN32 marking example (package top view)	
Figure 26.	UFQFPN48 marking example (package top view)	25
Figure 27.	UFQFPN48 marking example (package top view)	26
Figure 28.	VFQFPN32 marking example (package top view)	27
Figure 29.	VFQFPN36 marking example (package top view)	28
Figure 30.	VFQFPN48 marking example (package top view)	
Figure 31.	VFQFPN68 marking example (package top view)	
Figure 32.	LFBGA36 marking example (package top view)	
Figure 33.	LFBGA100 marking example (package top view)	32
Figure 34.	LFBGA144 marking example (package top view)	
Figure 35.	LFBGA289 marking example (package top view)	33
Figure 36.	LFBGA354 marking example (package top view)	34
Figure 37.	LFBGA448 marking example (package top view)	
Figure 38.	TFBGA64 marking example (package top view)	36
Figure 39.	TFBGA100 marking example (package top view)	37
Figure 40.	TFBGA169 marking example (package top view)	
Figure 41.	TFBGA216 marking example (package top view)	
Figure 42.	TFBGA225 marking example (package top view)	
Figure 43.	TFBGA257 marking example (package top view)	
Figure 44.	TFBGA240+25 marking example (package top view)	42
Figure 45.	TFBGA289 marking example (package top view)	
Figure 46.	TFBGA289 marking example (package top view)	
Figure 47.	TFBGA320 marking example (package top view)	
Figure 48.	TFBGA361 marking example (package top view)	
Figure 49.	TFBGA361 marking example (package top view)	
Figure 50.	TFBGA436 marking example (package top view)	
Figure 51.	UFBGA59 marking example (package top view)	
Figure 52.	UFBGA64 marking example (package top view)	
Figure 53.	UFBGA73 marking example (package top view)	51

TN1433 - Rev 7

TN1433

List of figures



Figure 54.	UFBGA81 marking example (package top view)	52
Figure 55.	UFBGA100 marking example (package top view)	53
Figure 56.	UFBGA121 marking example (package top view)	54
Figure 57.	UFBGA129 marking example (package top view)	55
Figure 58.	UFBGA132 marking example (package top view)	56
Figure 59.	UFBGA144 marking example (package top view)	57
Figure 60.	UFBGA144 marking example (package top view)	58
Figure 61.	UFBGA169 marking example (package top view)	59
Figure 62.	UFBGA176+25 marking example (package top view)	60
Figure 63.	VFBGA142 marking example (package top view)	61
Figure 64.	VFBGA169 marking example (package top view)	62
Figure 65.	VFBGA178 marking example (package top view)	63
Figure 66.	VFBGA198 marking example (package top view)	64
Figure 67.	VFBGA223 marking example (package top view)	65
Figure 68.	VFBGA225 marking example (package top view)	66
Figure 69.	VFBGA264 marking example (package top view)	67
Figure 70.	VFBGA273 marking example (package top view)	
Figure 71.	VFBGA361 marking example (package top view)	69
Figure 72.	VFBGA424 marking example (package top view)	70
Figure 73.	VFBGA436 marking example (package top view)	71
Figure 74.	SiP-LGA76 marking example (package top view)	72
Figure 75.	SiP-LGA77 marking example (package top view)	73
Figure 76.	SiP-LGA86 marking example (package top view)	
Figure 77.	SiP-LGA92 marking example (package top view)	75
Figure 78.	SiP-LGA141 marking example (package top view)	76



IMPORTANT NOTICE - READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice.

In the event of any conflict between the provisions of this document and the provisions of any contractual arrangement in force between the purchasers and ST, the provisions of such contractual arrangement shall prevail.

The purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgment.

The purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of the purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

If the purchasers identify an ST product that meets their functional and performance requirements but that is not designated for the purchasers' market segment, the purchasers shall contact ST for more information.

ST and the ST logo are trademarks of ST. For additional information about ST trademarks, refer to www.st.com/trademarks. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2025 STMicroelectronics - All rights reserved

TN1433 - Rev 7 page 84/84