Figure 1. X-NUCLEO-SNK1M1 circuit schematic (1 of 2)

For the STM32 Nucleo families embedding L412, L433, L452 and L476, the USB DM pin coexists with STMG4 CC1 pin.
To exploit the USB functionality with these L4 families, the solder bridges R12, R13 must be fit and removed the R8 and R9.

Interface Configuration

X-NUCLEO-SNK1M1 - Rev 7 - May 2025
For further information, contact your local STMicroelectronics sales office.

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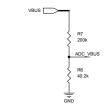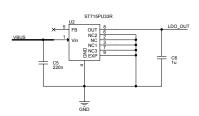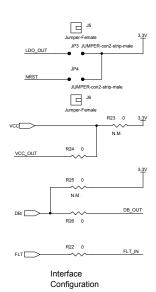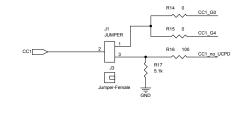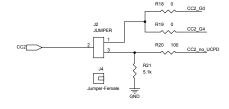## Figure 2. X-NUCLEO-SNK1M1 circuit schematic (2 of 2)

VBUS max	Pmax	Resistor reference	Onboard values (mounted)	Recommended values for noncompliant adapters
6 V	15 W	R1_6V	2.7 k	2.4 k
10 V	27 W	R1_10V	1.5 k	1.27 k
13 V	36 W	R1_13V	1.1 k	910
17 V	45 W	R1_17V	820	715
22 V	100 W	R1_22V	620	536

Controller side
Connector side
TCPP01_M12

10kΩ to be added between TCPP01-M12 SOURCE PIN and VBUS in harsh environment

USB TYPE-C® RECEPTACLE

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