



Schematic diagrams

Figure 1. X-NUCLEO-PM33A1 circuit schematic (1 of 3)

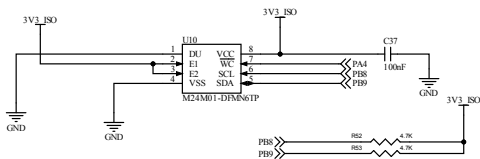
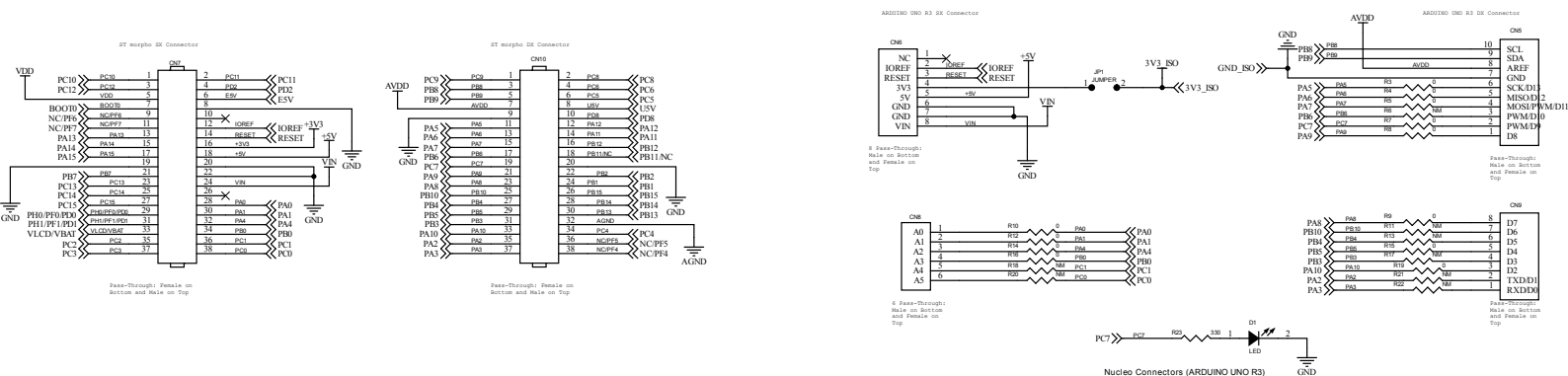


Figure 2. X-NUCLEO-PM33A1 circuit schematic (2 of 3)

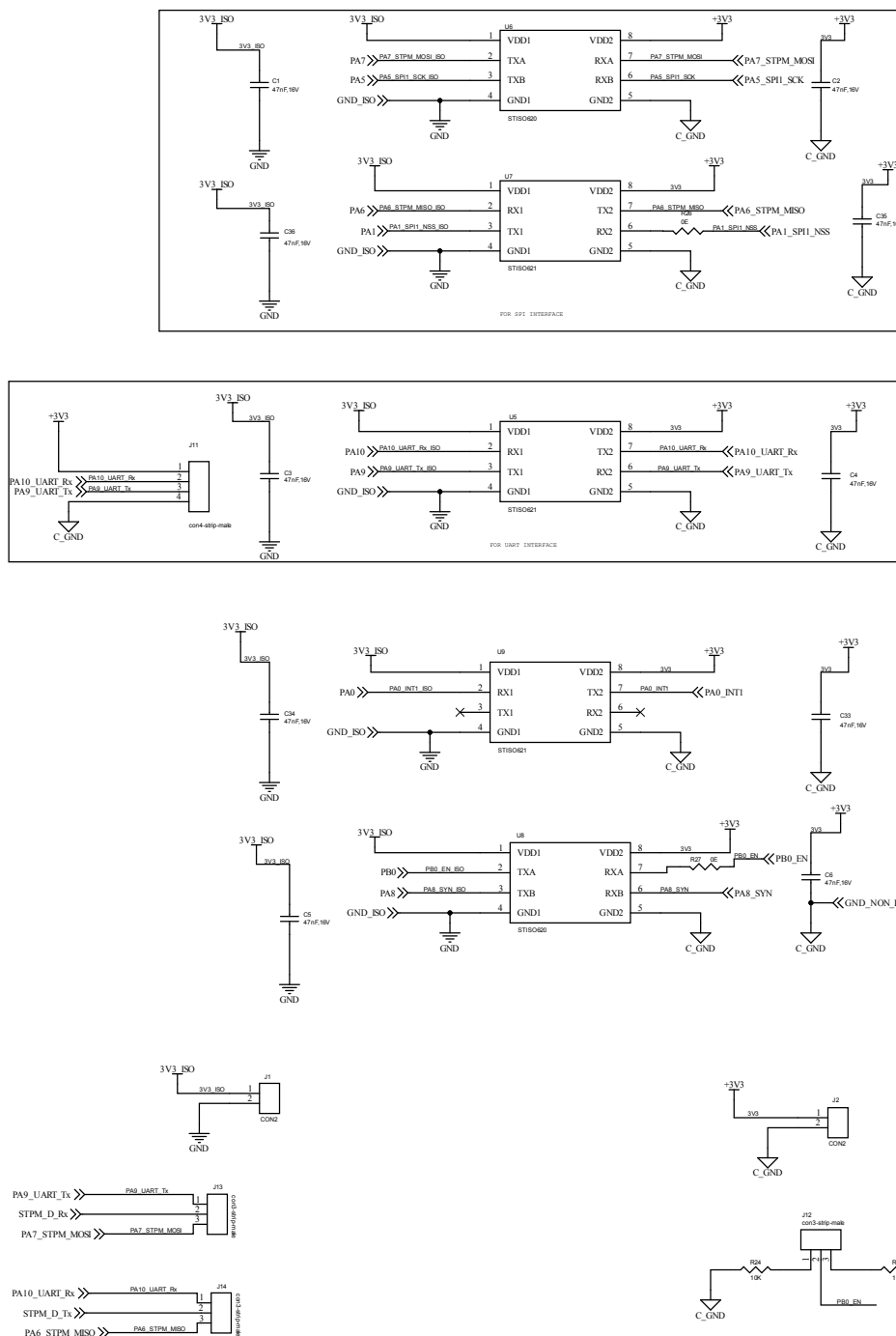
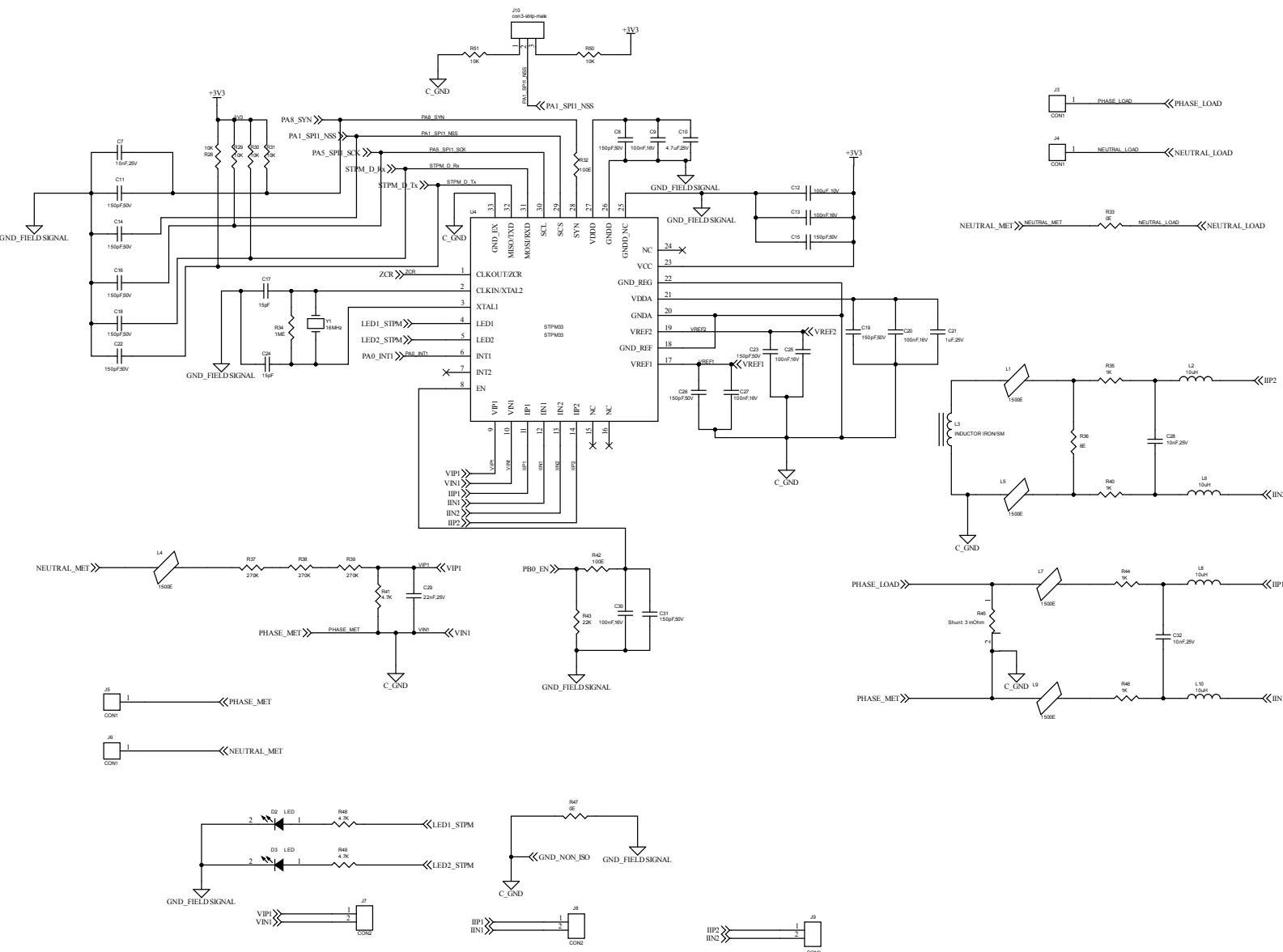


Figure 3. X-NUCLEO-PM33A1 circuit schematic (3 of 3)



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