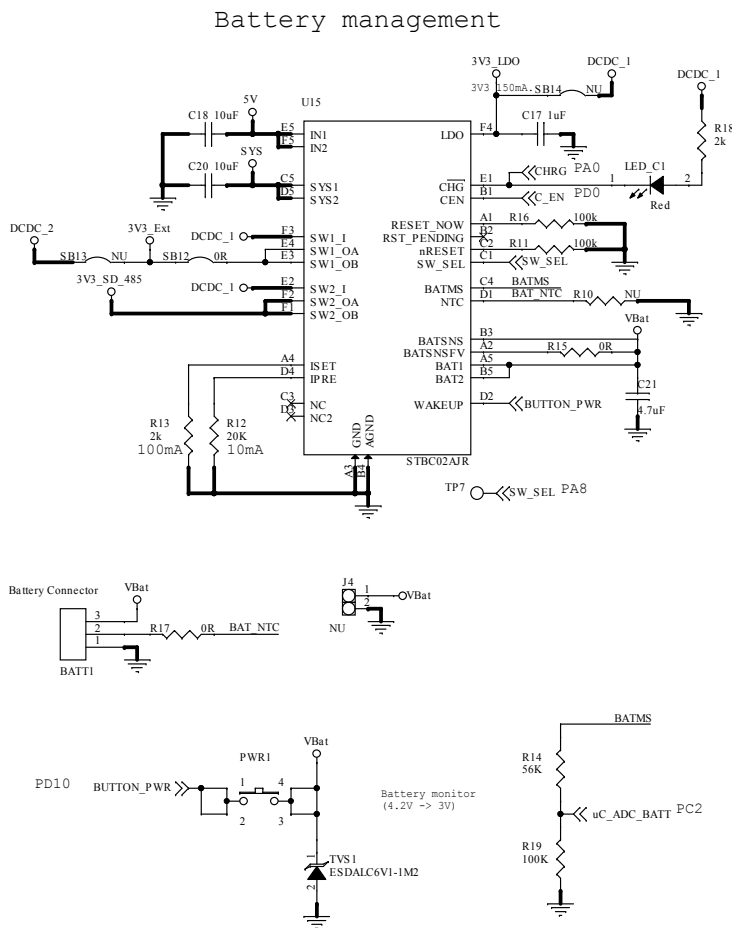
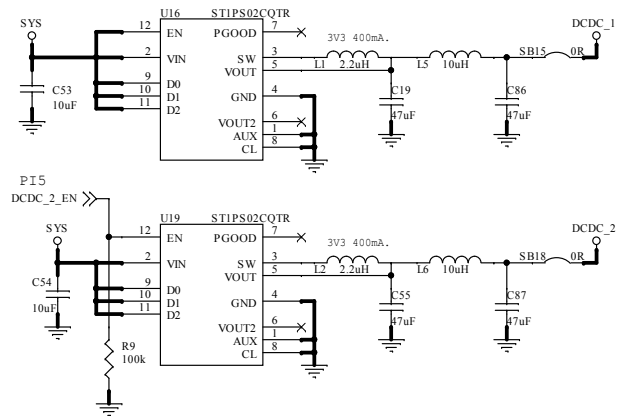


Schematic diagrams

Figure 1. Core system board circuit schematic (1 of 8)



3.3V DC-DC



2.7V Analog LDO

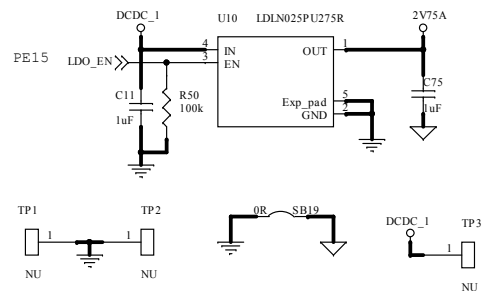


Figure 2. Core system board circuit schematic (2 of 8)

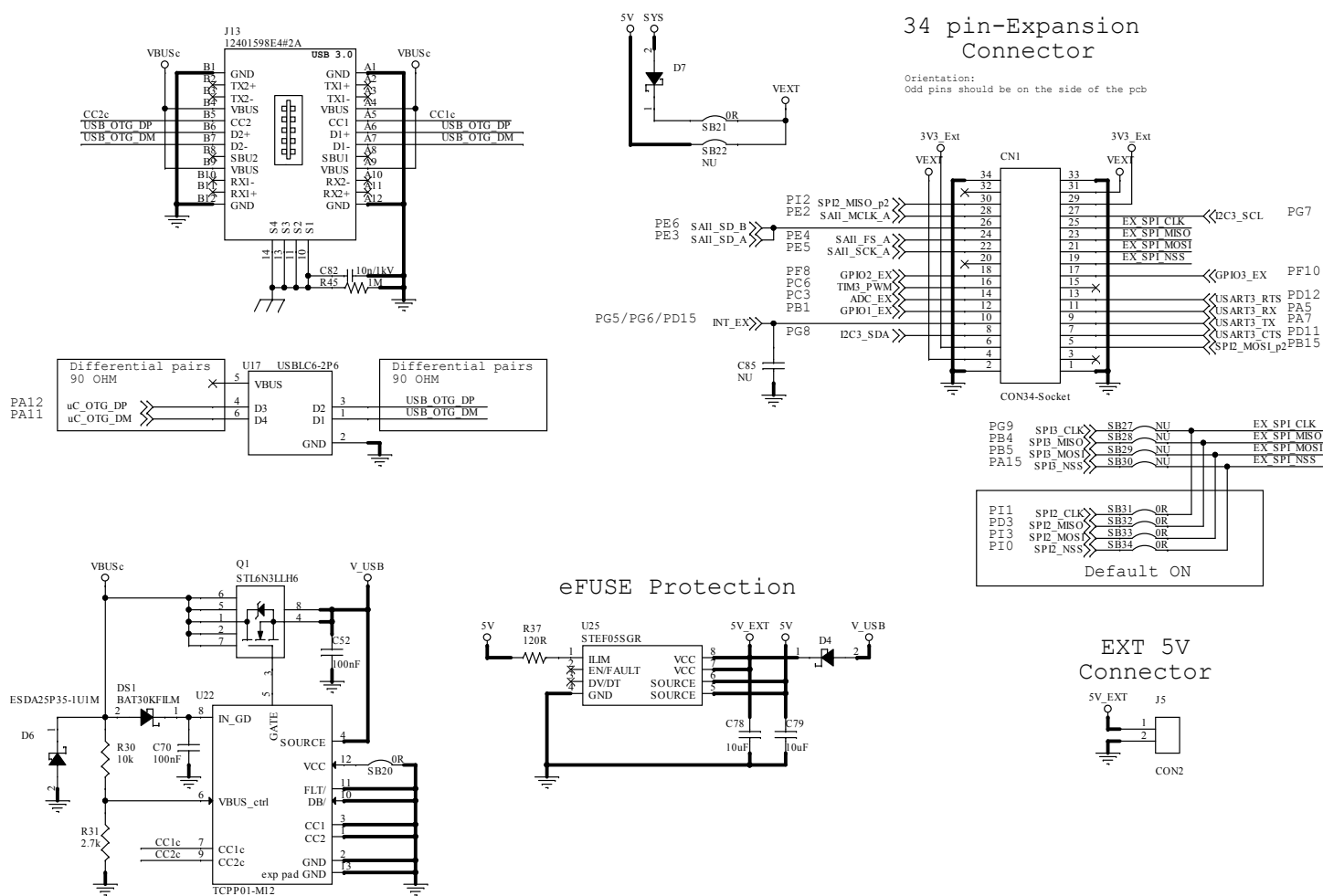


Figure 3. Core system board circuit schematic (3 of 8)

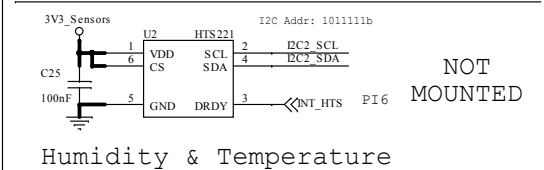
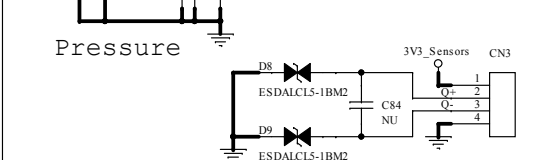
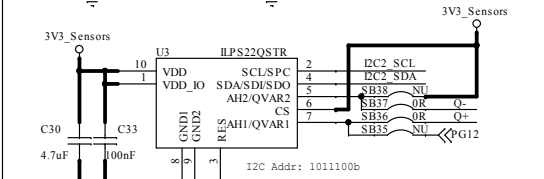
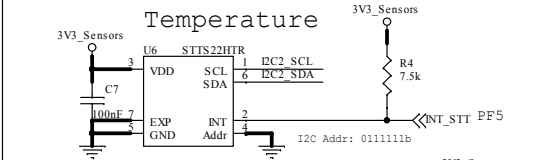
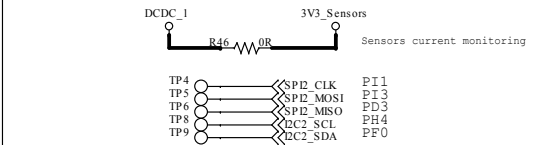
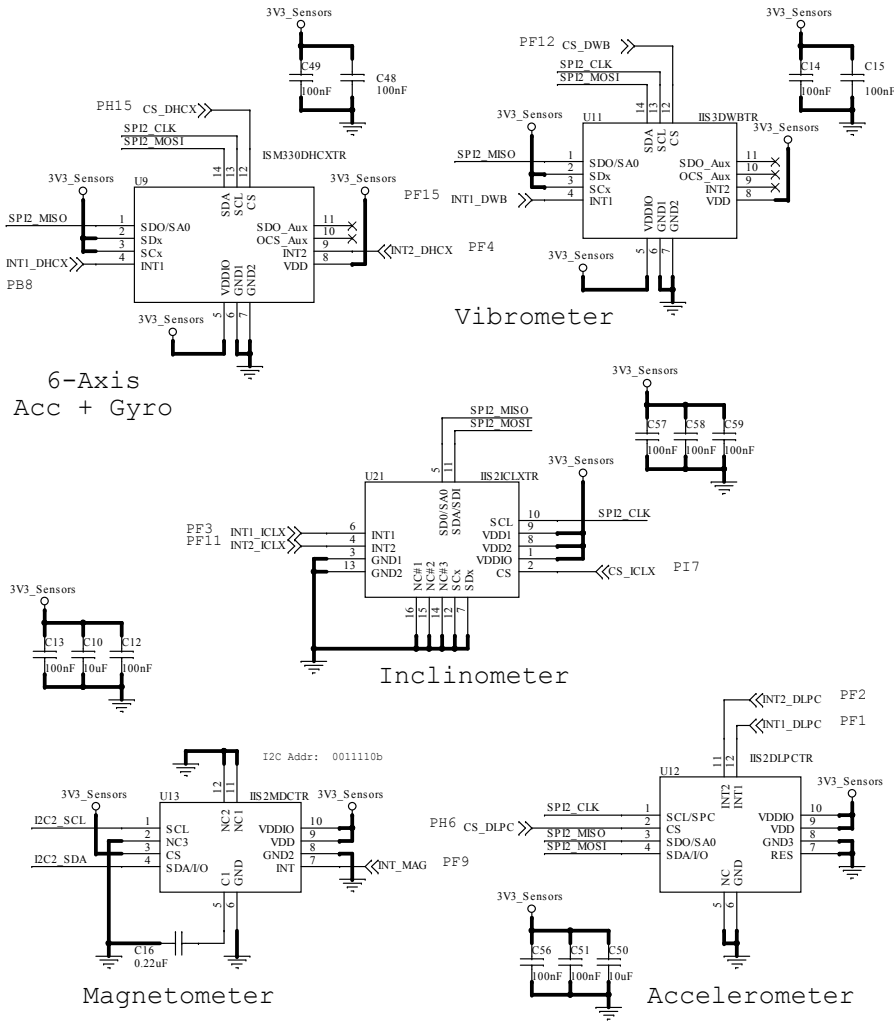
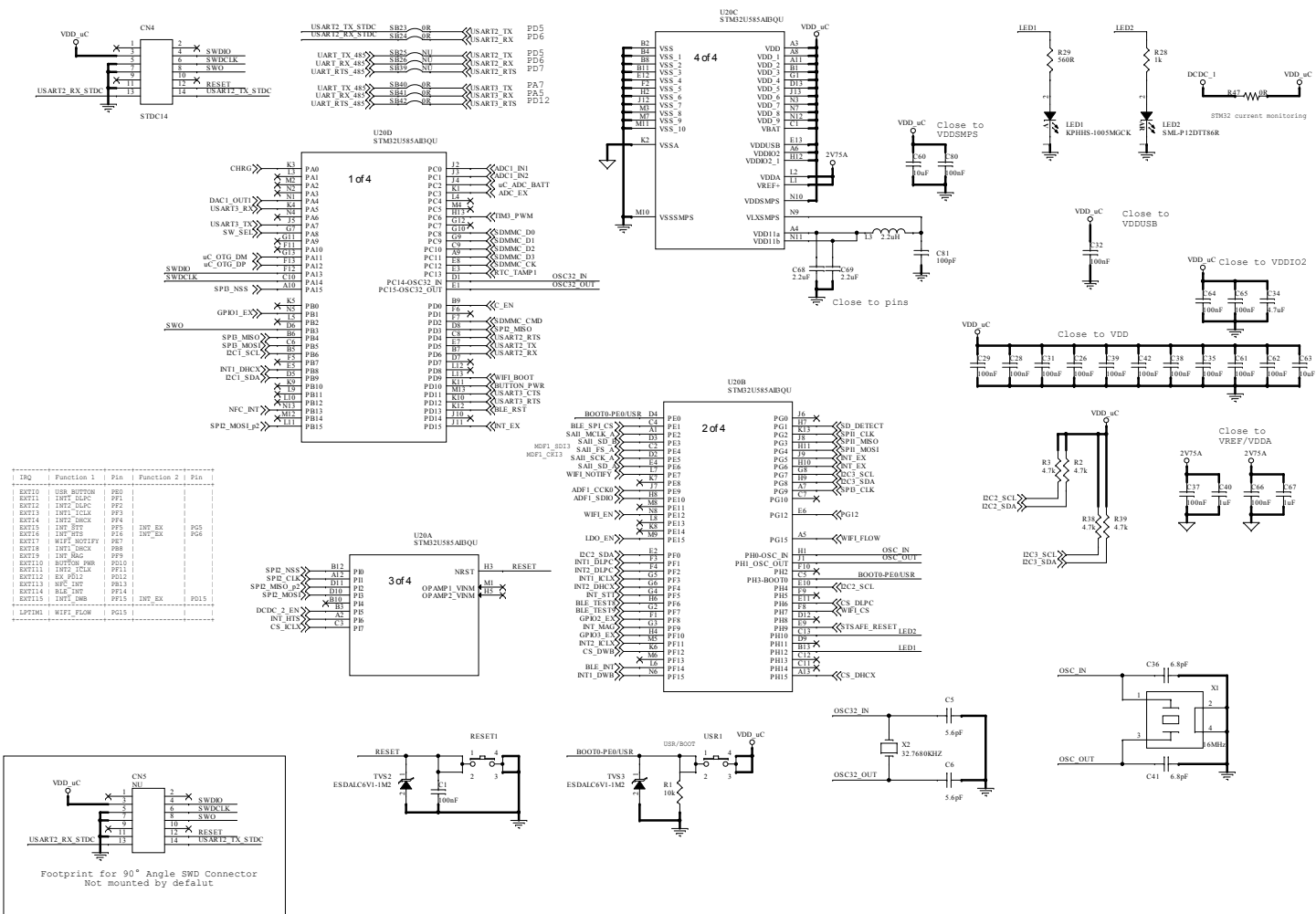


Figure 4. Core system board circuit schematic (4 of 8)



The schematic diagram shows the ADF105TR circuit. It includes a 3V3_Sensors power supply, a TP12 test point, a PE9 input, and a PE10 output. The ADF105TR is configured with VDD, DOUT, CLK, and GND pins. It includes a 100nF capacitor (C2) and a 1uF capacitor (C24). The output is labeled ADF1_SDO.

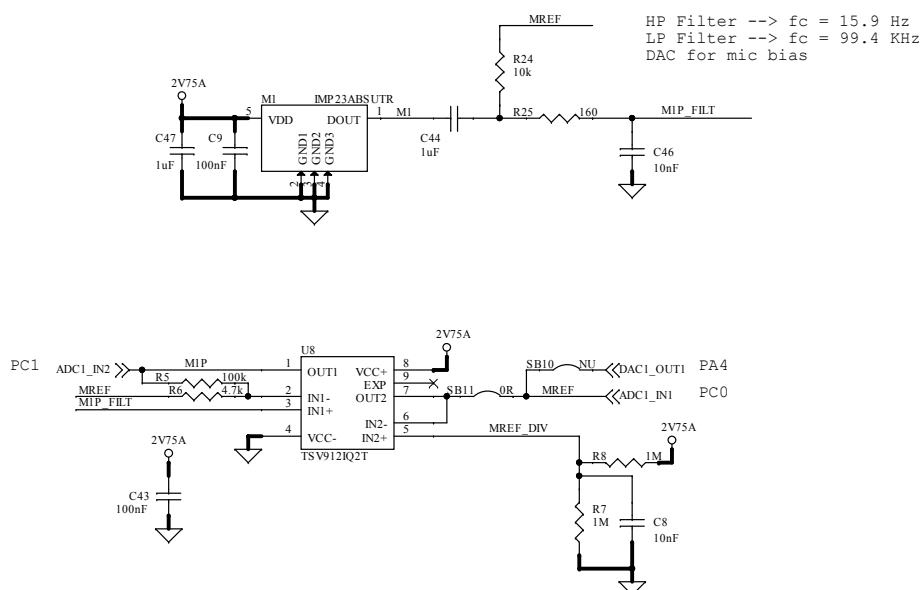
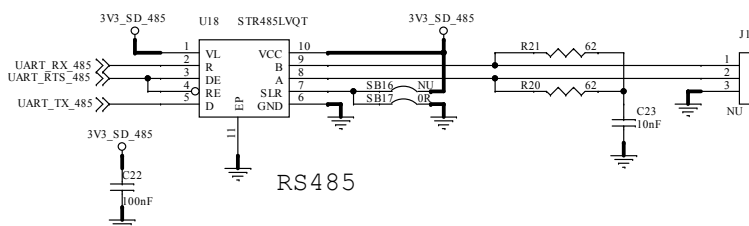
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Figure 7. Core system board circuit schematic (7 of 8)

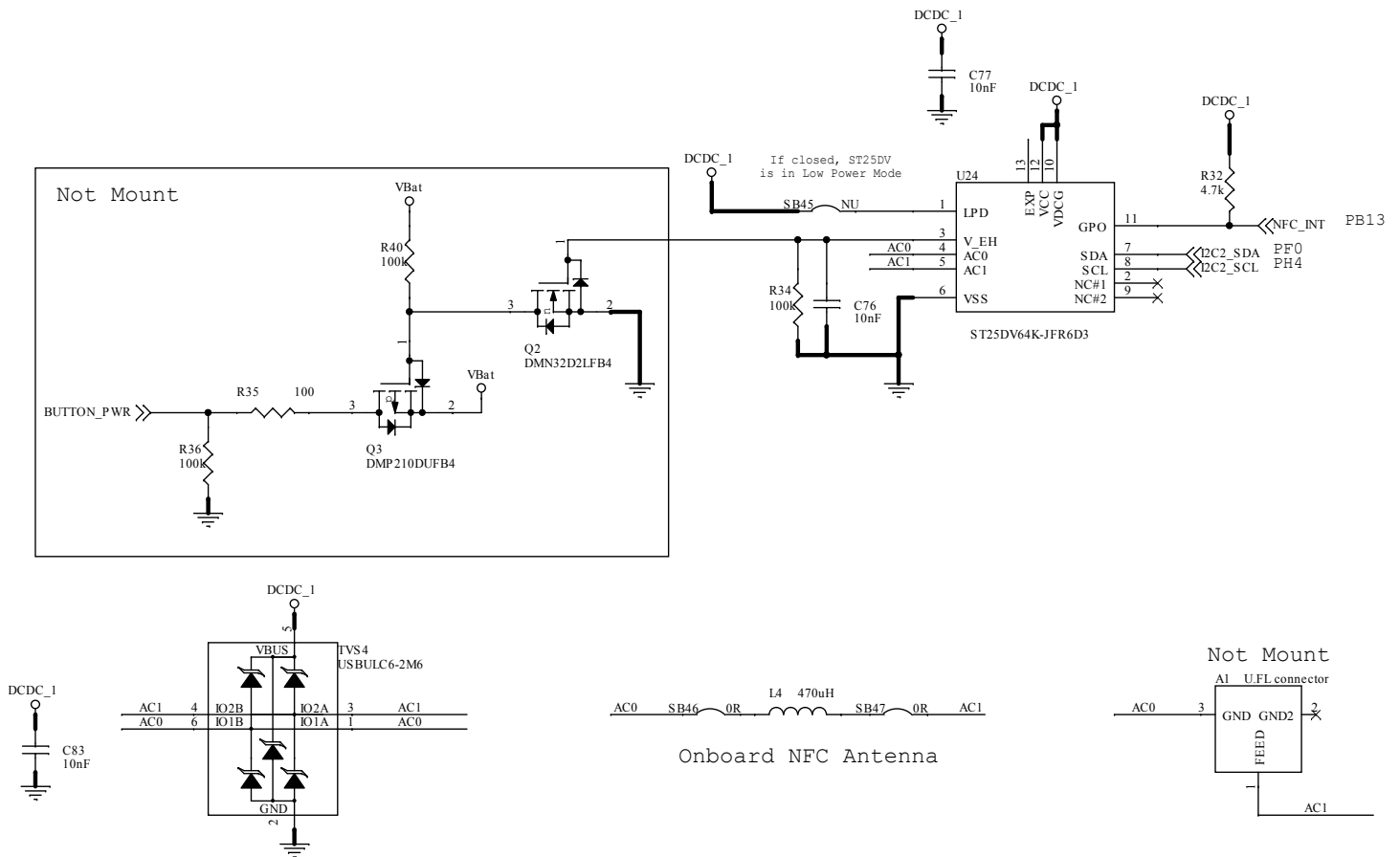
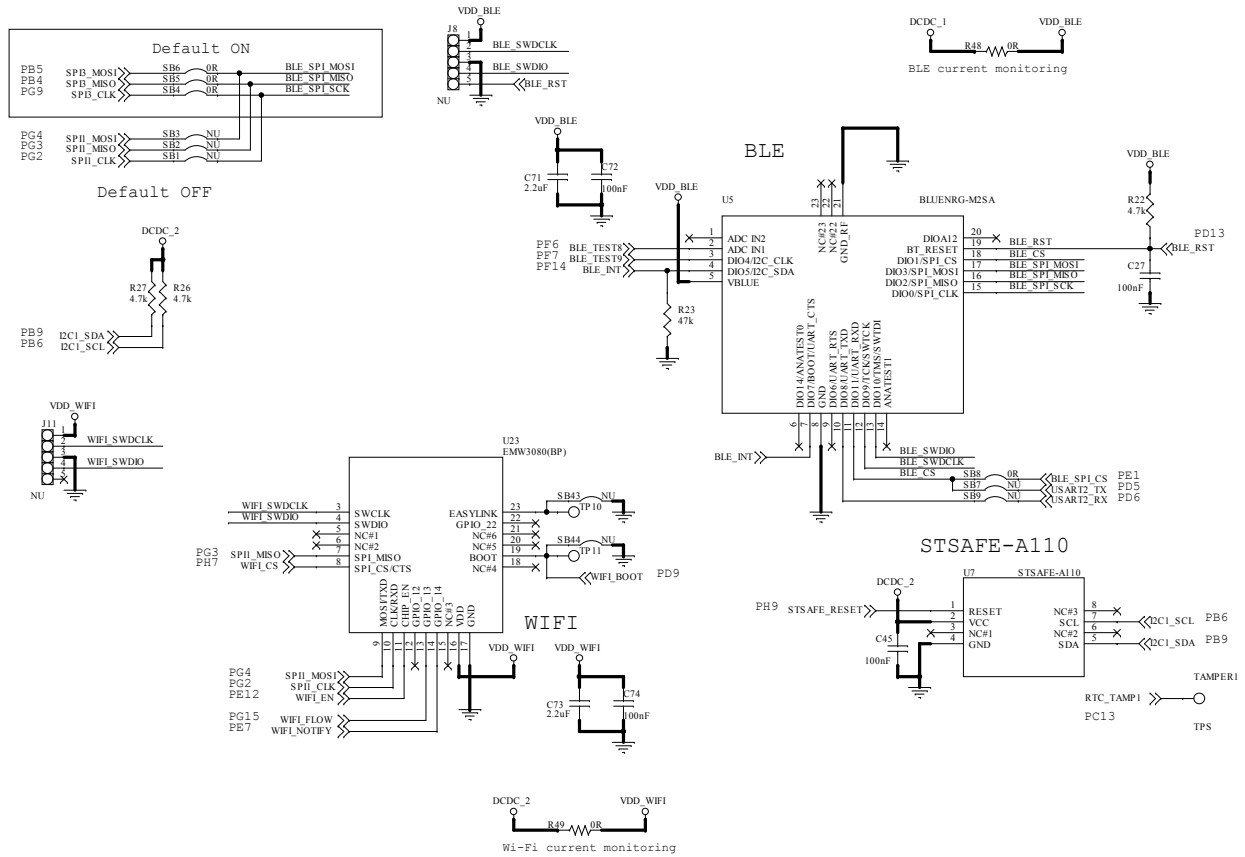
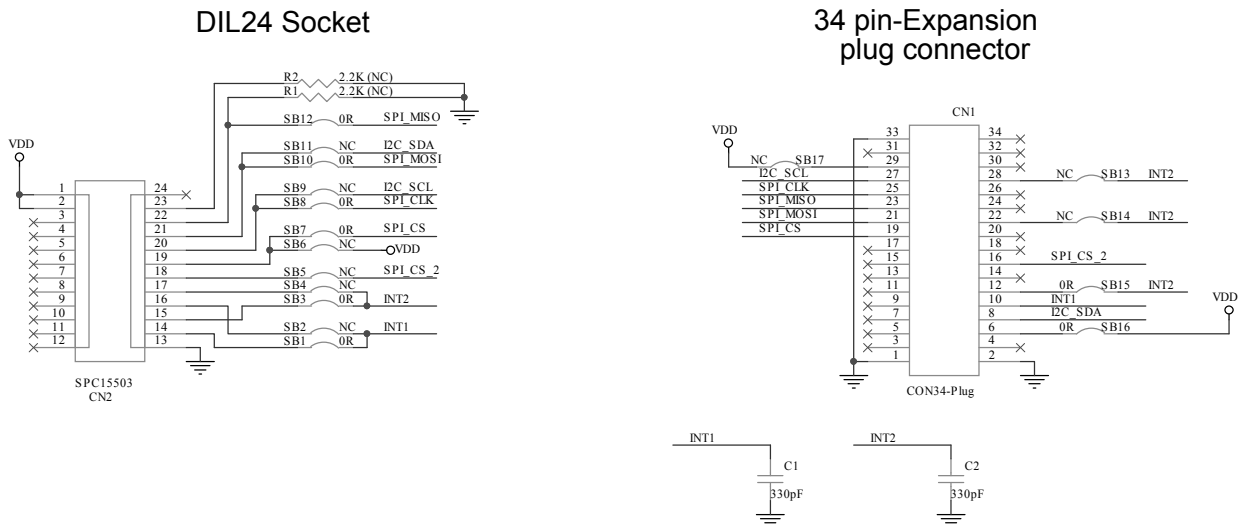


Figure 8. Core system board circuit schematic (8 of 8)

Figure 9. Adapter board circuit schematic


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