STEVAL-IEG001V2

Schematic diagrams

Figure 1. µSD connections

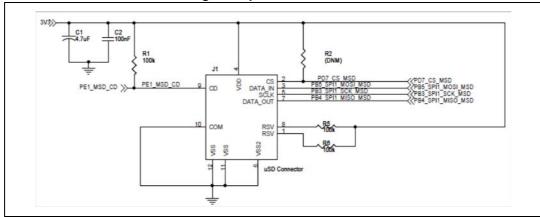


Figure 2. IrDA transceiver

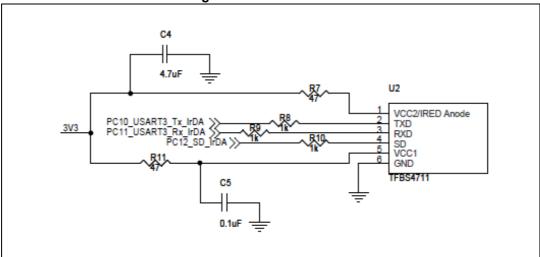


Figure 3. EEPROM section

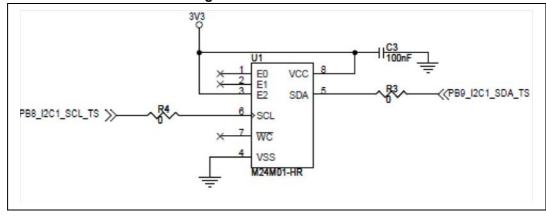


Figure 4. LCD connector on VTS motherboard

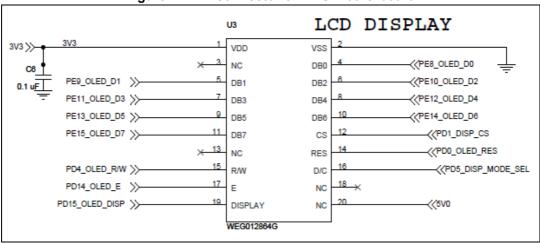


Figure 5. Graphical LCD daughter card

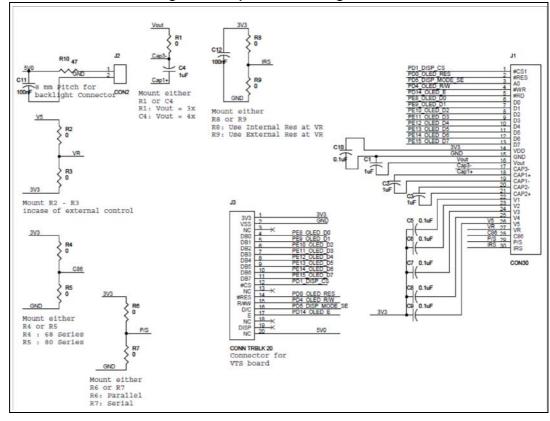


Figure 6. GPS module interface

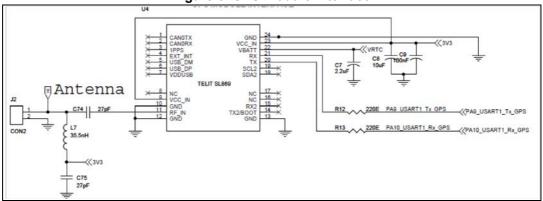


Figure 7. GSM

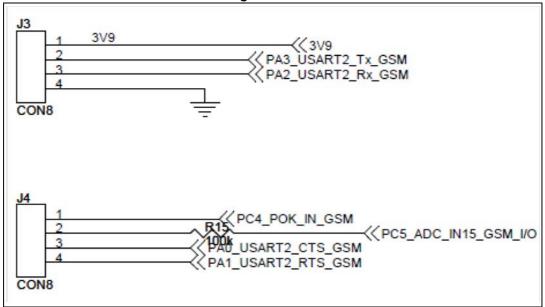


Figure 8. GSM module interface

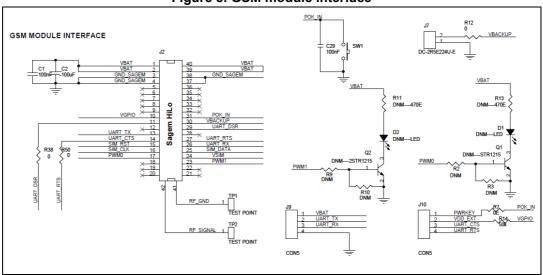
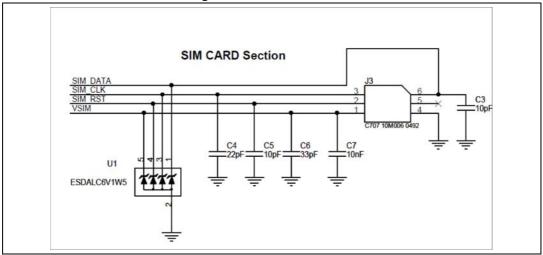


Figure 9. SIM card section



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Figure 10. Compatibility between STM32F10x and STM32F20x



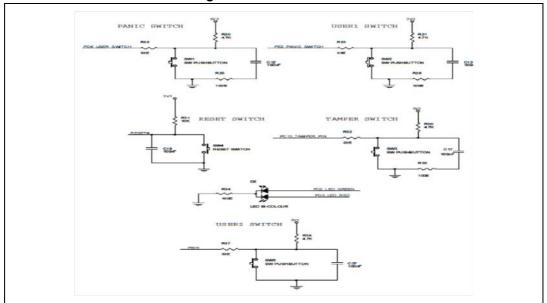


Figure 12. SWD section

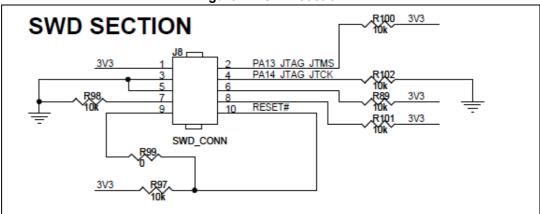


Figure 13. 5 V regulator

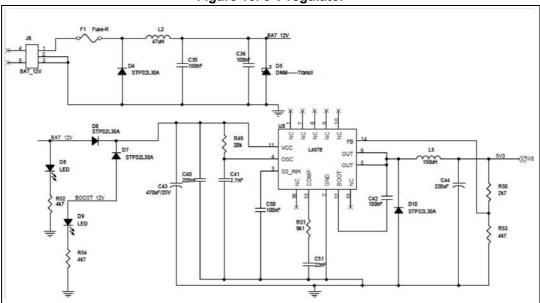


Figure 14. L6924D battery charger

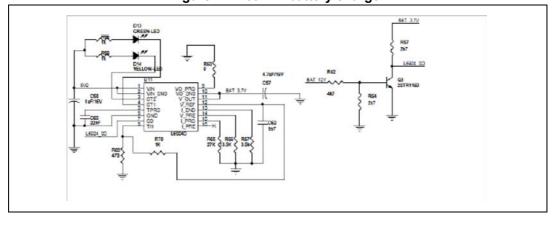


Figure 15. Voltage regulators

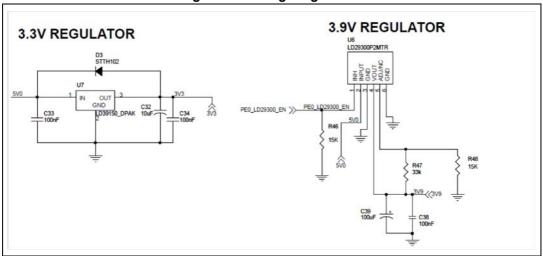


Figure 16. Gyroscope section

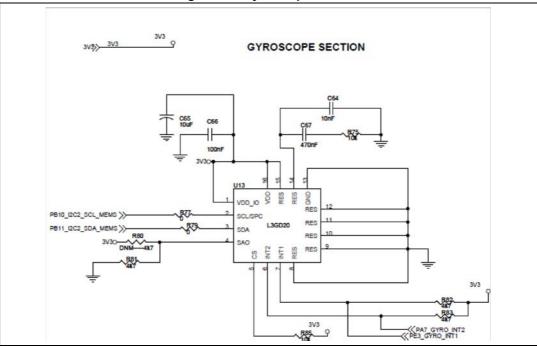
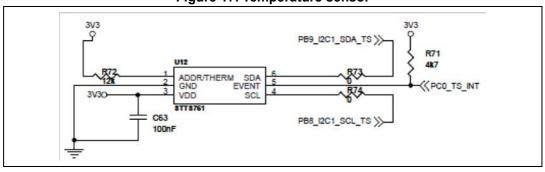


Figure 17. Temperature sensor



PC1_ACC_INT1 >> PC1_ACC_INT1
PA5_ACC_INT2 >> PA5_ACC_INT2 C70 =100nF VDD_IO GND NC GND PC1 ACC INT1 NC INT 1 LIS331DLH SCL RESERVED PA5 ACC INT2 GND INT 2 SD SDA -((PB10_I2C2_SCL_MEMS -</PB11_I2C2_SDA_MEMS

Figure 18. Accelerometer section