

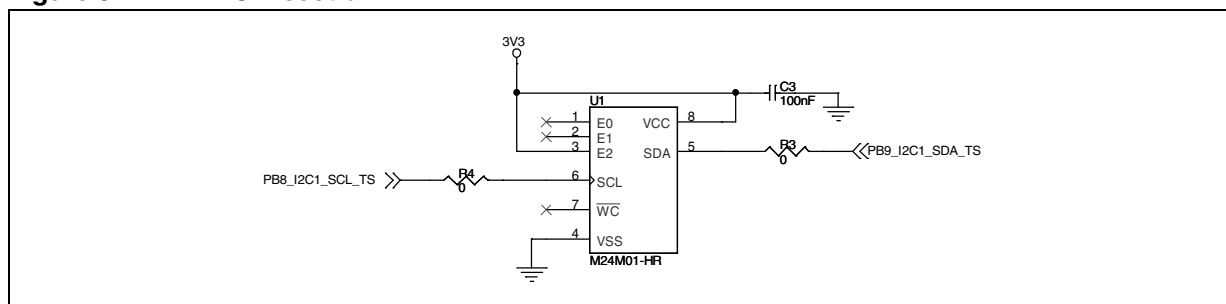


Figure 4. LCD connector on VTS motherboard

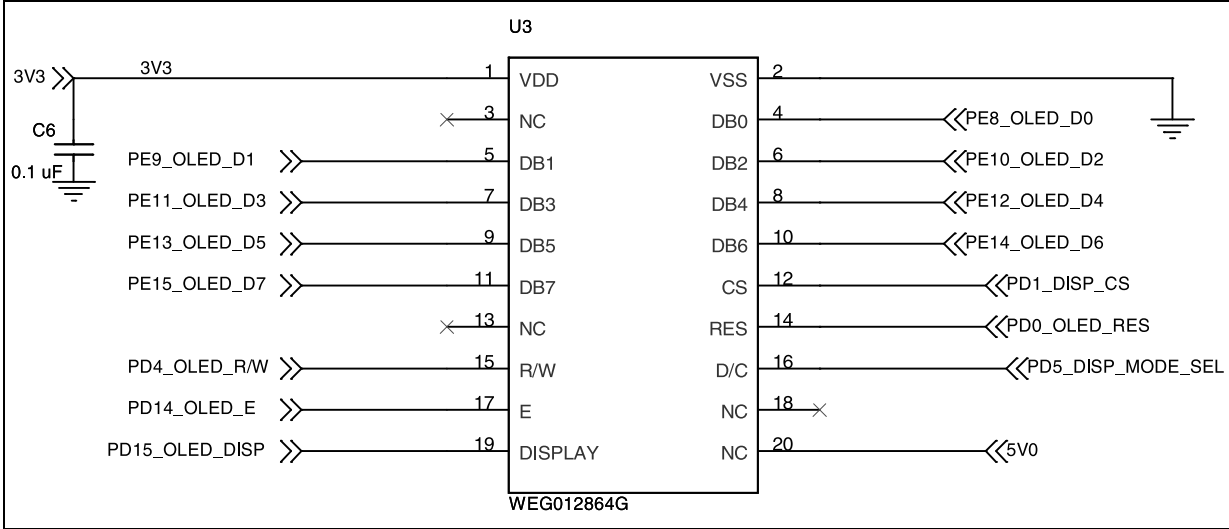


Figure 5. Graphical LCD daughter card

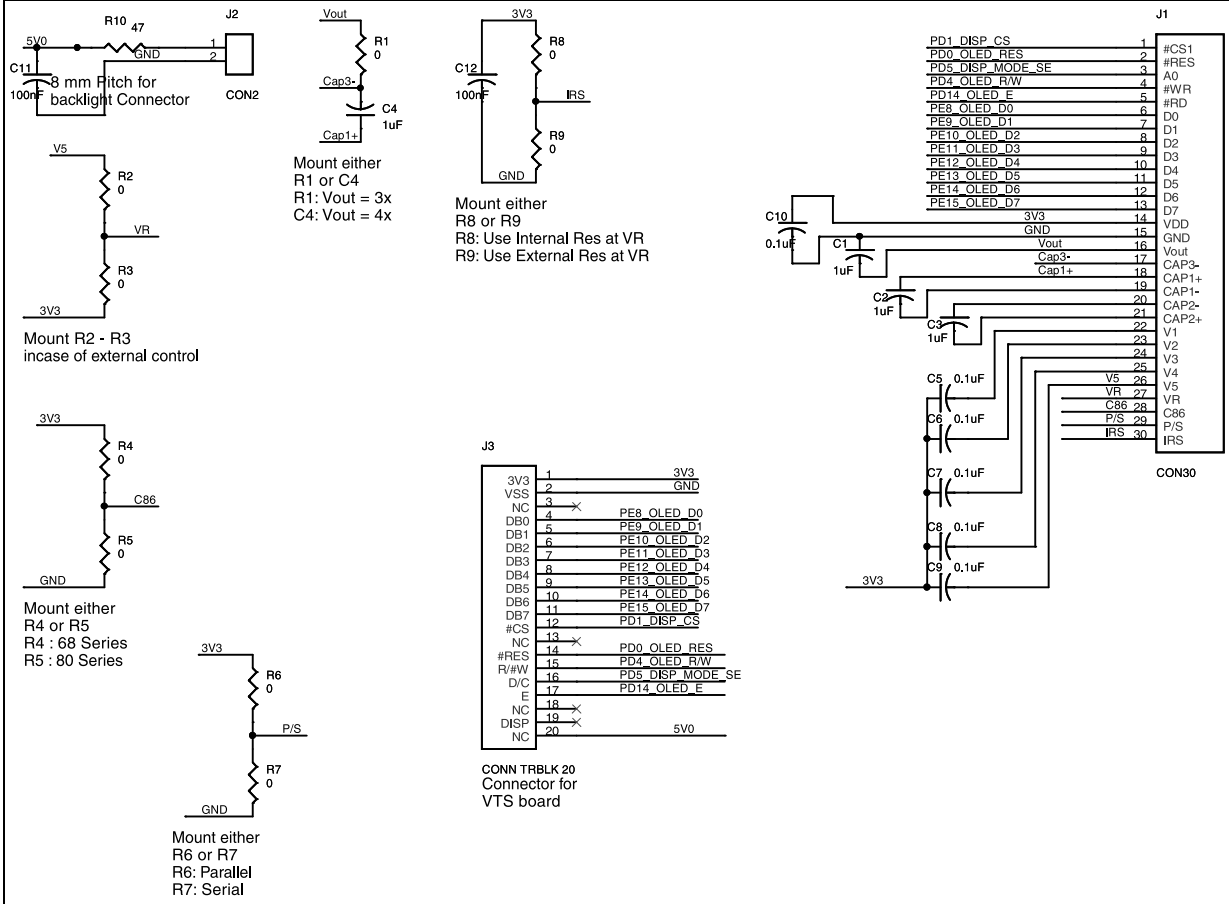


Figure 8. SIM card section

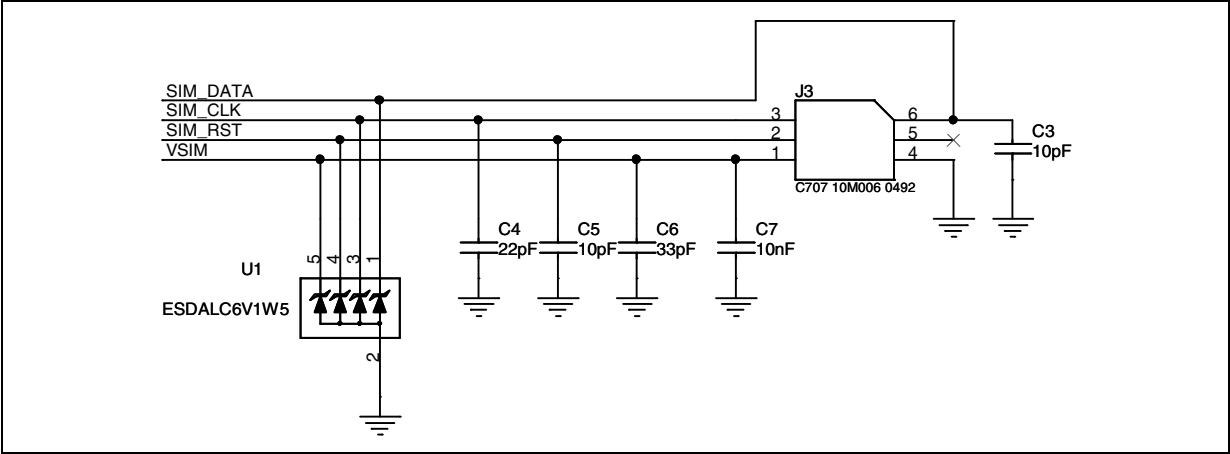


Figure 10. Switch section

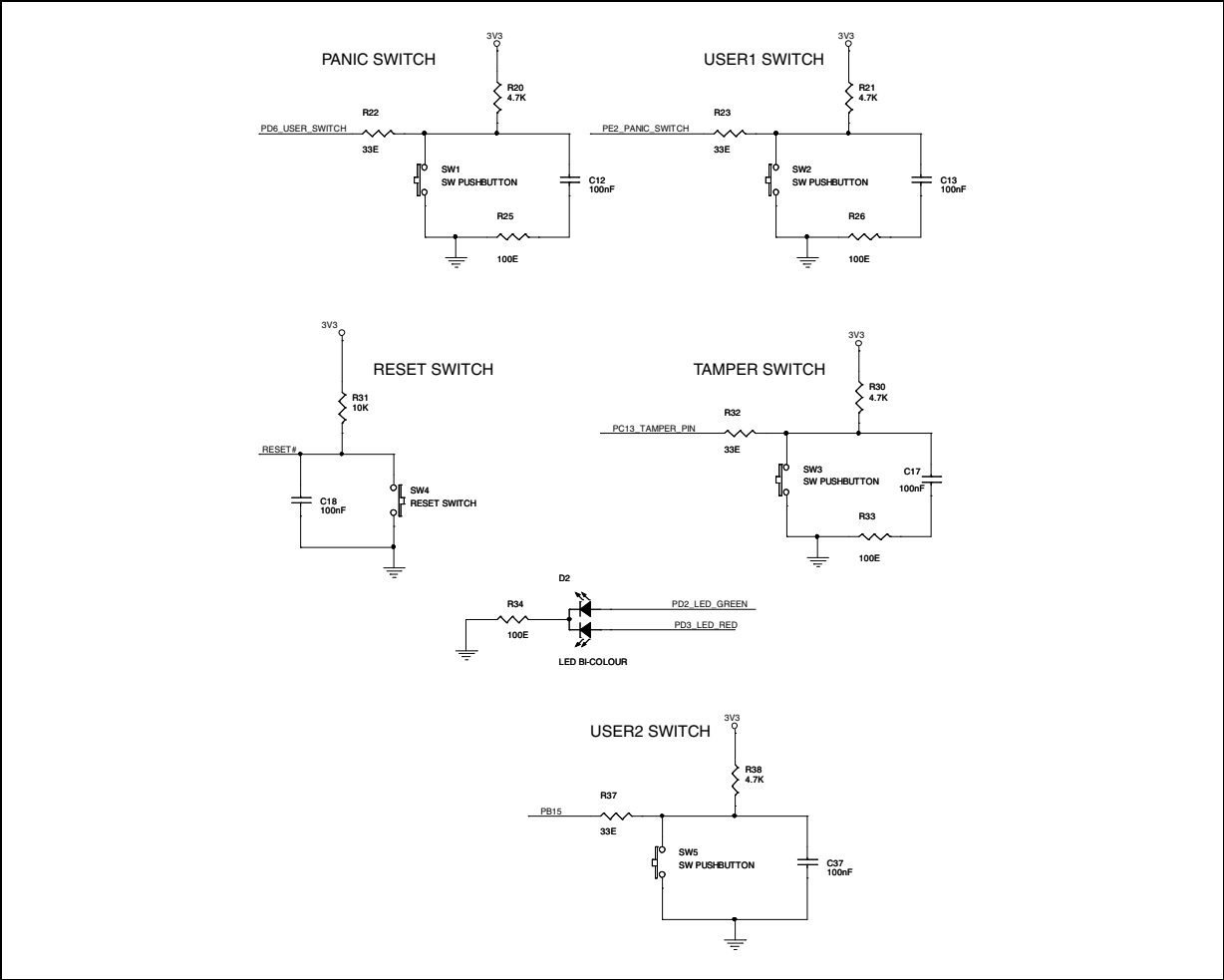


Figure 11. SWD section

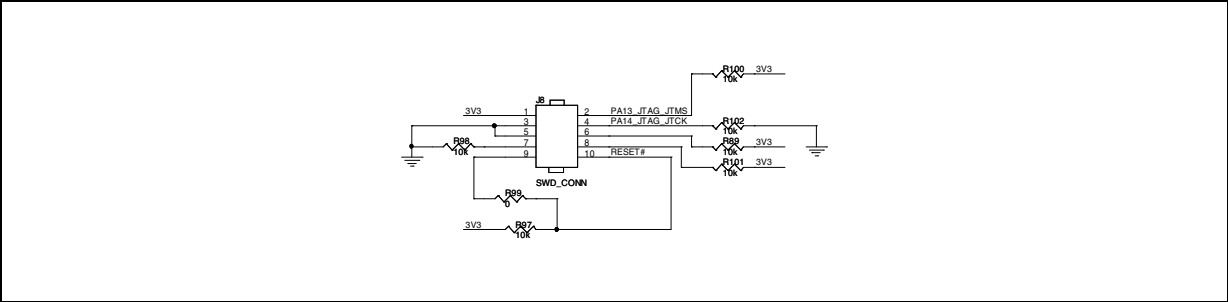


Figure 12. 5 V regulator

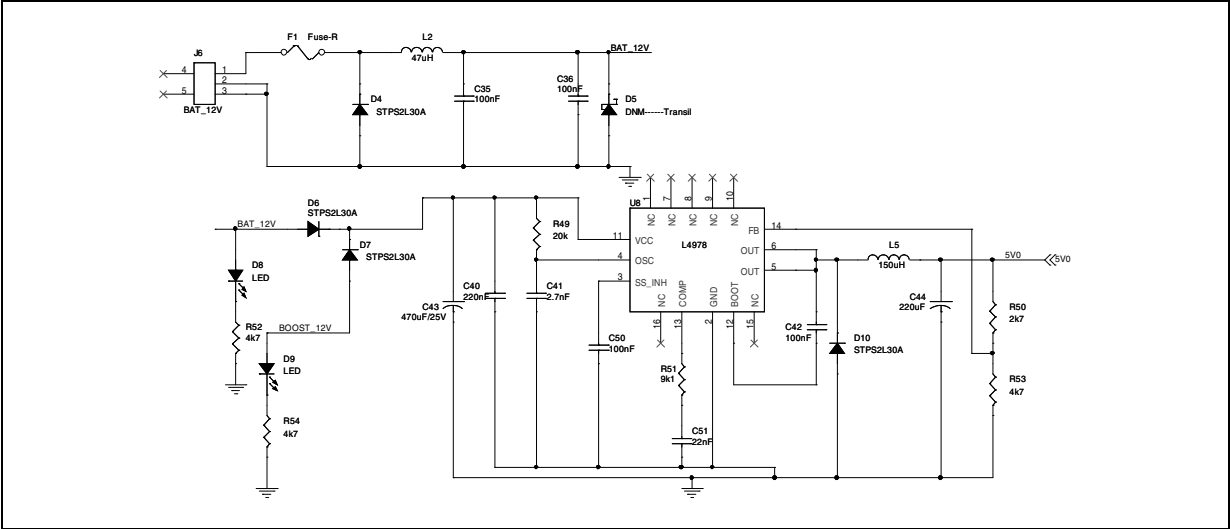


Figure 13. L6924D battery charger

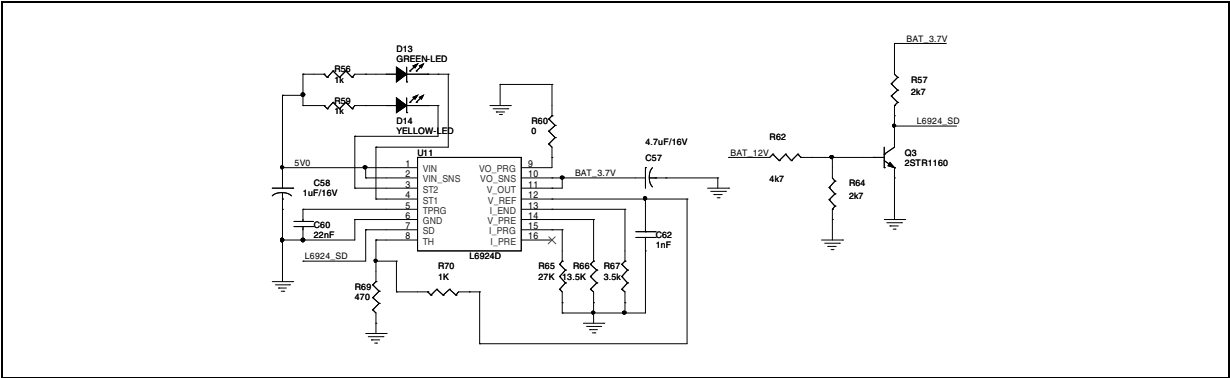


Figure 14. Voltage regulators

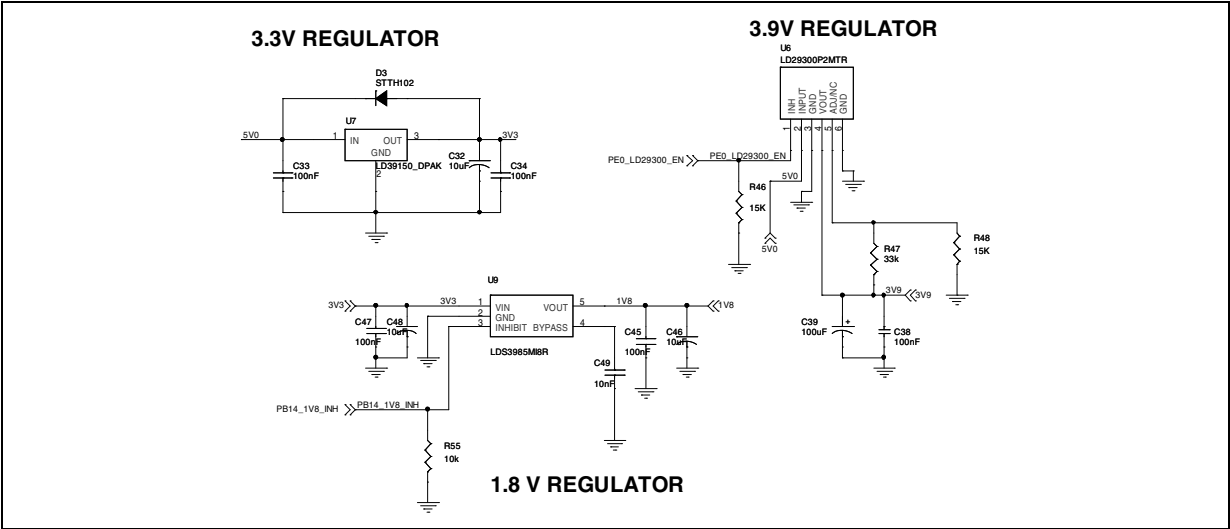


Figure 15. Boost using STLDC08

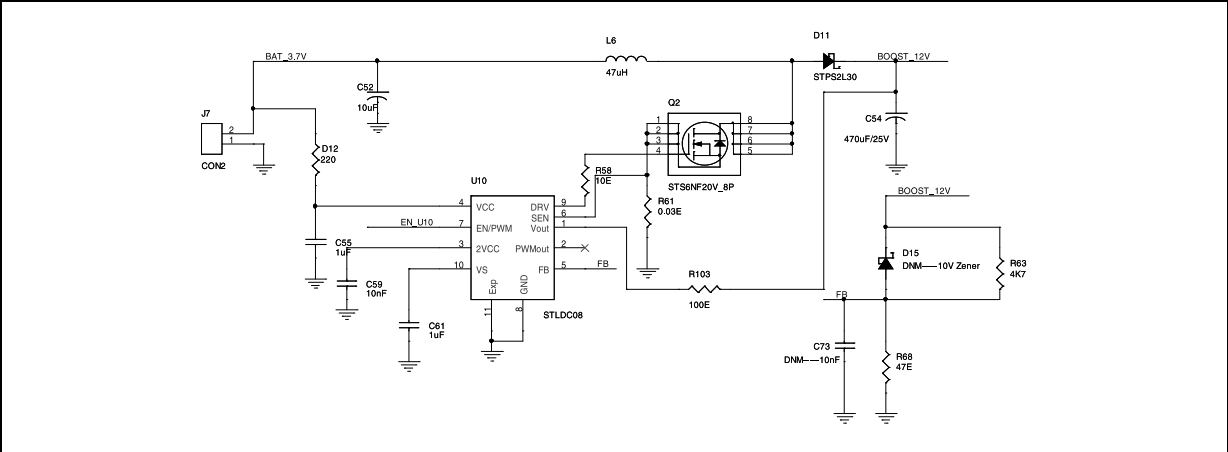


Figure 16. Battery protection and boost/charger synchronization

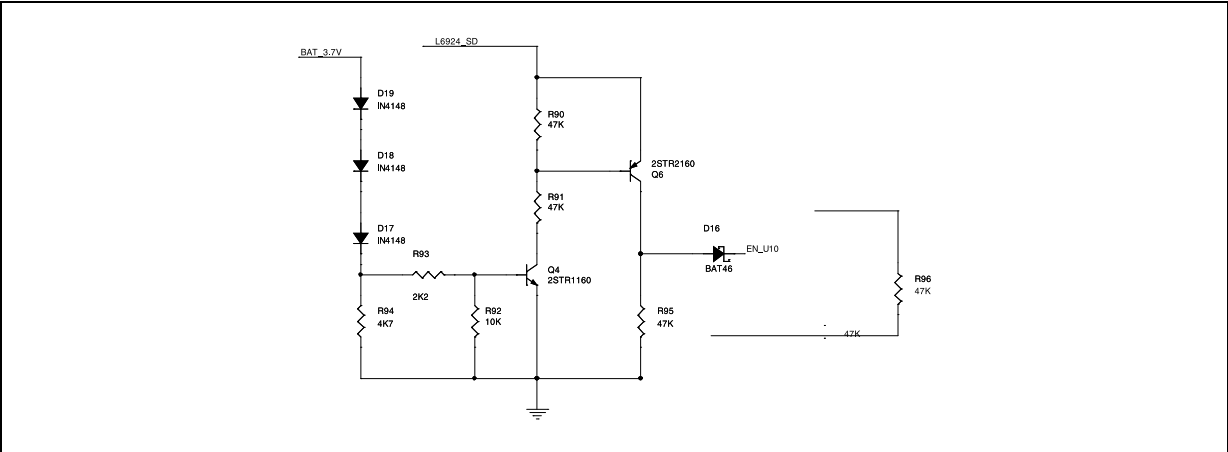
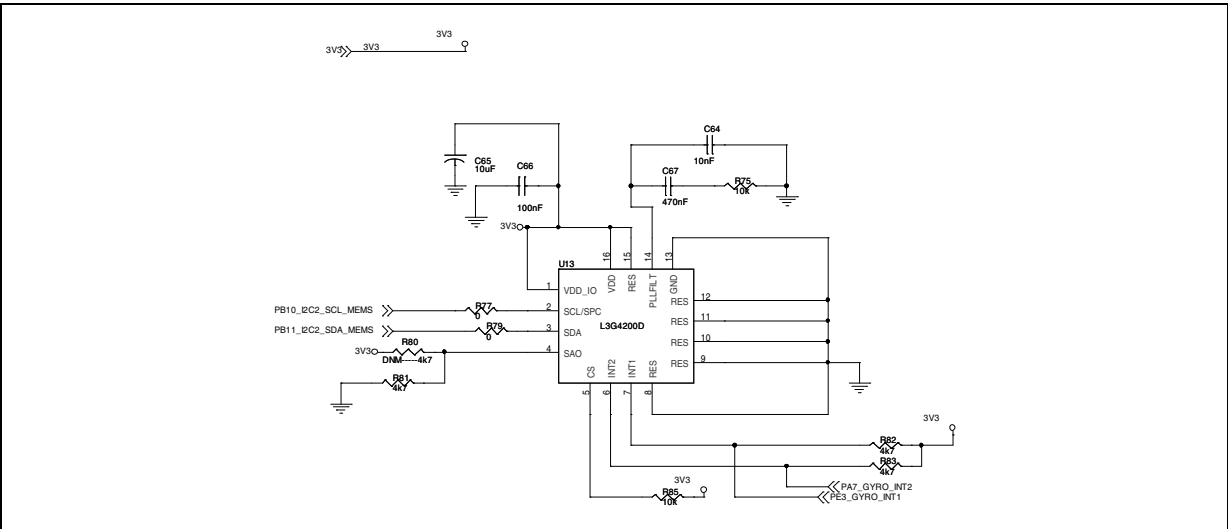


Figure 17. Gyroscope section



The schematic shows the I2C connection between the STM32F751 microcontroller (U2) and the PC0-TS module. The STM32F751 pins are: 1 (ADDR/THERM), 2 (GND), 3 (VDD), 4 (SCL), 5 (EVENT), and 6 (SCL). The PC0-TS module pins are: 6 (PB9_I2C1_SDA_TS), 5 (PB9_I2C1_SDA_TS), 4 (PB8_I2C1_SCL_TS), and 3 (PC0_TS_INT). The connections are: ADDR/THERM to PB9_I2C1_SDA_TS via resistor R72 (12k); GND to GND; VDD to VDD; SCL to PB8_I2C1_SCL_TS via resistor R73 (1k); EVENT to PB9_I2C1_SDA_TS via resistor R74 (1k); and SCL to PC0_TS_INT via resistor R71 (4k7). A 100nF capacitor (C63) is connected between VDD and GND.