

# M.2 Serial Memory QSPI

MB1928 18LA

## Table of contents

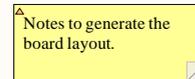
- Sheet 1: Project overview (this page)
- Sheet 2: Top
- Sheet 3: Peripherals
- Sheet 4: M.2 Memory QuadSPI

## Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.



## Open Platform License Agreement

The Open Platform License Agreement (“Agreement”) is a binding legal contract between you (“You”) and STMicroelectronics International N.V. (“ST”), a company incorporated under the laws of the Netherlands acting for the purpose of this Agreement through its Swiss branch 39, Chemin du Champ des Filles, 1228 Plan-les-Ouates, Geneva, Switzerland.

By using the enclosed reference designs, schematics, PC board layouts, and documentation, in hardcopy or CAD tool file format (collectively, the “Reference Material”), You are agreeing to be bound by the terms and conditions of this Agreement. Do not use the Reference Material until You have read and agreed to this Agreement terms and conditions. The use of the Reference Material automatically implies the acceptance of the Agreement terms and conditions.

The complete Open Platform License Agreement can be found on [www.st.com/opla](http://www.st.com/opla).

HW1

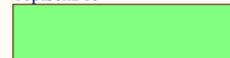
PCB

MB1928

HW2

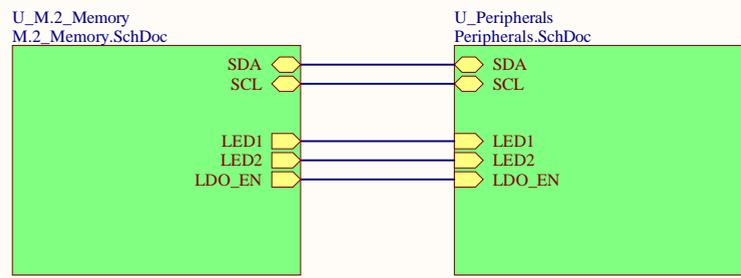
STICKER PRODUCT

U\_Top  
Top.SchDoc

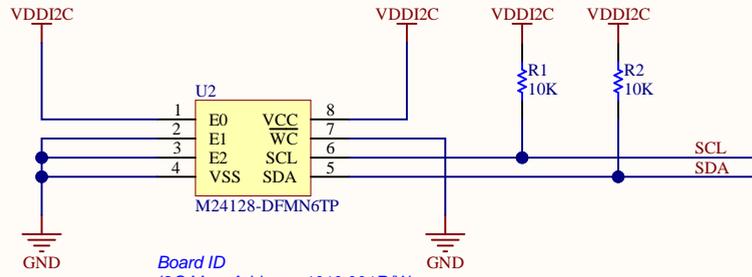


Title: <b>Project overview</b>		
Project: <b>M.2 Serial Memory QSPI</b>		
Variant: 18LA		
Revision: C-01		Reference: MB1928
Size: A4	Date: 22-MAY-25	Sheet: 1 of 4

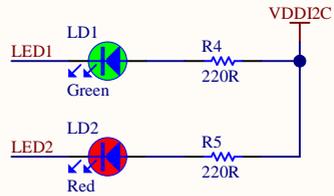




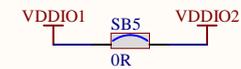
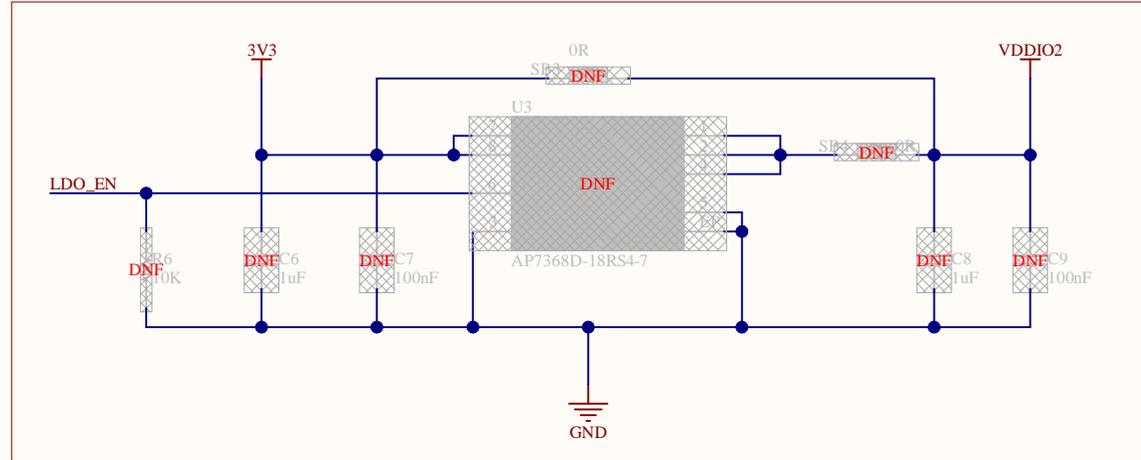
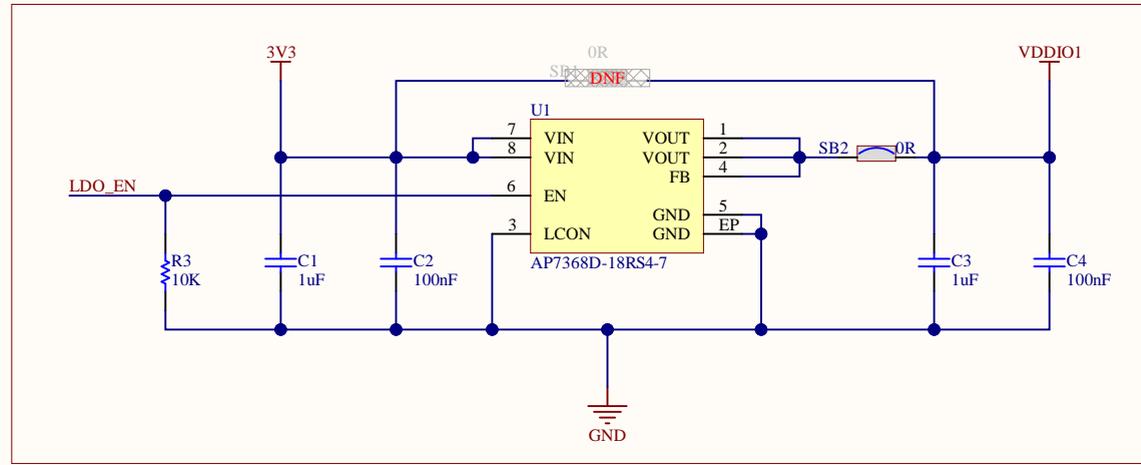
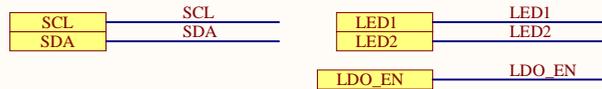
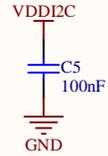
### Memory, EEPROM



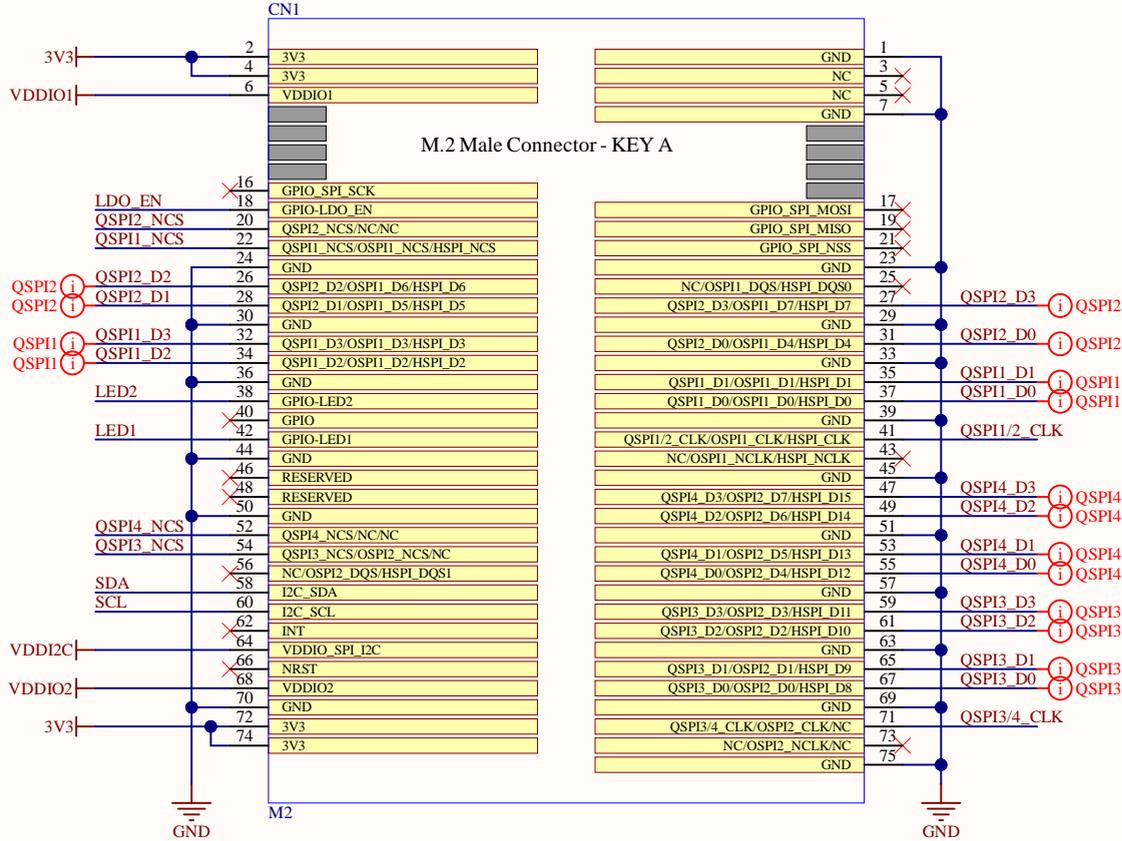
Board ID  
 I2C Mem Address: 1010 001R/W  
 I2C Id Address: 1011 001R/W



Decoupling cap U2



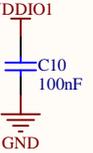
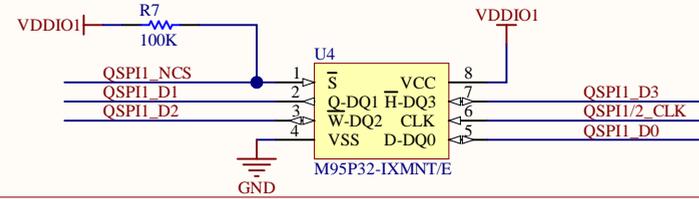
## M.2 Connector



## QUADSPI\_1 FLASH

Operating range: 1.62<VDD<1.98V (1.8V type)  
Operating range: 2.7<VDD<3.6V (3.3V type)

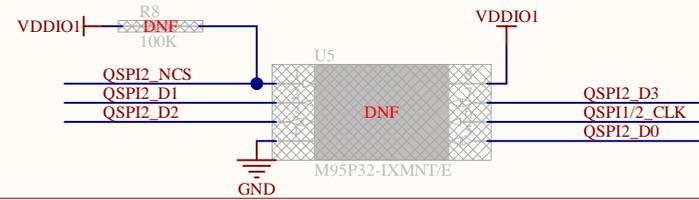
▲ QuadSPI bus should be routed in 50 ohm +/- 15%



## QUADSPI\_2 FLASH

Operating range: 1.62<VDD<1.98V (1.8V type)  
Operating range: 2.7<VDD<3.6V (3.3V type)

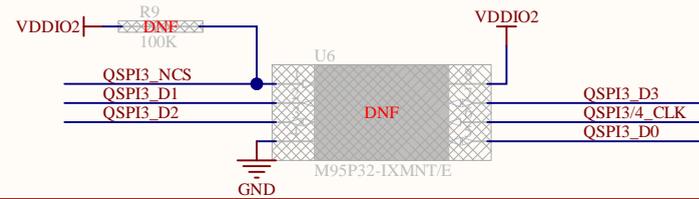
▲ QuadSPI bus should be routed in 50 ohm +/- 15%



## QUADSPI\_3 FLASH

Operating range: 1.62<VDD<1.98V (1.8V type)  
Operating range: 2.7<VDD<3.6V (3.3V type)

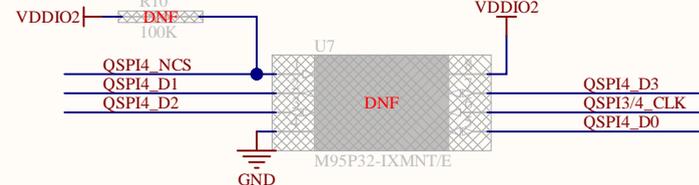
▲ QuadSPI bus should be routed in 50 ohm +/- 15%

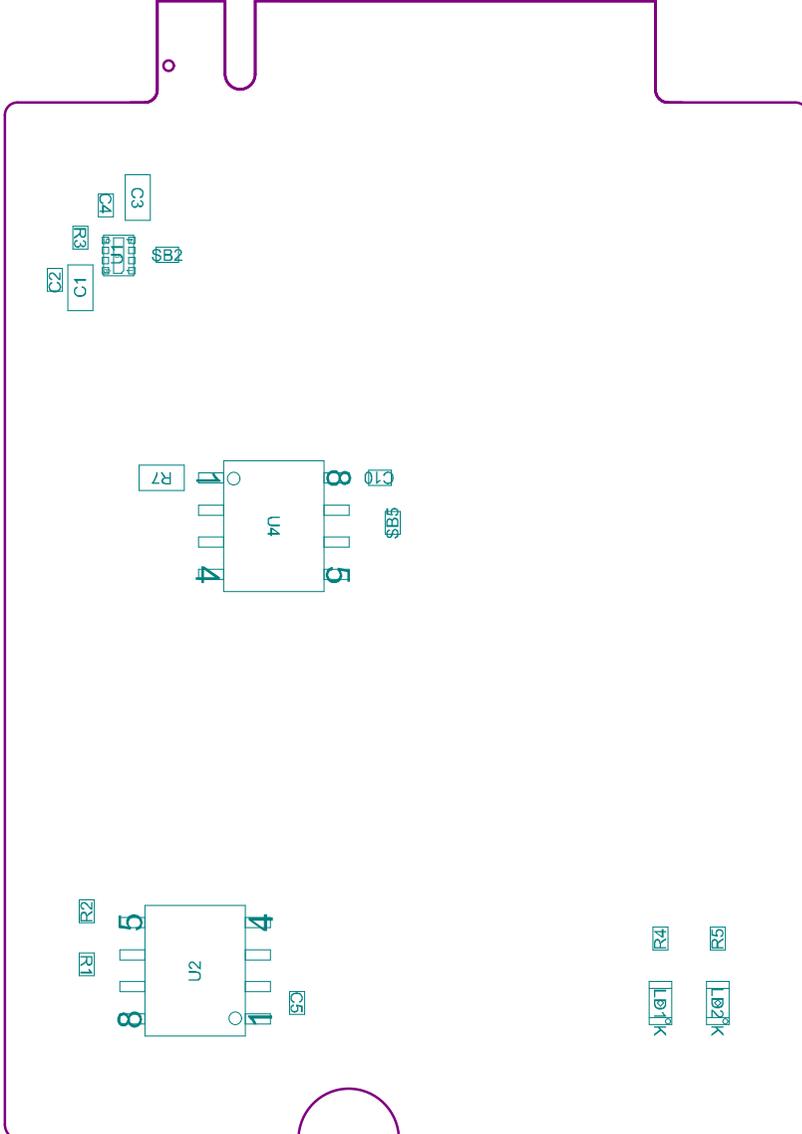


## QUADSPI\_4 FLASH

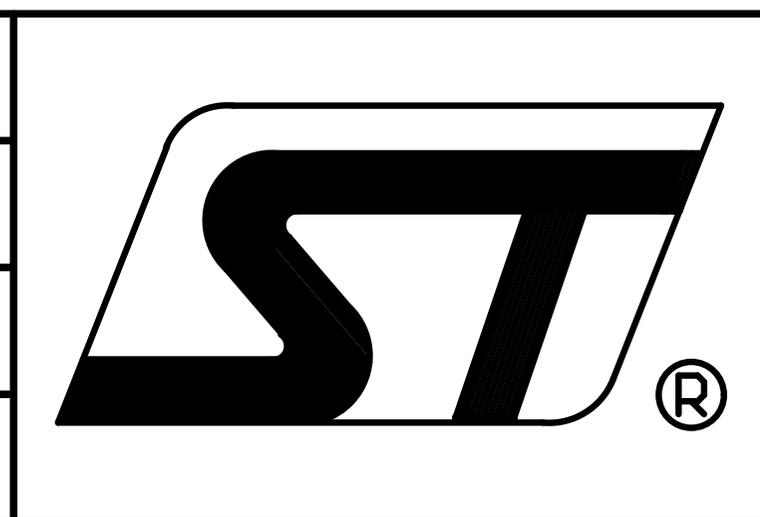
Operating range: 1.62<VDD<1.98V (1.8V type)  
Operating range: 2.7<VDD<3.6V (3.3V type)

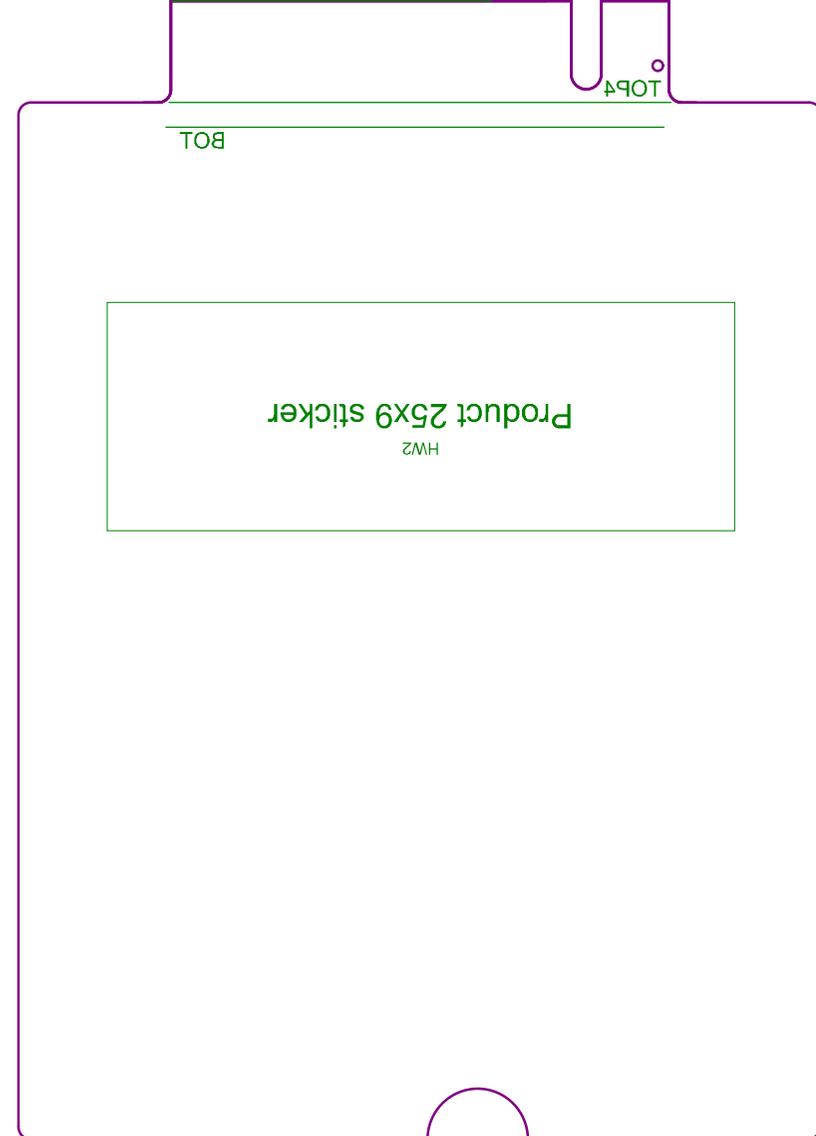
▲ QuadSPI bus should be routed in 50 ohm +/- 15%





Project: M.2 Serial Memory QSPI	
Layer: M14-Top Assembly	Gerber: .GM14
Variant: 18LA	MB1928
Date: 22-MAY-25	Rev: C





Project: M.2 Serial Memory QSPI	
Layer: M15-Bottom Assembly	Gerber: .GM15
Variant: 18LA	MB1928
Date: 22-MAY-25	Rev: C



