

NUCLEO-64 - Mezzanine Board

MB1801

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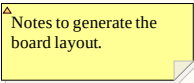
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- Sheet 5: Power Management
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Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.



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Top
Top.SchDoc



HW1



MB1801

HW2

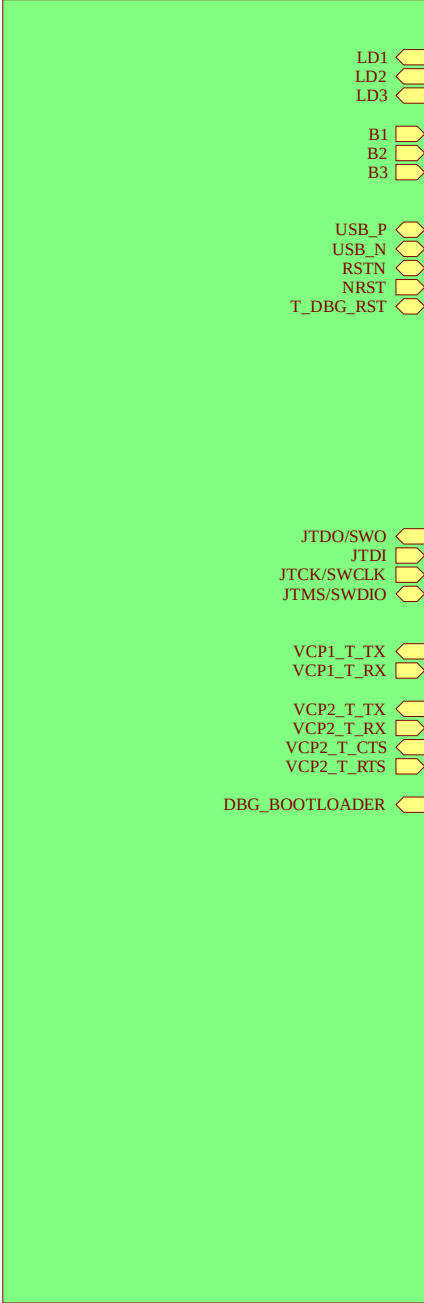


Sticker board

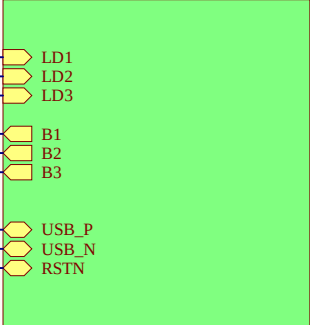
Title: Project overview		
Project: NUCLEO-64 - Mezzanine Board		
Variant: USB		
Revision: D-01		Reference: MB1801
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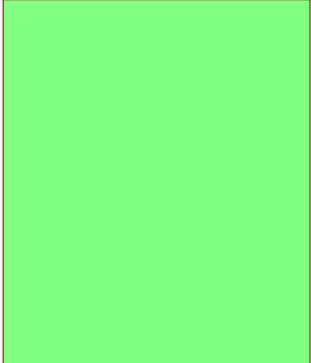
Extension Connectors



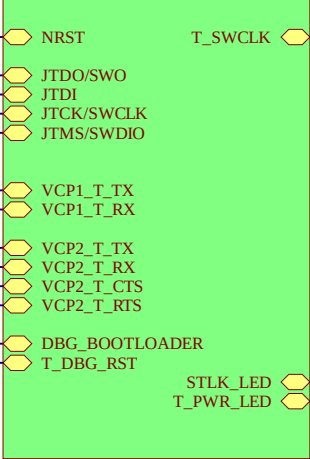
LEDs & Push Buttons & USB-C Connector



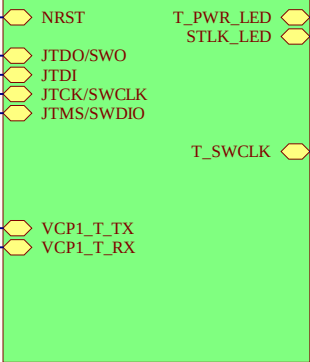
Power Management

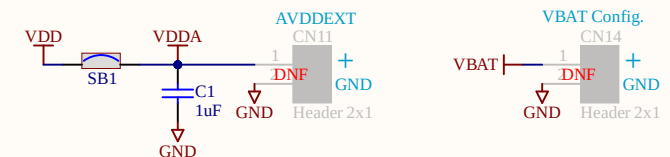
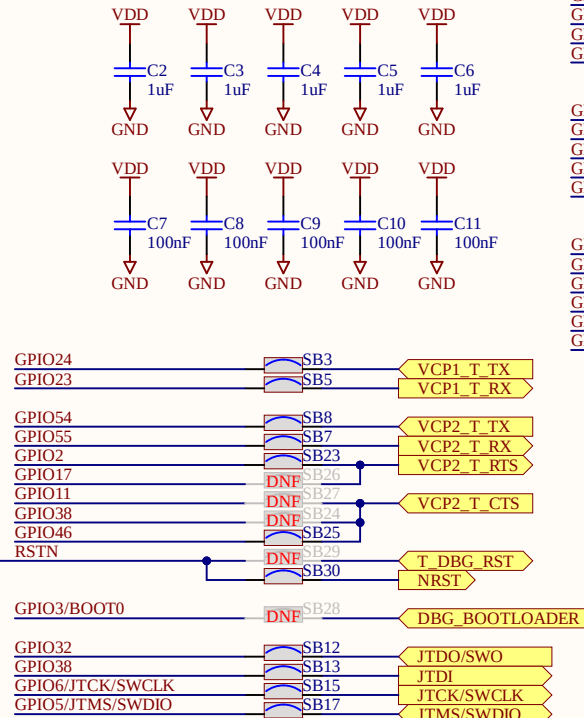
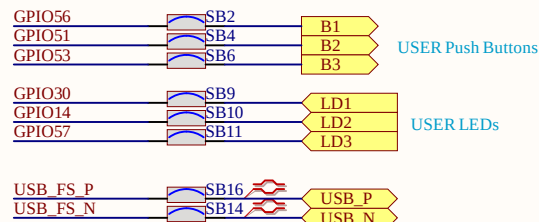
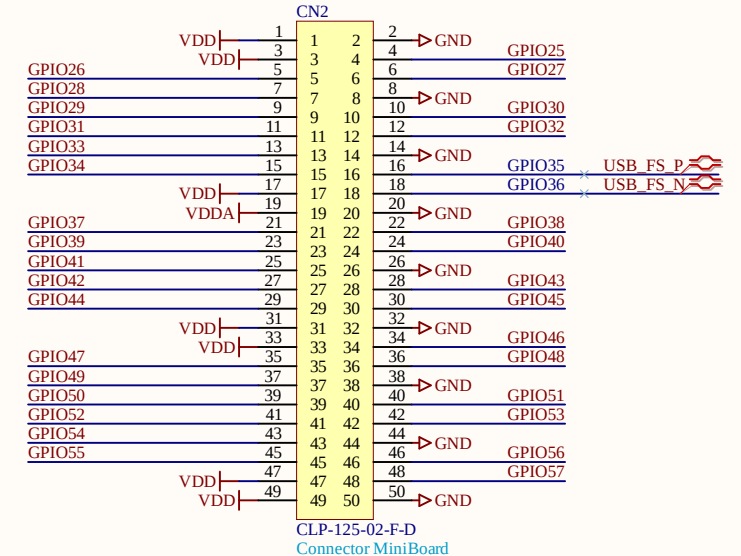
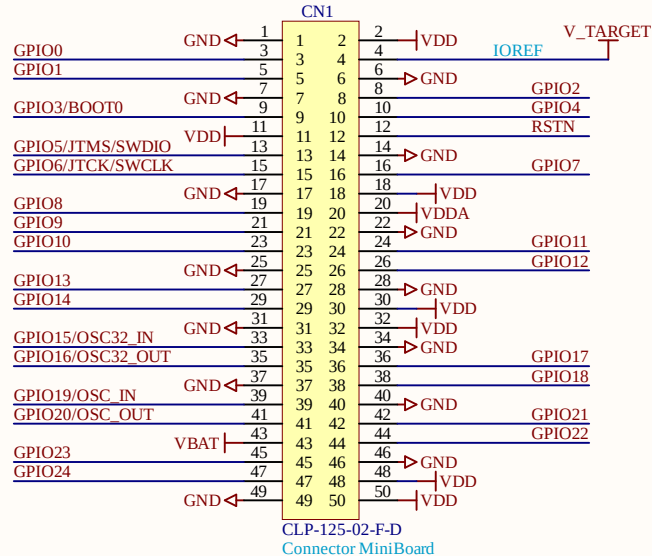
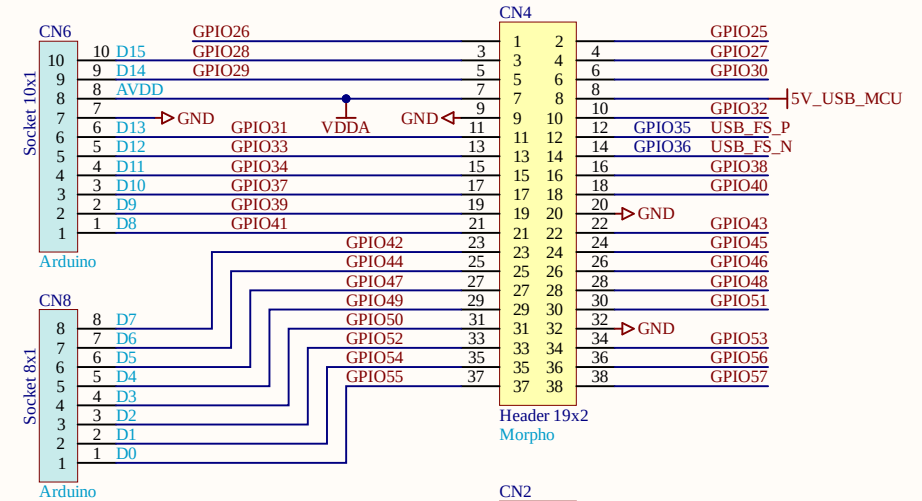
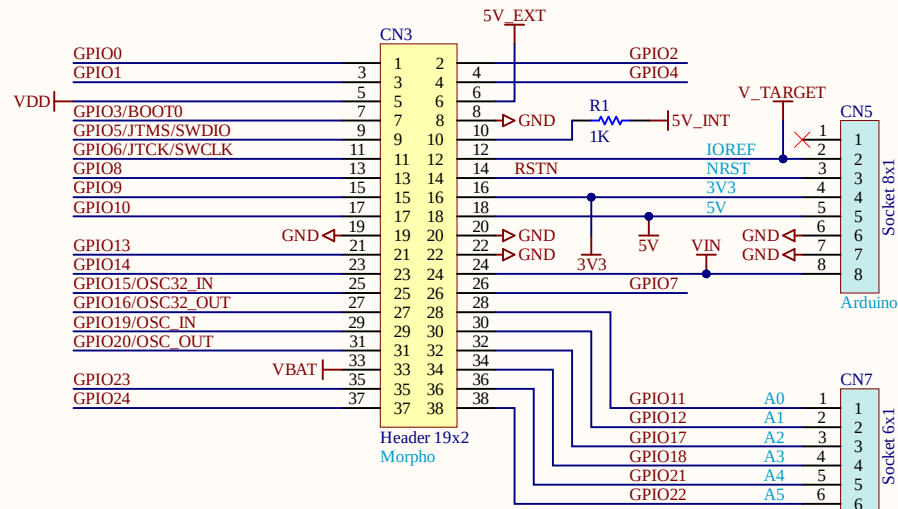


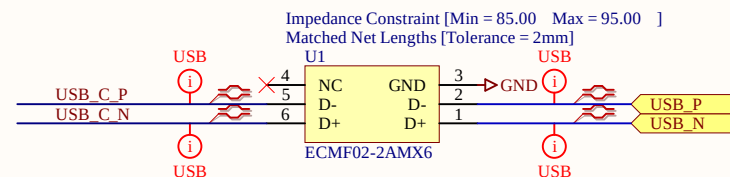
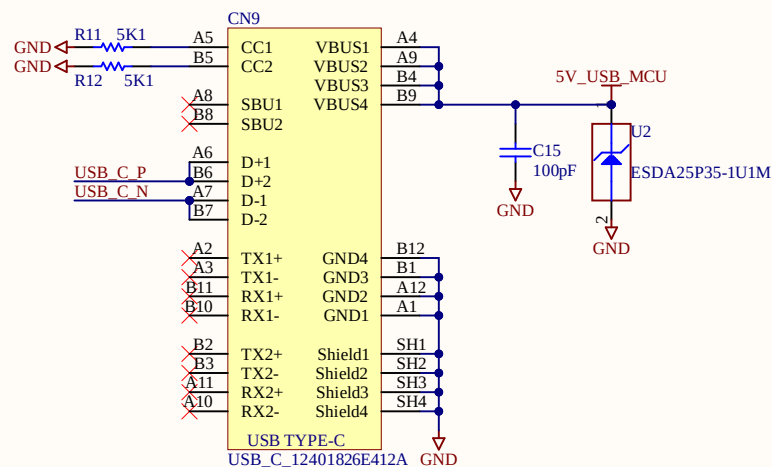
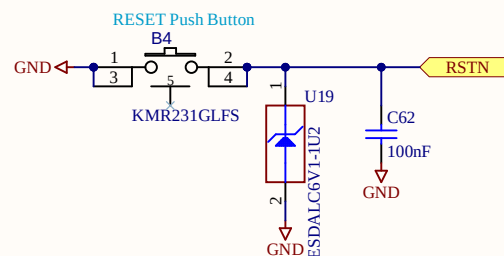
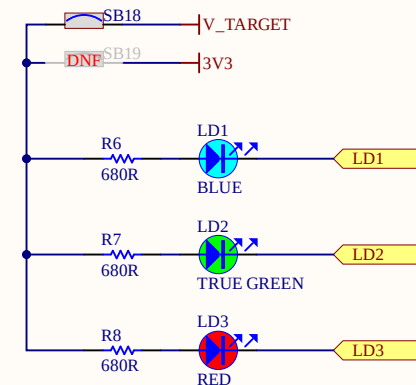
U_STlink-V3EC
STlink-V3EC.SchDoc



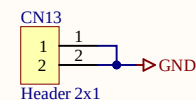
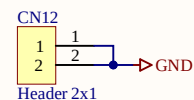
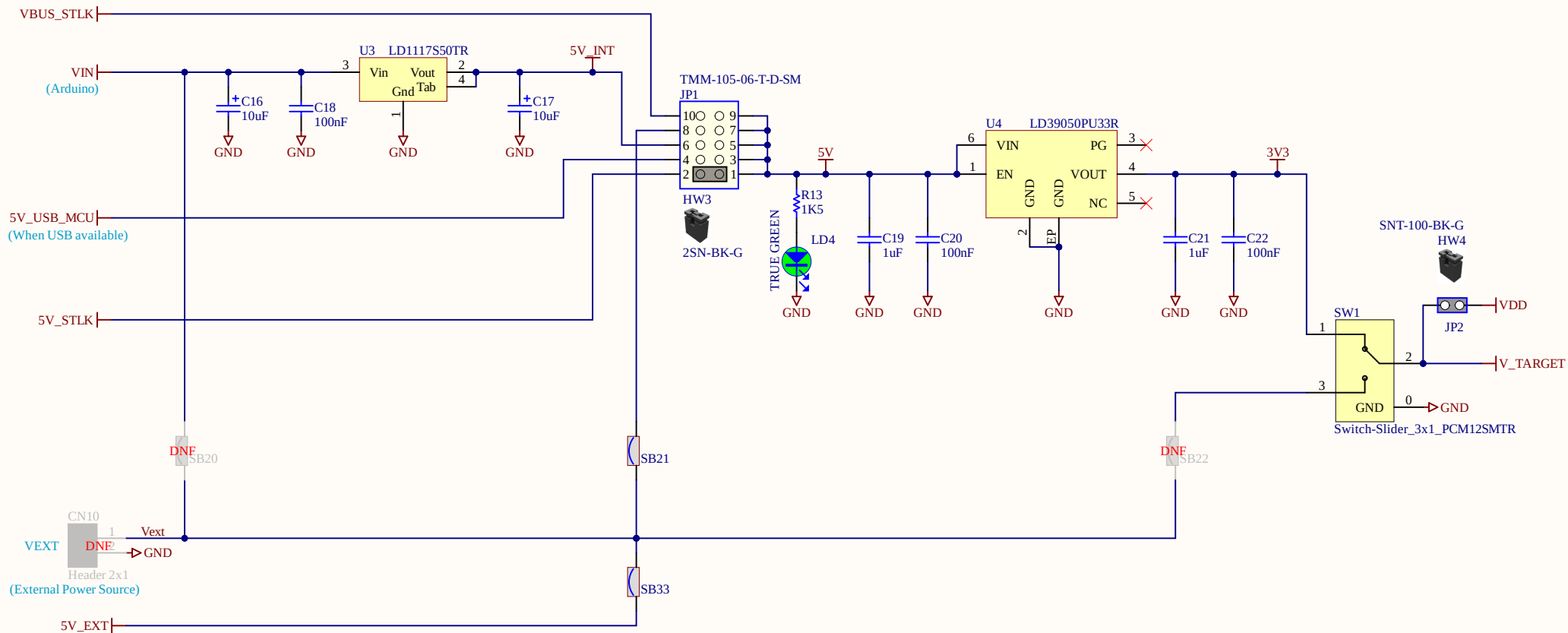
U_External_Debug_Interface
External_Debug_Interface.SchDoc







Fitted and Used only when the USB is available on the MCU



Title: Power Management		
Project: NUCLEO-64 - Mezzanine Board		
Variant: USB		
Revision: D-01	Reference: MB1801	
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[illegible]

5V_STLK OVERCURRENT PROTECTION MANAGEMENT

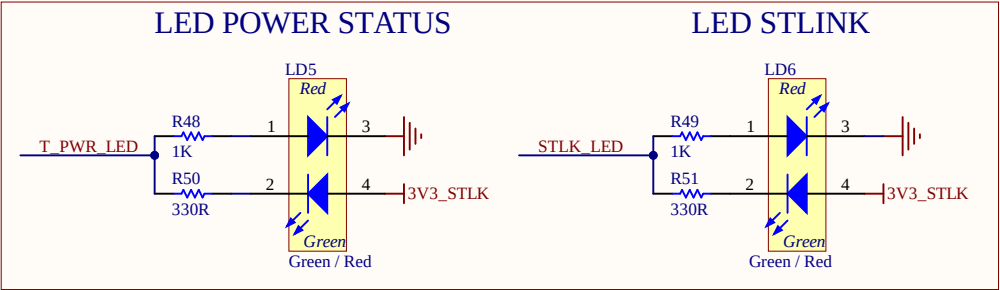
Hi-Z = IO set in high impedance

(1): NOT FITTED FOR DEBUG WITH INTERNAL STLINK (DEFAULT)
 (1): FITTED FOR DEBUG WITH EXTERNAL DEBUGGER
 Or output STLINK IOs isolated (Hi-Z)

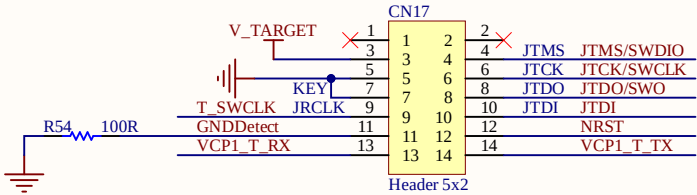
[illegible][illegible]

Title: STLINK-V3EC		
Project: NUCLEO-64 - Mezzanine Board		
Variant: USB		
Revision: D-01		Reference: MB1801
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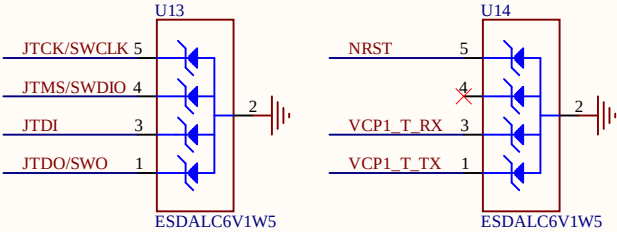




STDC14 RECEIVER



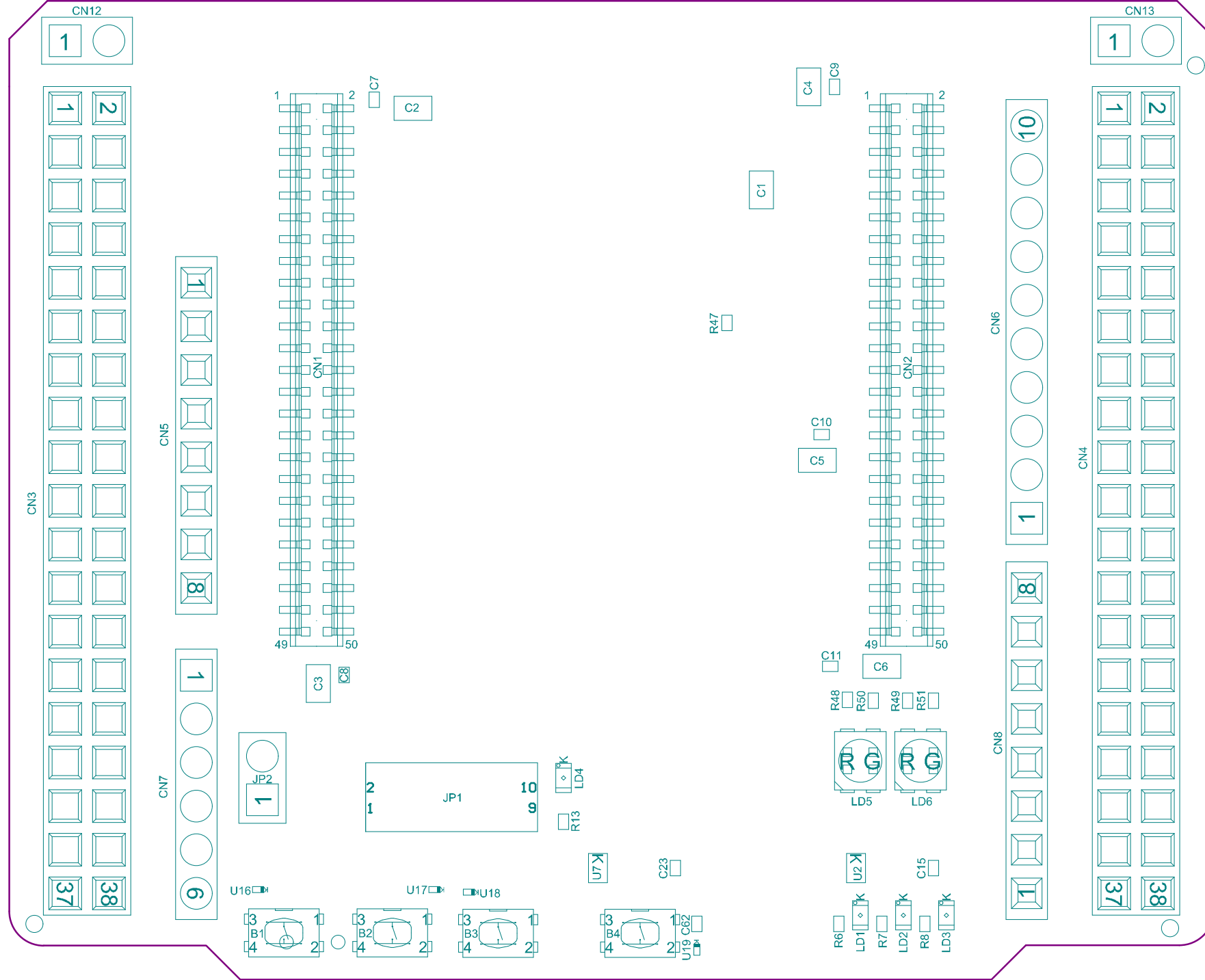
ESD PROTECTIONS



JTMS/SWDIO	JTMS/SWDIO
JTCK/SWCLK	JTCK/SWCLK
JTDO/SWO	JTDO/SWO
JTDI	JTDI
T_SWCLK	T_SWCLK
VCP1_T_TX	VCP1_T_TX
VCP1_T_RX	VCP1_T_RX
NRST	NRST
STLK_LED	STLK_LED
T_PWR_LED	T_PWR_LED



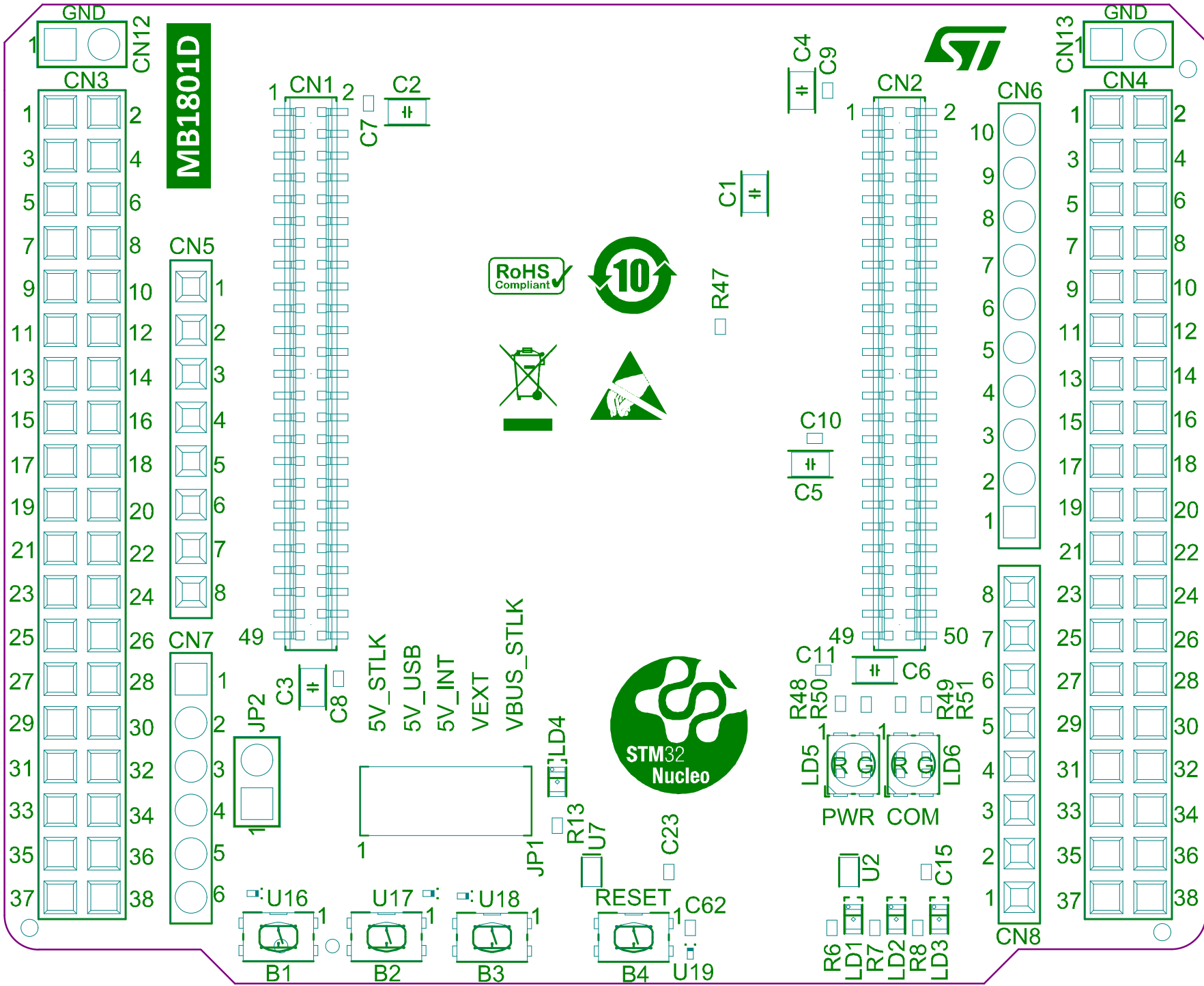
Project: NUCLEO-64 - Mezzanine Board		
Layer: M14-Top Assembly	Gerber: .GM14	
Variant: USB	Ref: MB1801	
Date: 11-JAN-2024	Rev: D	





Top Overlay

.GTO



Bottom Overlay

.GBO