

STM32WL-QFN48-2Layers_Reference_Board

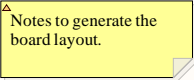
MB1791

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- Sheet 1: Project overview (this page)
- Sheet 2: STM32WL_BGA73_Ref_Board_with_TCXO.SchDoc

Legend

- General comment such as function title, configuration, ...
- Text to be added to silkscreen.
- Warning text.



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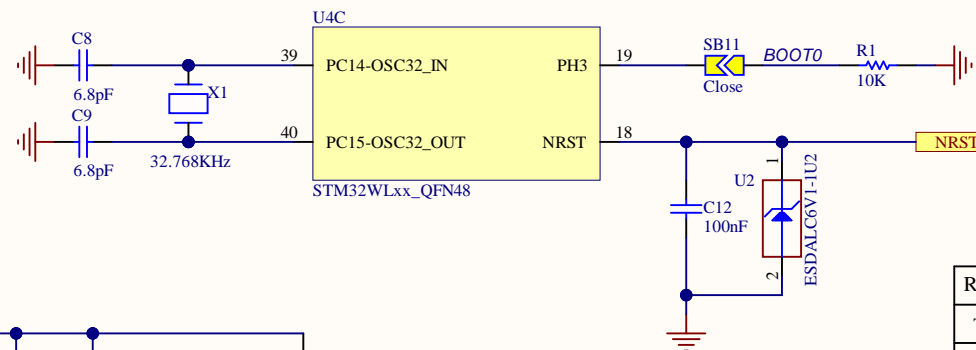
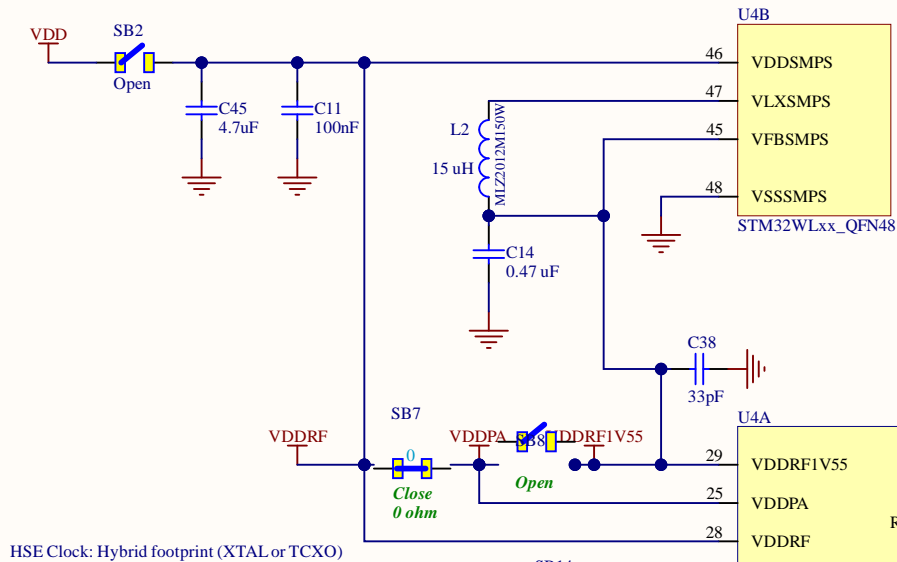
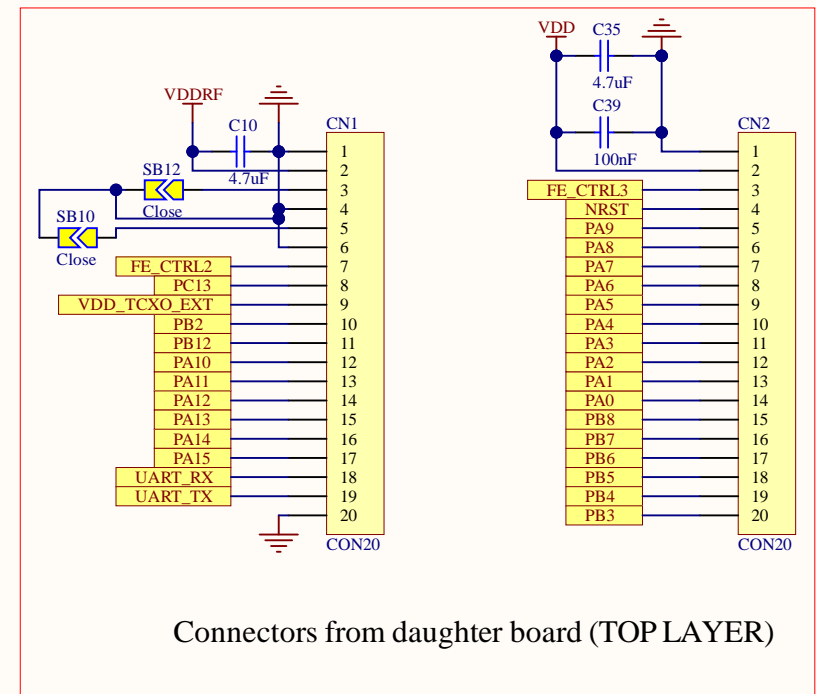
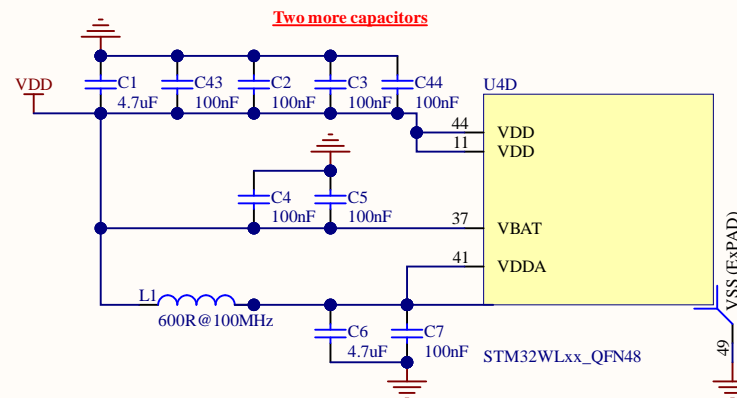
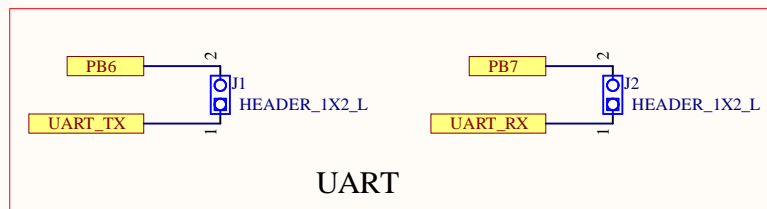
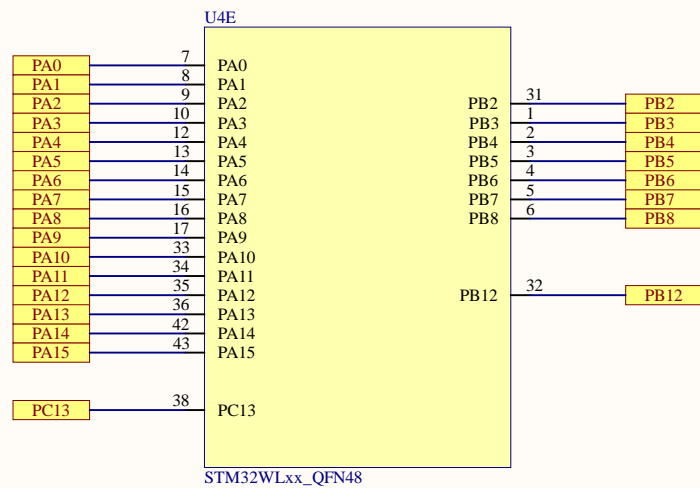
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Reference_Design_STM32WL_2L_RF_SW.SchDoc

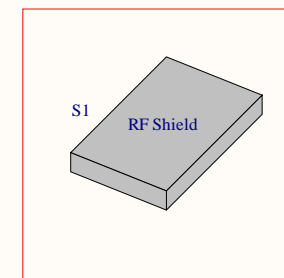
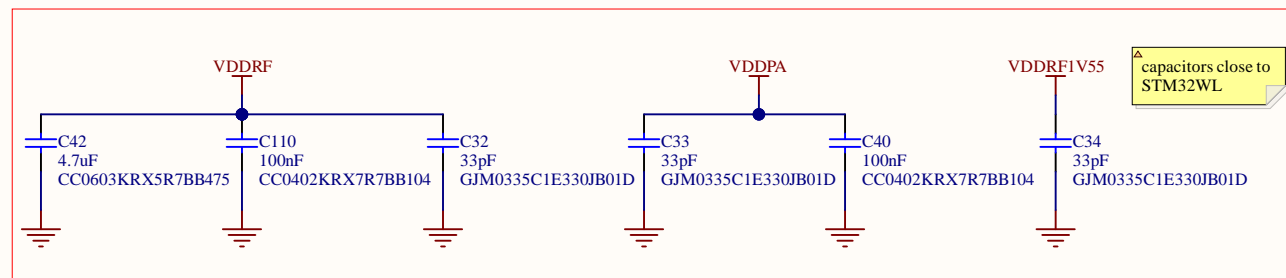
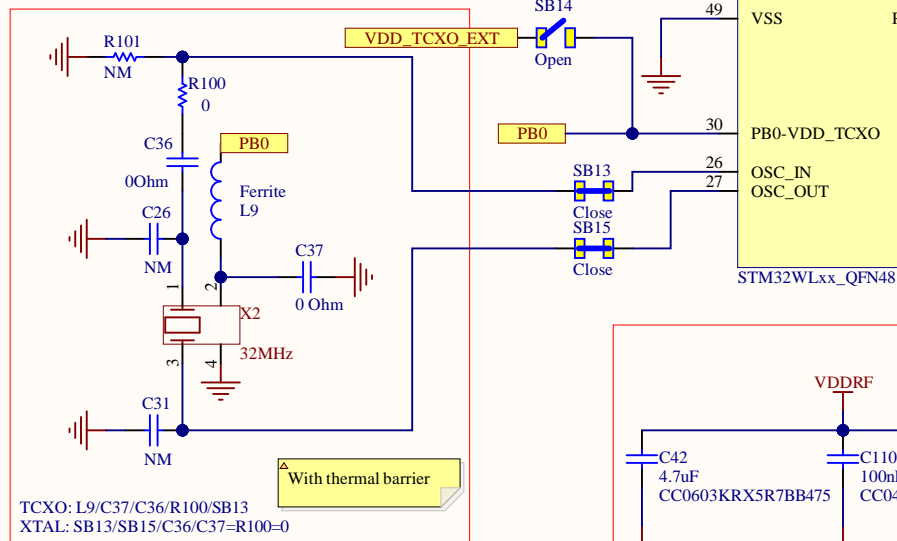


Title: Schematic sheet title to update		
Project: STM32WL-QFN48-2Layers_Reference_Board		
Variant: LowBand_HighPower		
Revision: A-01	Reference: MB1791	
Size: A4	Date: 4-4-2022	Sheet: 1 of 2





HSE Clock: Hybrid footprint (XTAL or TCXO)

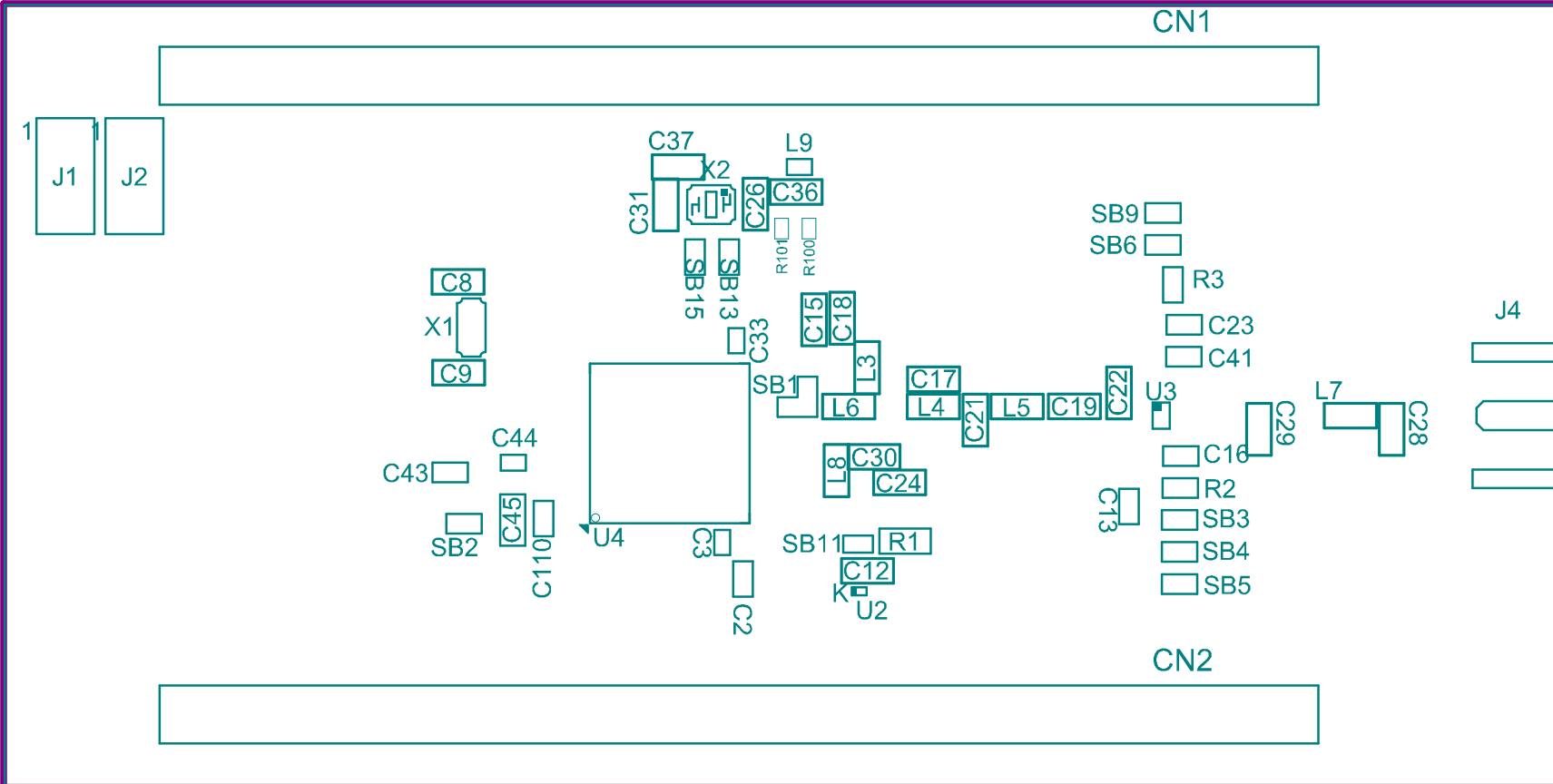



Notes:

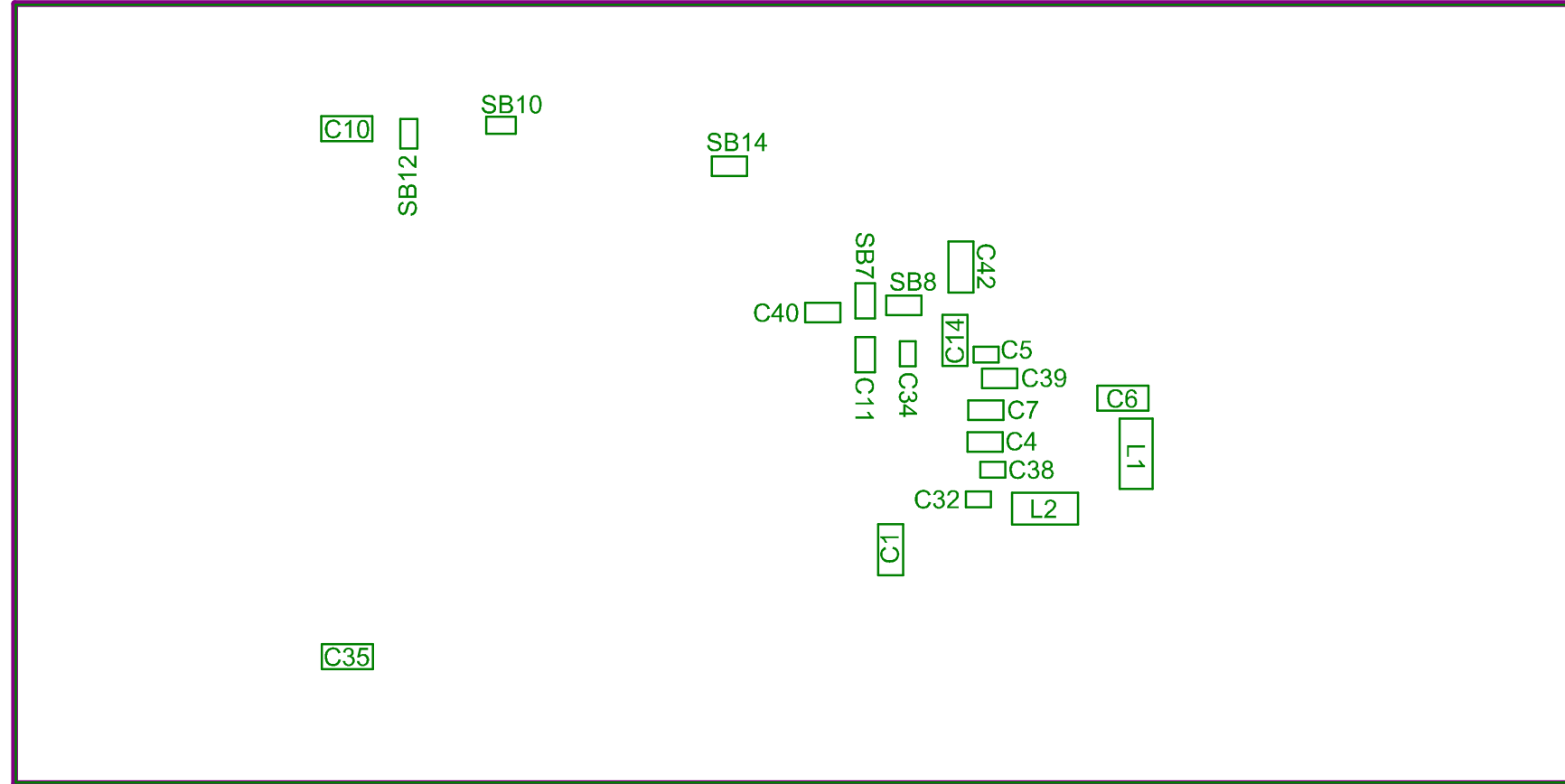
RF BoM LowBand_HighPower


RF components:

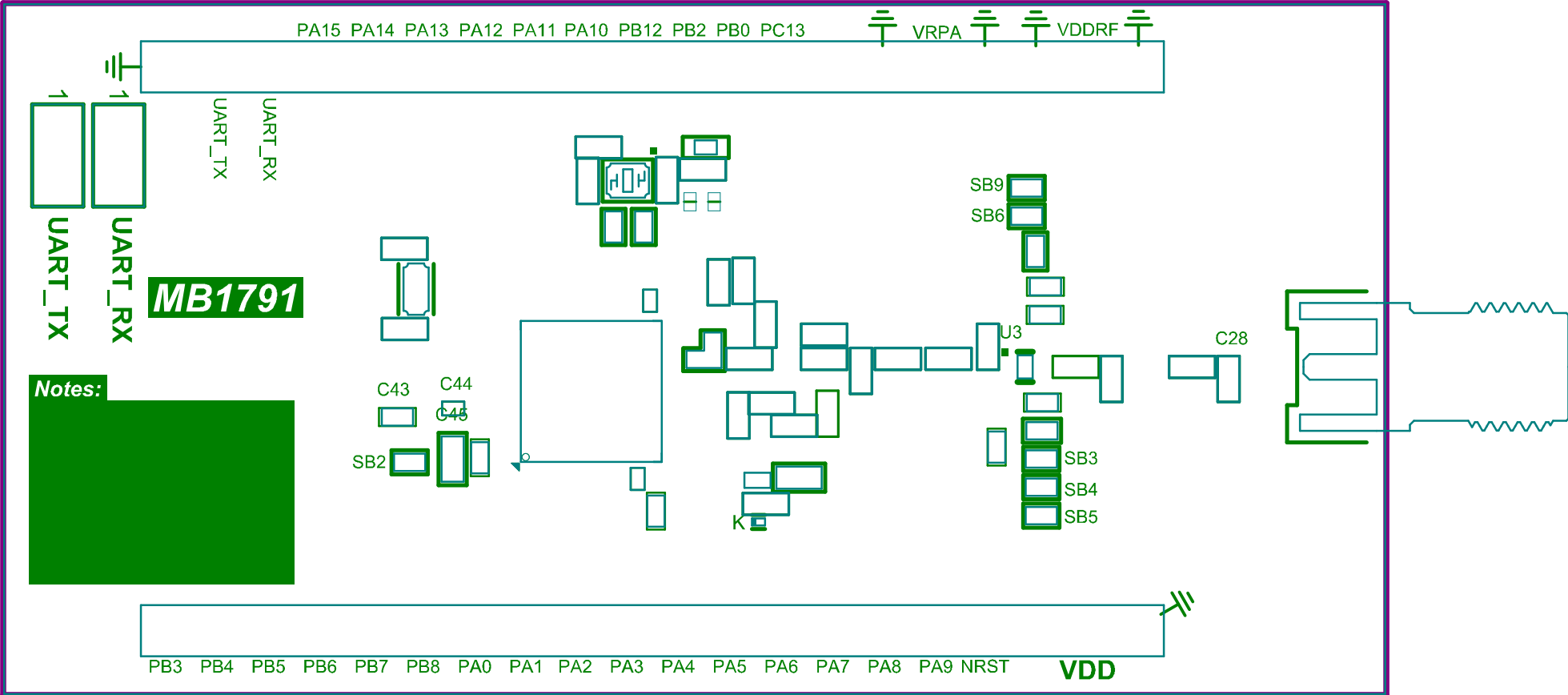
- RF High-Q muRata GJM15 series for capacitors
- RF High-Q muRata LQW15AN series for inductors



Project: STM32WL-QFN48-2Layers_Reference_Board		
Layer: Top Assembly	Gerber: .GM14	
Variant: LowBand_HighPower	Ref: MB1791	
Date: 4-4-2022	Rev: A	

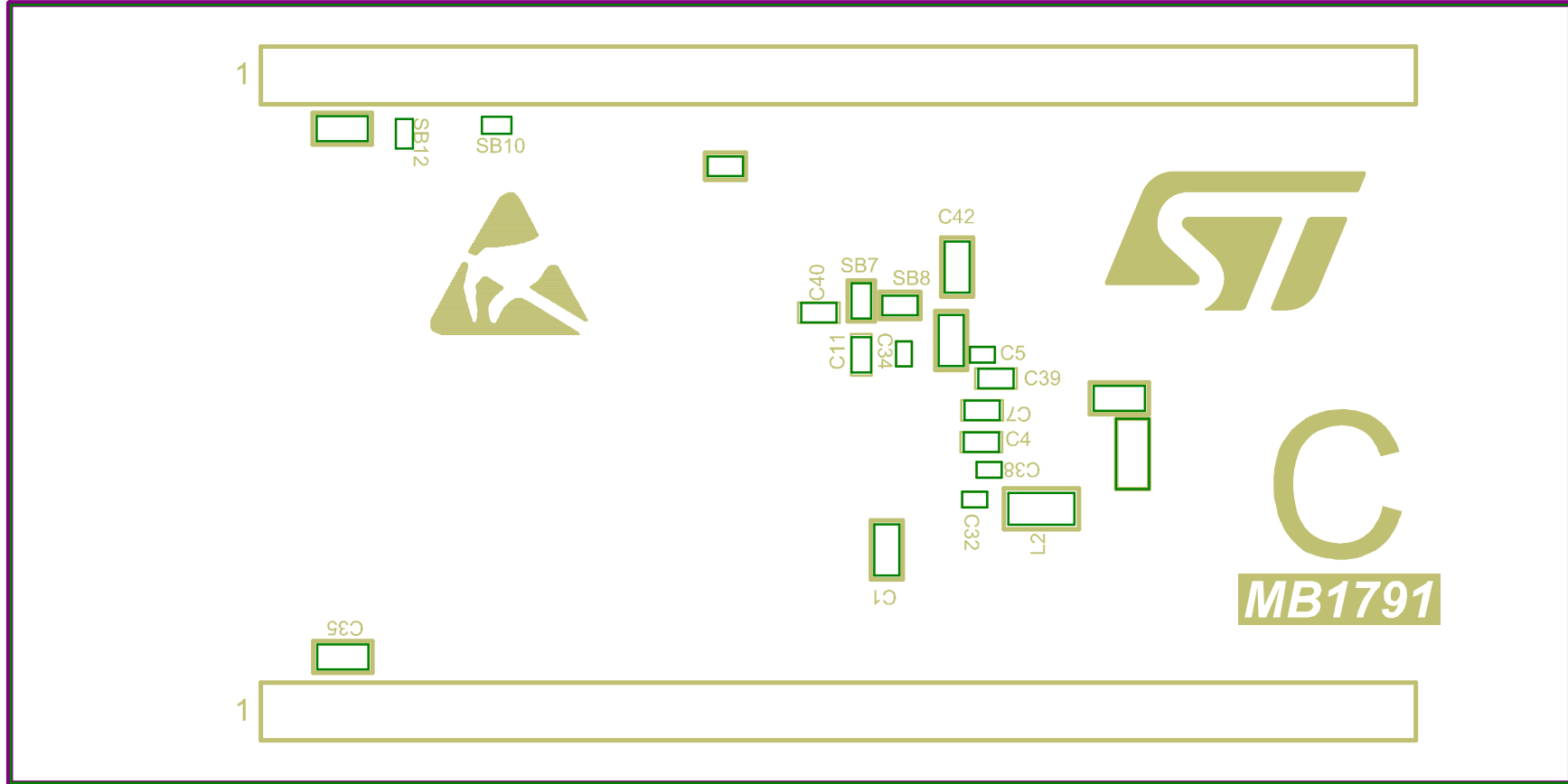


Project: STM32WL-QFN48-2Layers_Reference_Board		
Layer: Bottom Assembly	Gerber: .GM15	
Variant: LowBand_HighPower	Ref: MB1791	
Date: 4-4-2022	Rev: A	



Top Overlay

.GTO



Bottom Overlay

.GBO