

NUCLEO-144 LEGACY

MB1404

H563ZI

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U_Top
Top.SchDoc

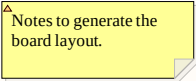


Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.



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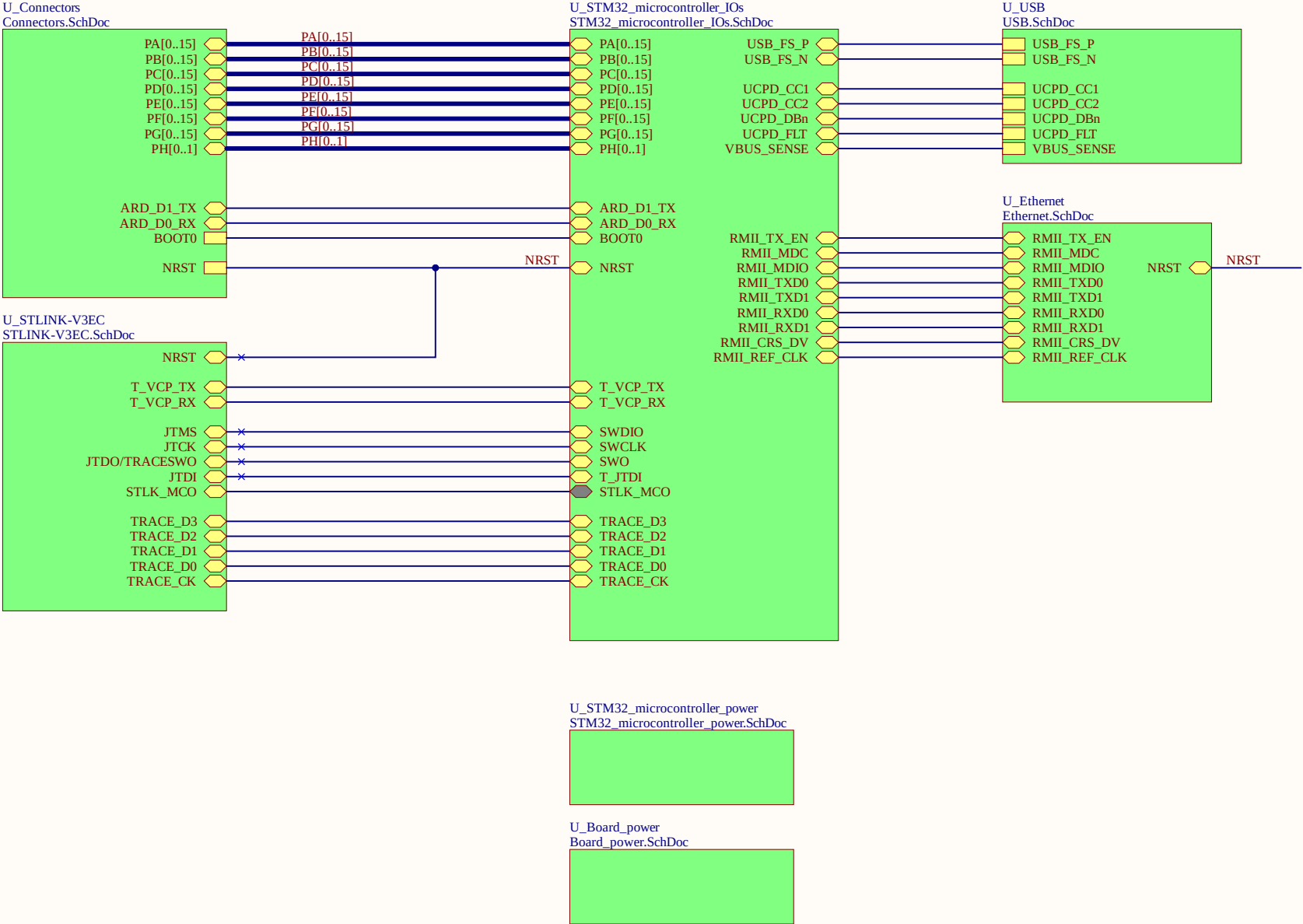
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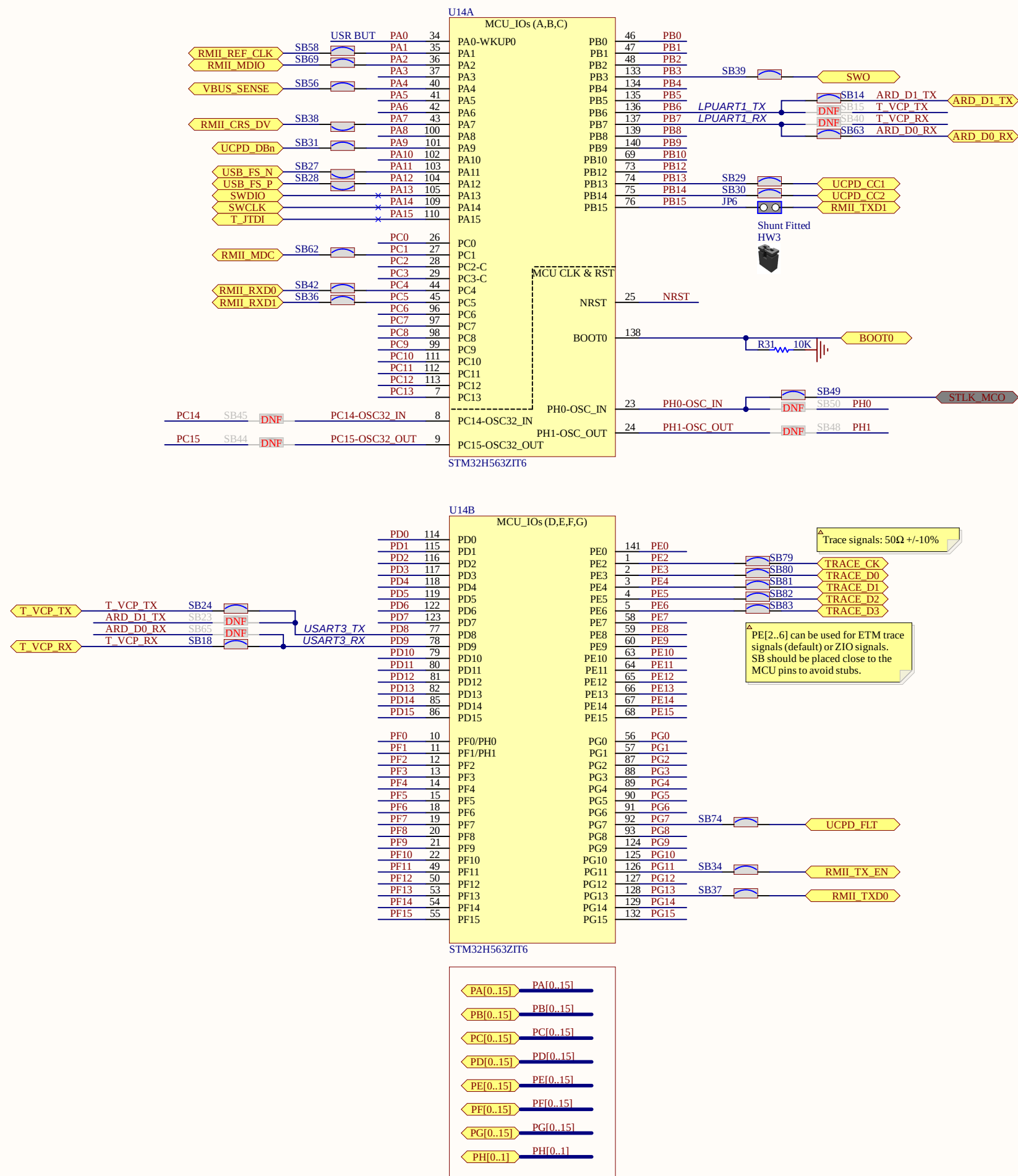
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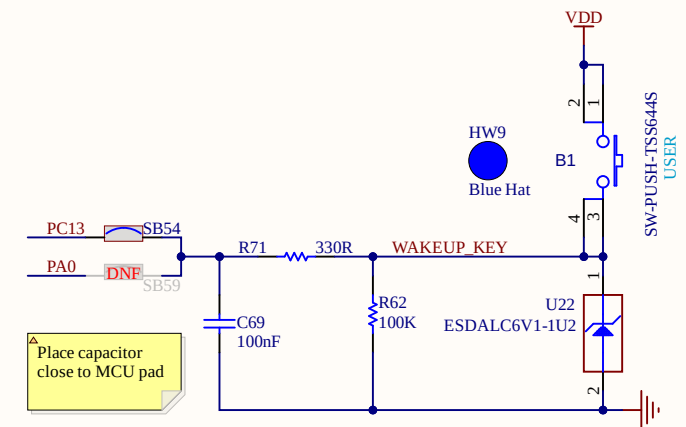
Title: Project overview		
Project: NUCLEO-144 LEGACY		
Variant: H563ZI		
Revision: C-01		Reference: MB1404
Size: A4	Date: 2023-JUL-20	Sheet: 1 of 9



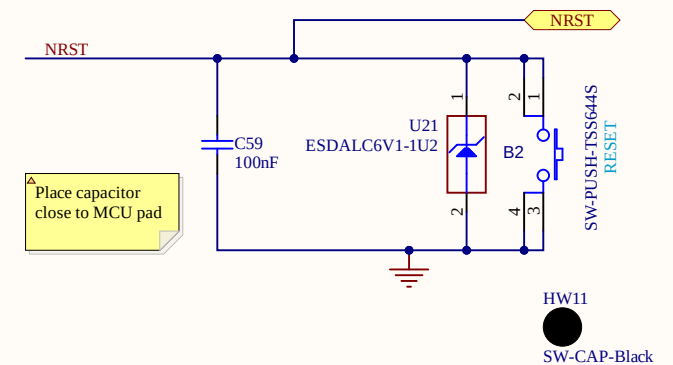




USER BUTTON

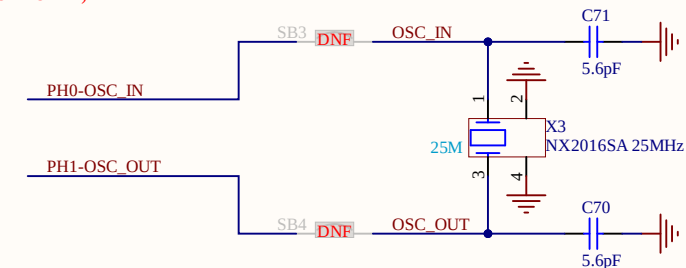


RESET FUNCTION

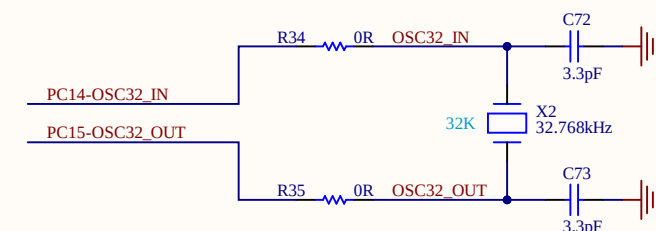


EXTERNAL HSE CLK

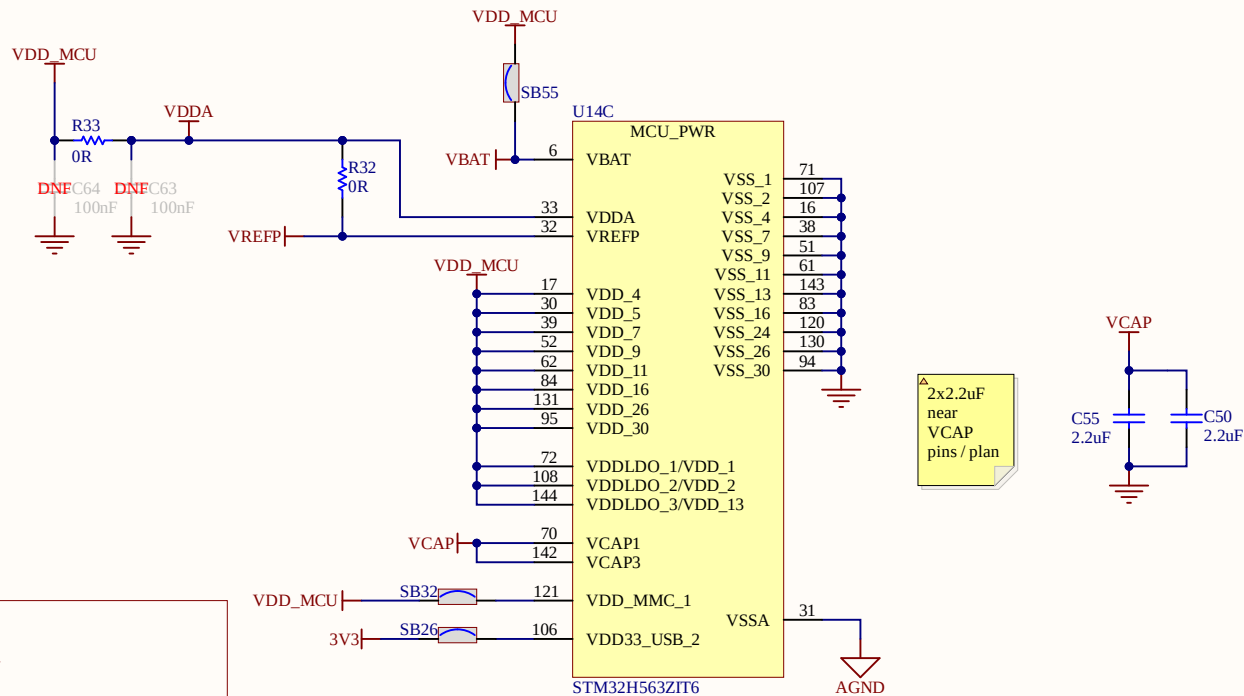
(OPTIONAL)



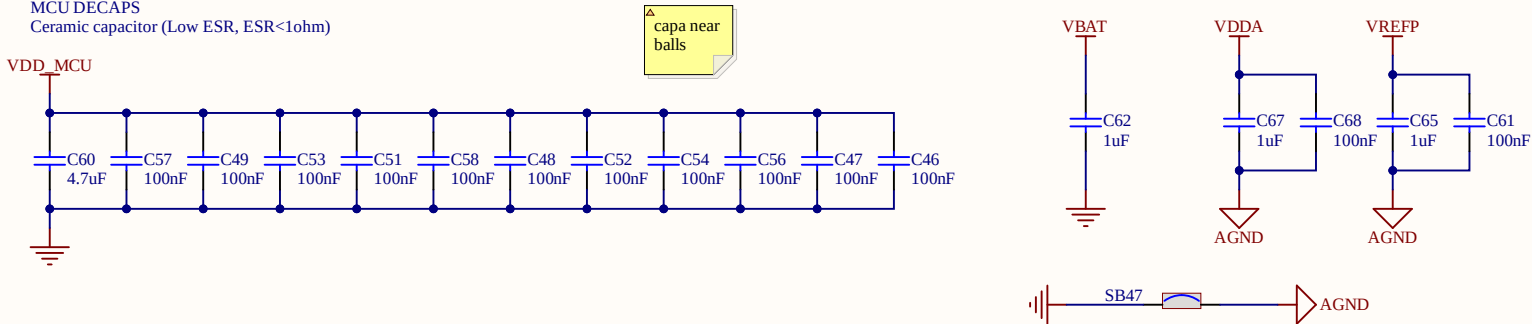
EXTERNAL LSE CLK



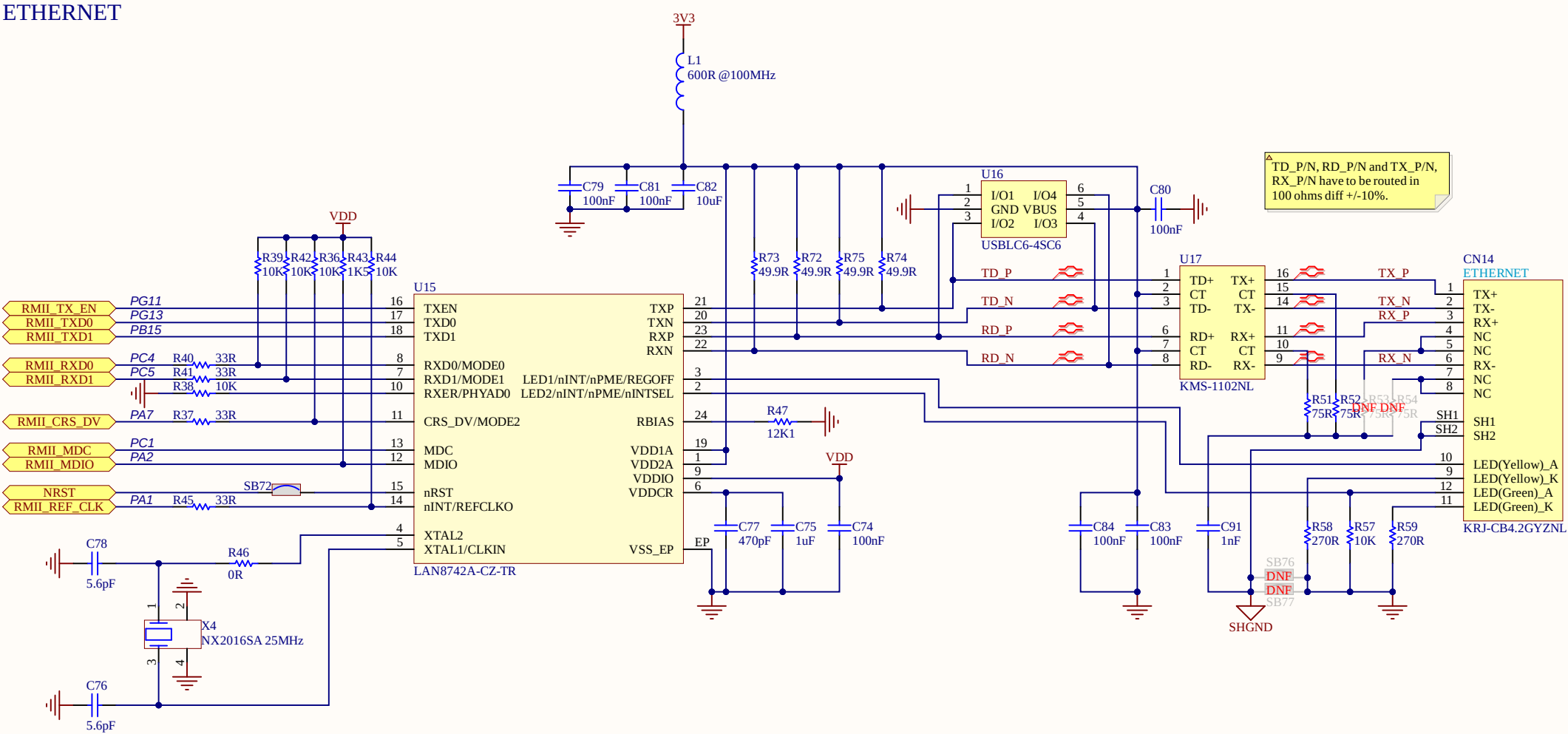
STM32_microcontroller_power



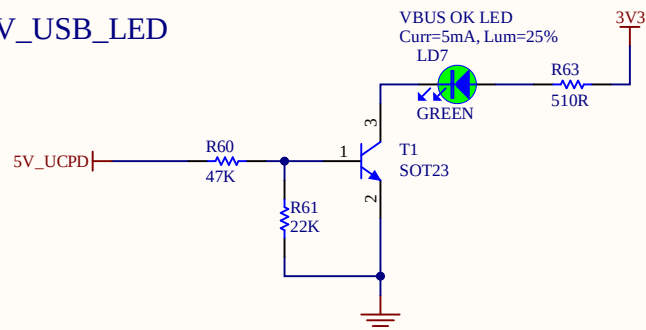
MCU DECAPS
Ceramic capacitor (Low ESR, ESR<1ohm)



ETHERNET



SINK only



BUS SENSE

5V_UCPD

R49
330K

VBUS_SENSE

PA4

C85
10pF

R48
49K9

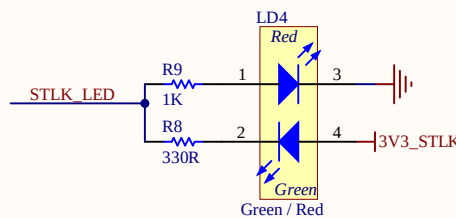
Title: User USB Type-C		
Project: NUCLEO-144 LEGACY		
Variant: H563ZI		
Revision: C-01		Reference: MB1404
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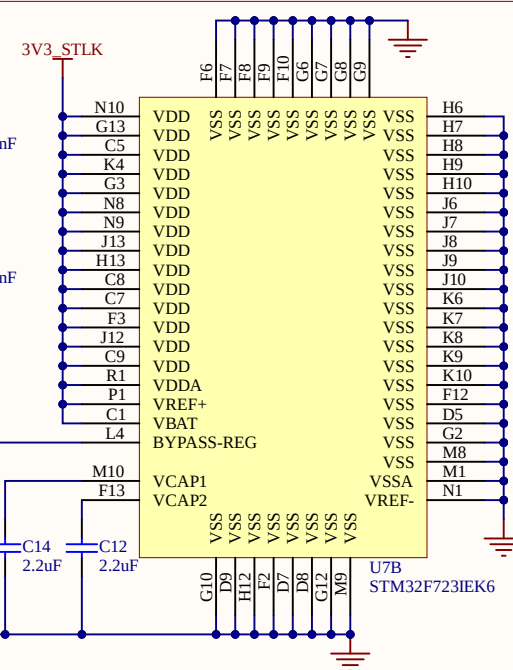
WARNING!
Only 5V/500mA supported on
VBUS_STLK



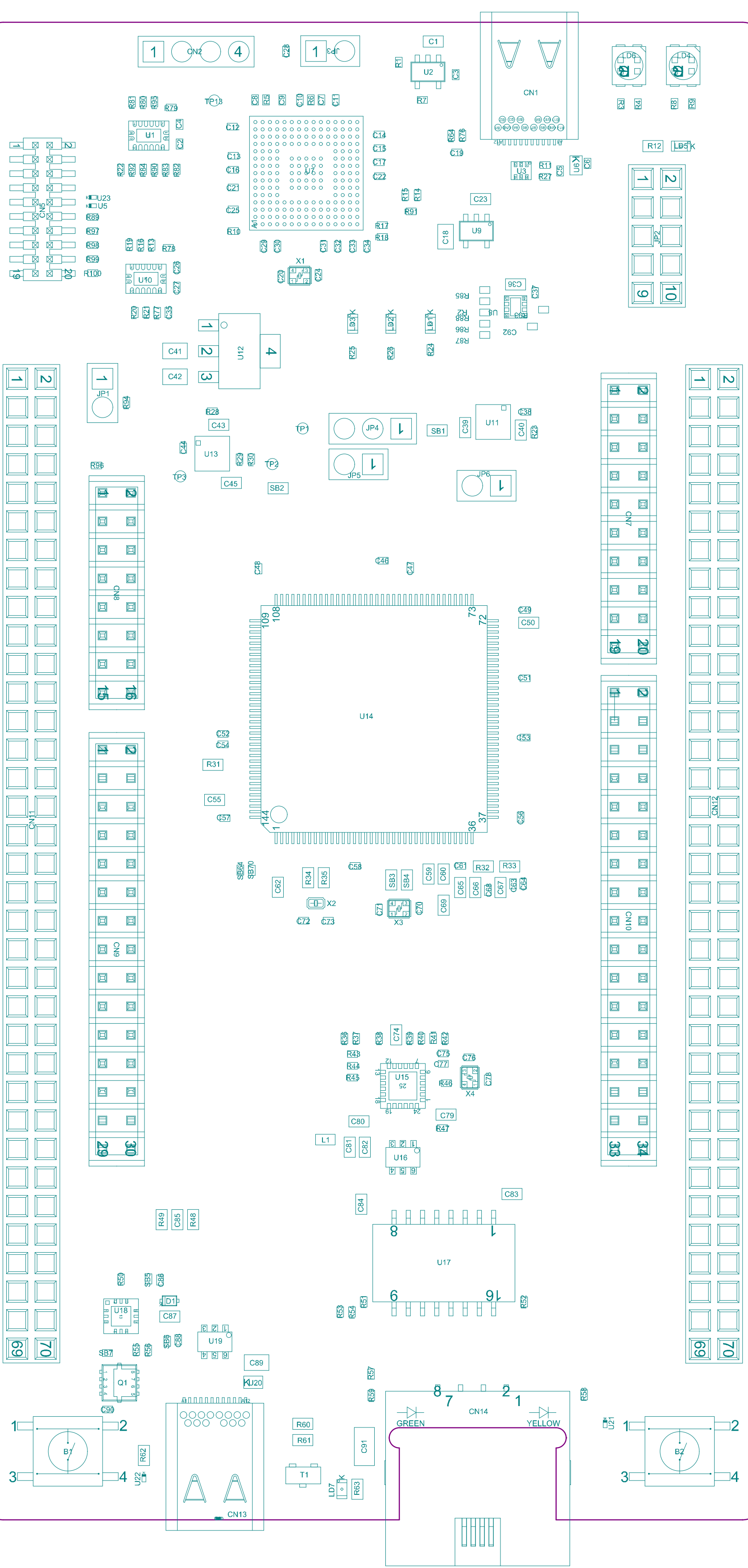
ST-LINK POWER (3V3/300mA)



(1): NOT FITTED FOR DEBUG WITH INTERNAL STLINK (DEFAULT)
(1): FITTED FOR DEBUG WITH EXTERNAL DEBUGGER
Or output STLINK IOs isolated (Hi-Z)

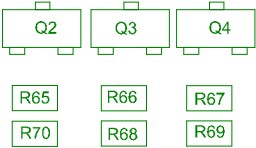
[illegible]

JTCK	PA14	JTCK
JTMS	PA13	JTMS
JTDI	PA15	JTDI
T_VCP_TX	PD8	T_VCP_TX
T_VCP_RX	PD9	T_VCP_RX
JTDO	PB3	JTDO/TRACESWO
NRST		NRST
STLK_MCO	PH0	STLK_MCO





SB13



SB10

SB12

SB11

SB13

SB14
SB15

SB17
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R72
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R74
R75

SB74



SB75



SB76

SB77

