# STM32G0 EVAL USB-C daughter board

## MB1352

## Table of contents


Sheet 1: Project overview (this page)

Sheet 2: MB1352_TOP

Sheet 3: CC & VCONN

Sheet 4: PORT1_DP_USB3

Sheet 5: TYPEC_SOURCE_CON

Sheet 6: PORT2_DP

Sheet 7: TYPEC_SINK_CON

Sheet 8: USB_PD

Sheet 9: POWER

Sheet 10: SOURCE POWER


## Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.

Notes to generate the board layout.

## Open Platform License Agreement



U_MB1352_TOP
MB1352_TOP.SchDoc

| | |
|---|---|
| Title: **Project overview** | |
| Project: **STM32G0 EVAL USB-C daughter board** | |
| Variant: G0C1E | |
| Revision: C-03 | Reference: MB1352 |
| Size: A4 | Date: 23-MAR-2020 |
| | Sheet: 1 of 10 |

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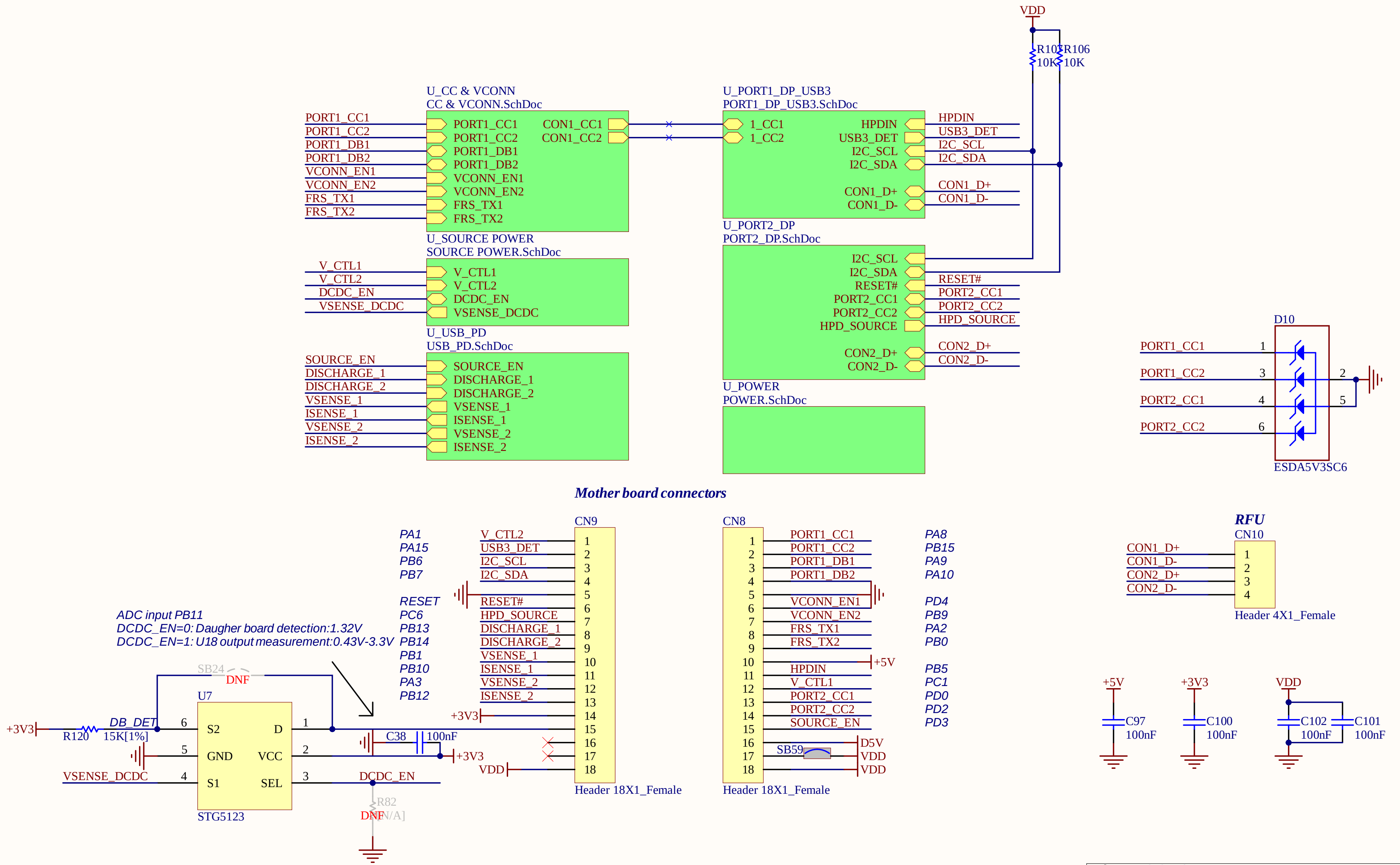


| | | |
|---|---|---|
| Title: MB1352_TOP | | |
| Project: STM32G0 EVAL USB-C daughter board | | |
| Variant: G0C1E | | |
| Revision: C-03 | | Reference: MB1352 |
| Size: A4 | Date: 23-MAR-2020 | Sheet: 2 of 10 |

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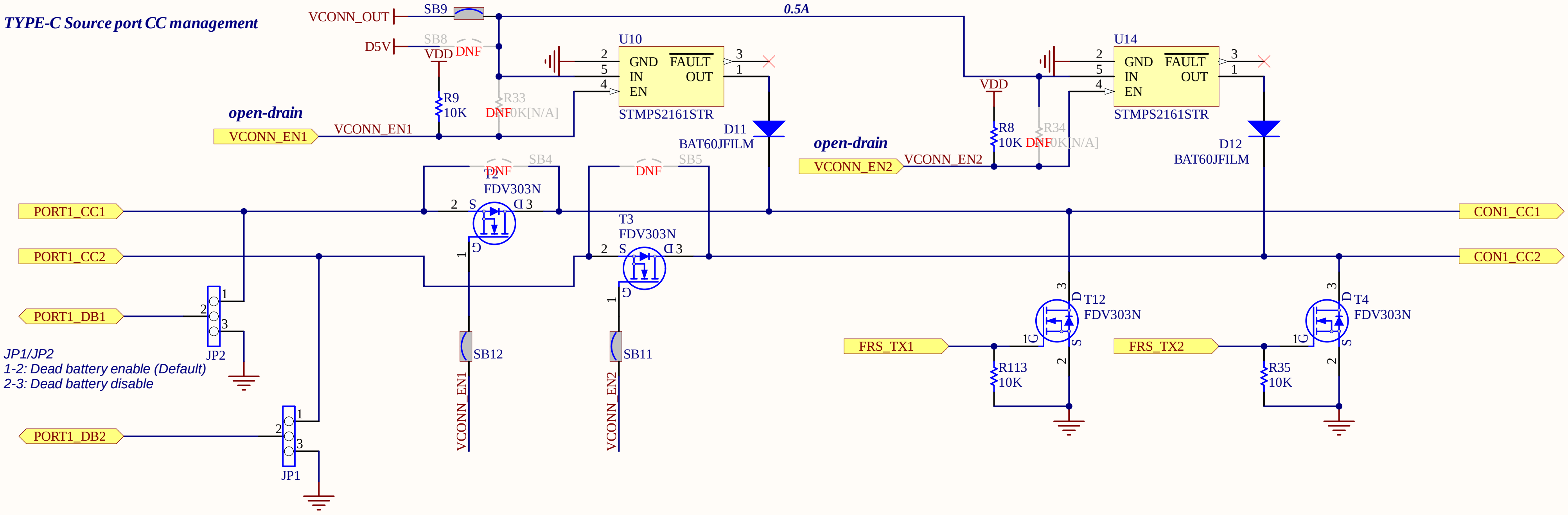
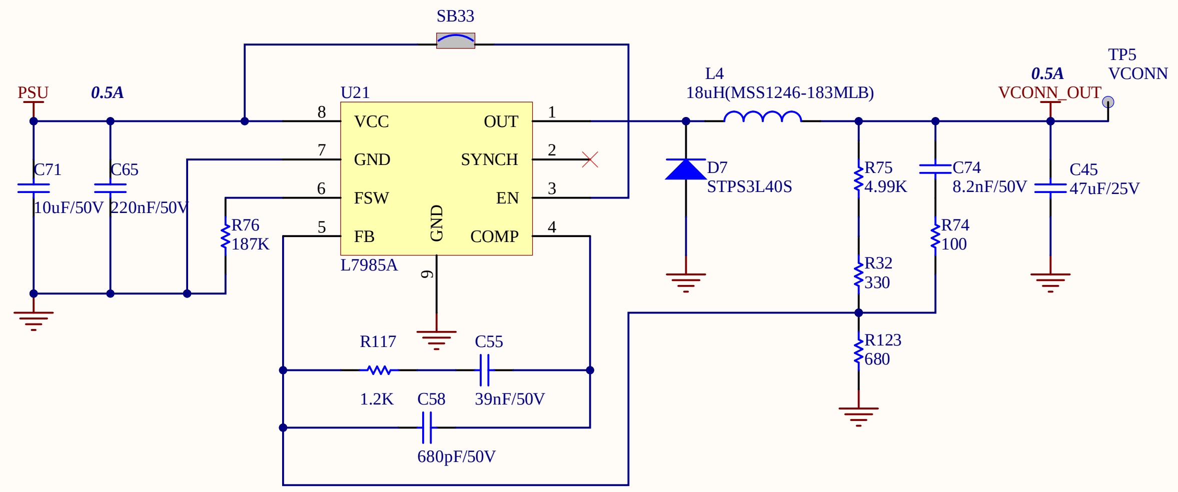
## TYPE-C Source port CC management




## VCONN power




| | |
|---|---|
| Title: CC & VCONN | |
| Project: STM32G0 EVAL USB-C daughter board | |
| Variant: G0C1E | |
| Revision: C-03 | Reference: MB1352 |
| Size: A4 Date: 23-MAR-2020 | Sheet: 3 of 10 |

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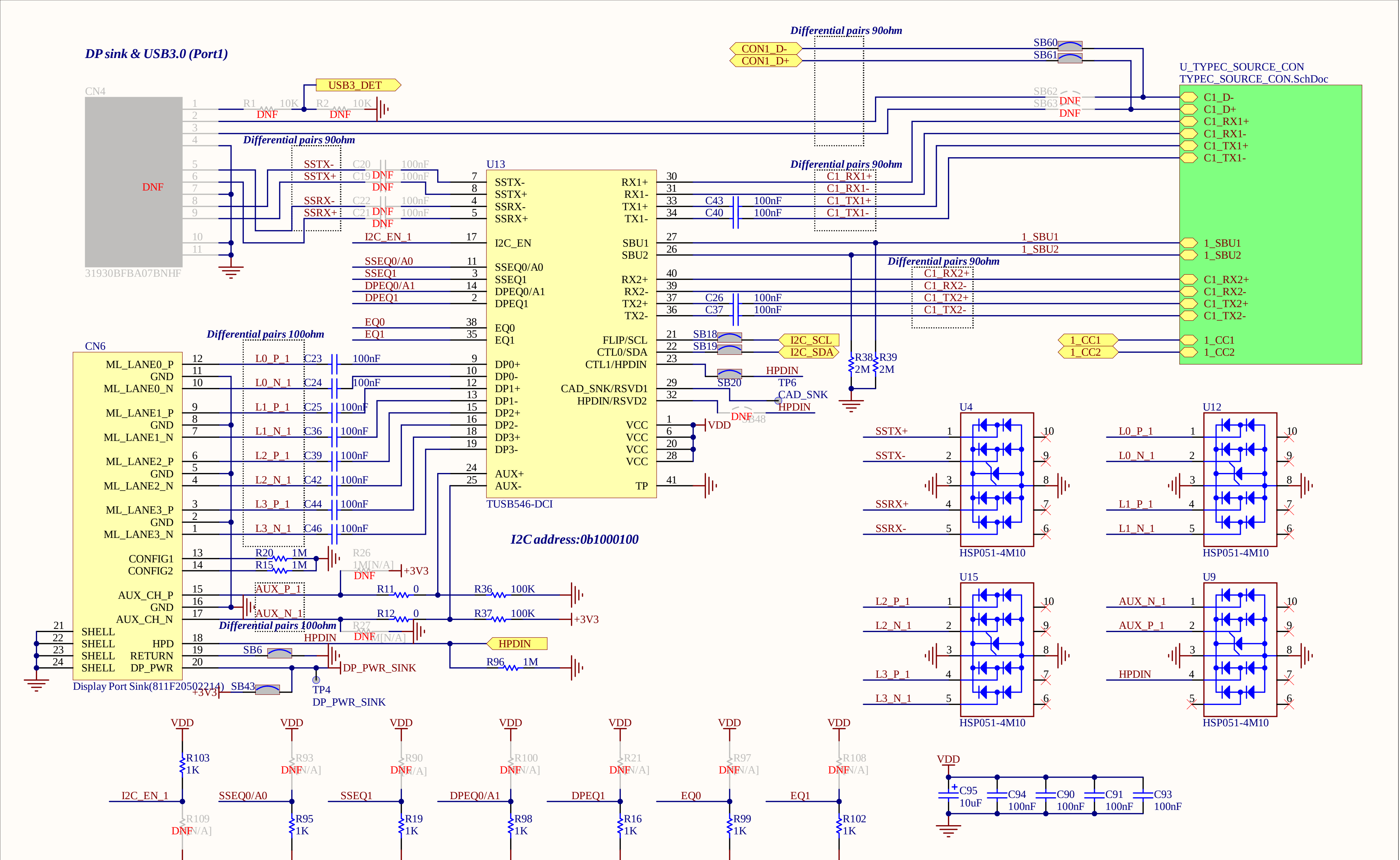


| | |
|---|---|
| Title: | PORT1_DP_USB3 |
| Project: | STM32G0 EVAL USB-C daughter board |
| Variant: | G0C1E |
| Revision: | C-03 |
| Reference: | MB1352 |
| Size: | A4 |
| Date: | 23-MAR-2020 |
| Sheet: | 4 of 10 |

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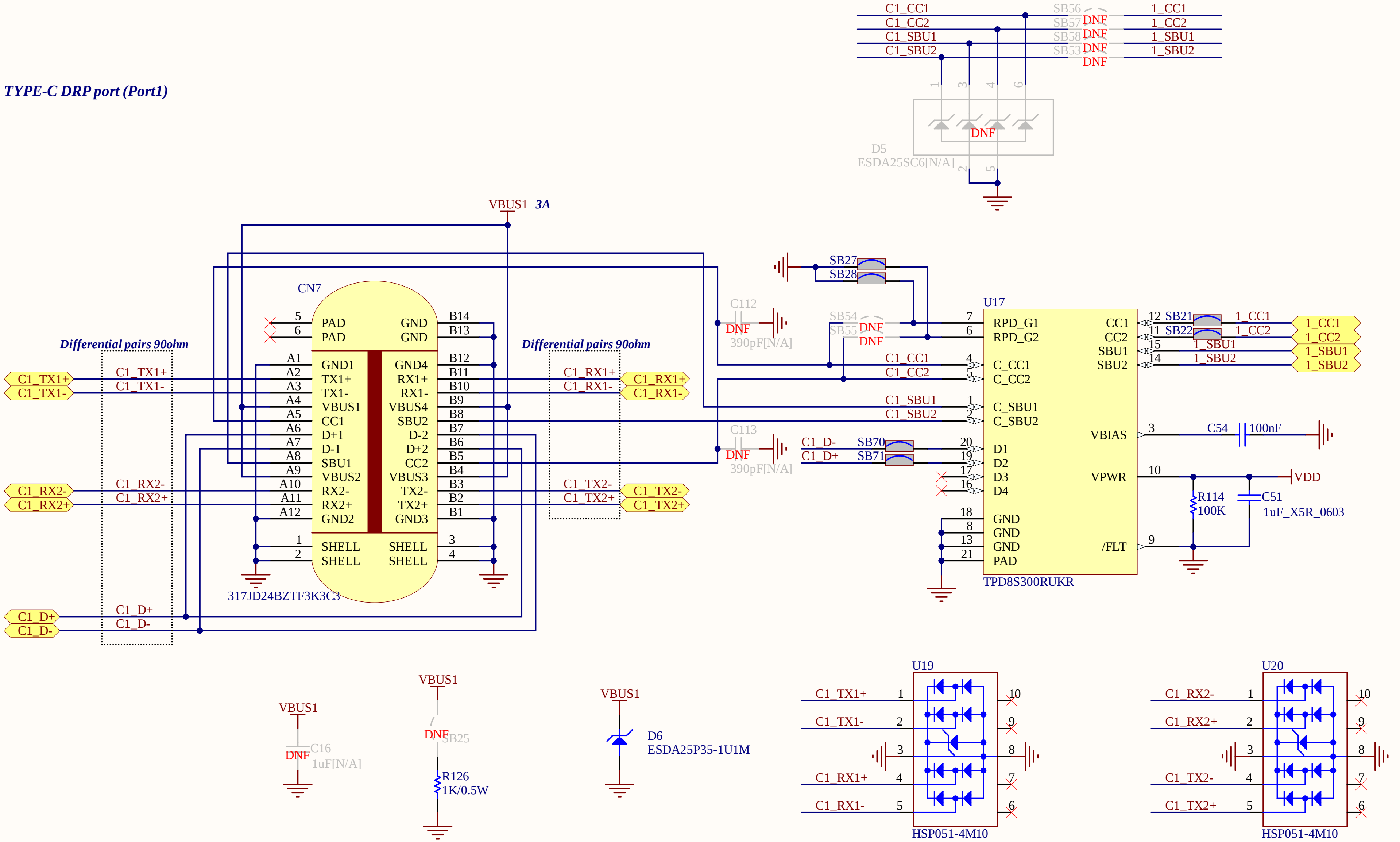
***TYPE-C DRP port (Port1)***




| | |
|---|---|
| Title: **TYPEC_SOURCE_CON** | |
| Project: **STM32G0 EVAL USB-C daughter board** | |
| Variant: G0C1E | |
| Revision: C-03 | Reference: MB1352 |
| Size: A4 Date: 23-MAR-2020 | Sheet: 5 of 10 |

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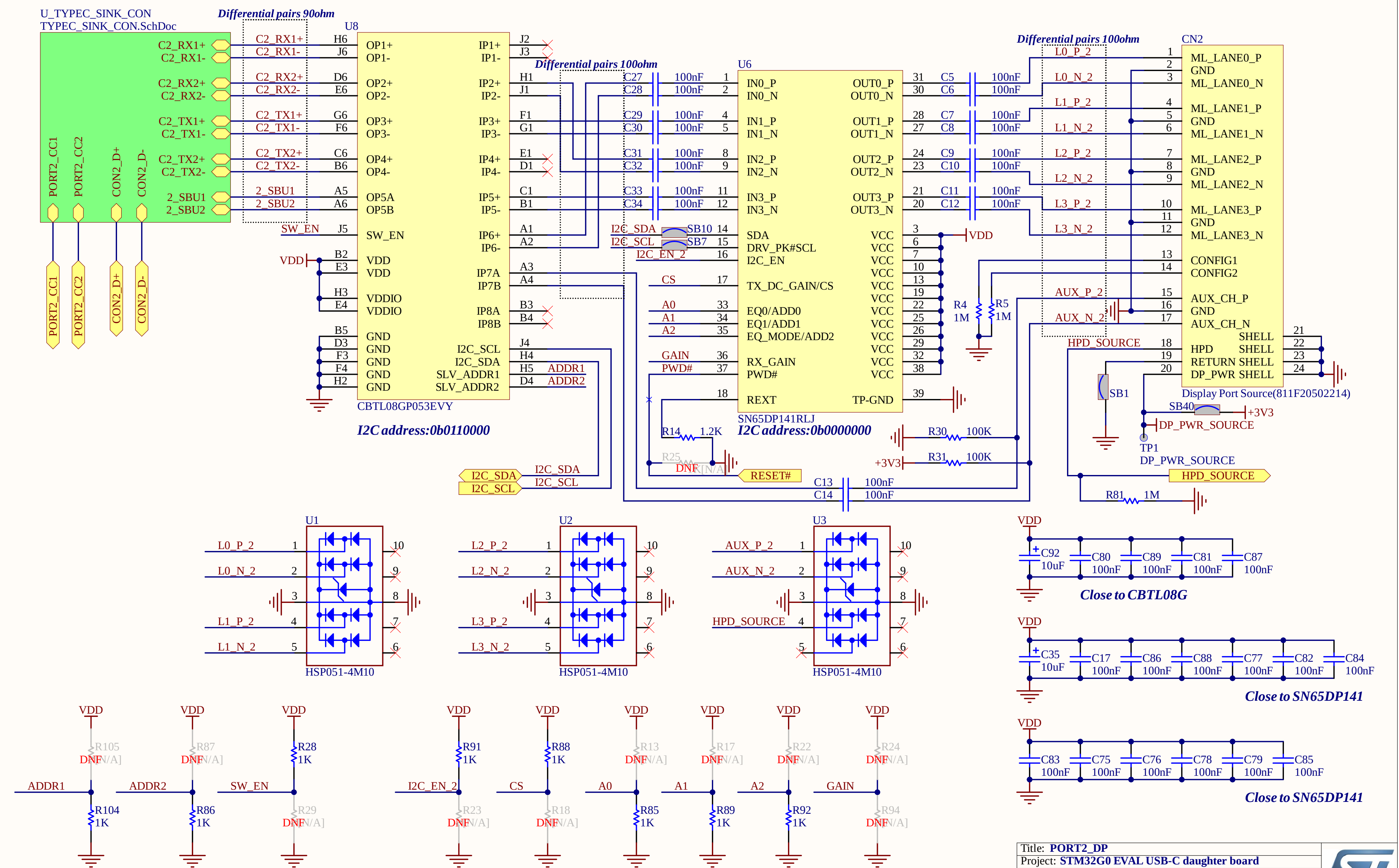
## DP source (Port2)




| | |
|---|---|
| Title: PORT2_DP | |
| Project: STM32G0 EVAL USB-C daughter board | |
| Variant: G0C1E | |
| Revision: C-03 | Reference: MB1352 |
| Size: A4 | Date: 23-MAR-2020 | Sheet: 6 of 10 |

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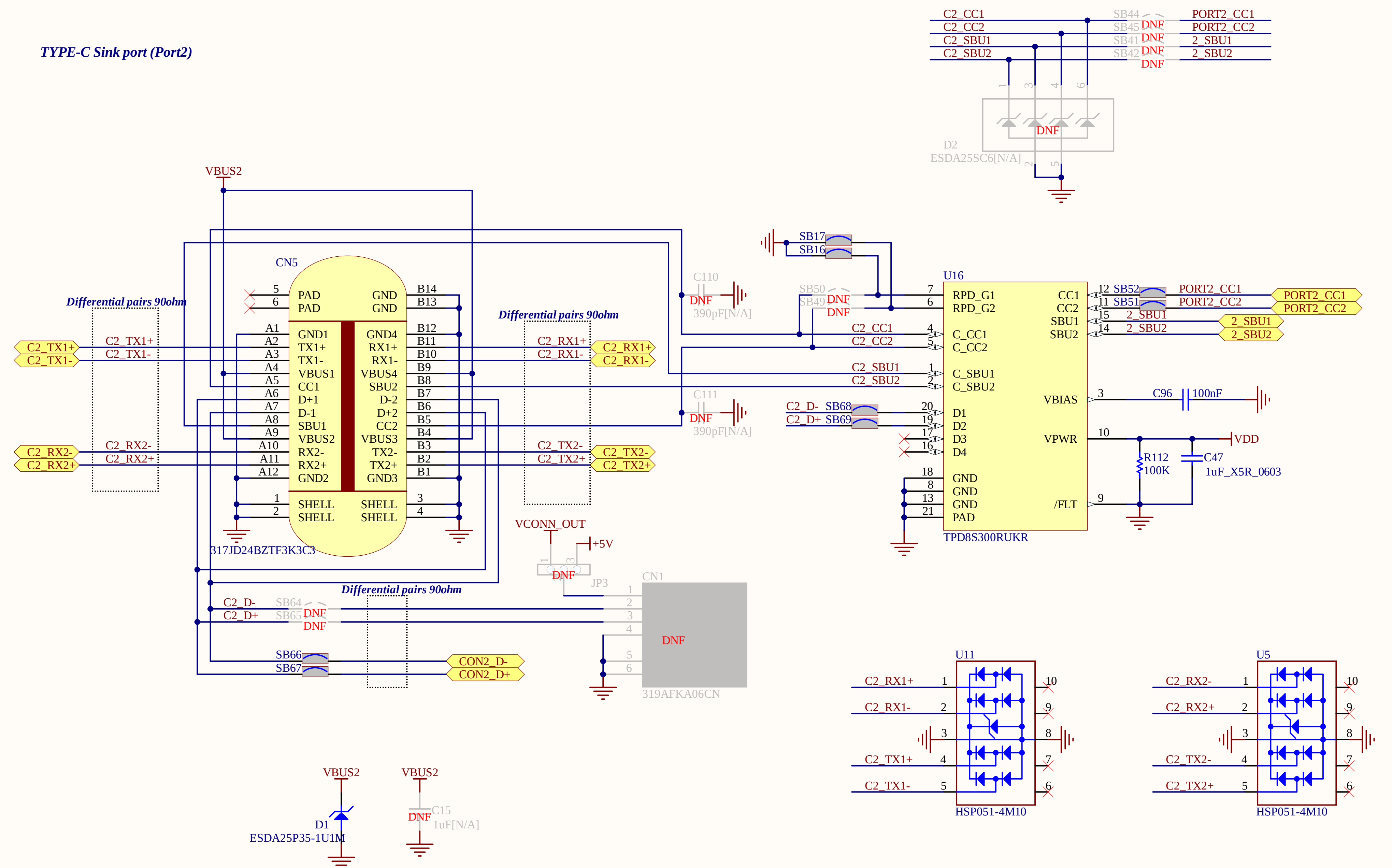
## TYPE-C Sink port (Port2)




| | |
|---|---|
| Title: **TYPEC_SINK_CON** | |
| Project: **STM32G0 EVAL USB-C daughter board** | |
| Variant: G0C1E | |
| Revision: C-03 | Reference: MB1352 |
| Size: A4 | Date: 23-MAR-2020 | Sheet: 7 of 10 |

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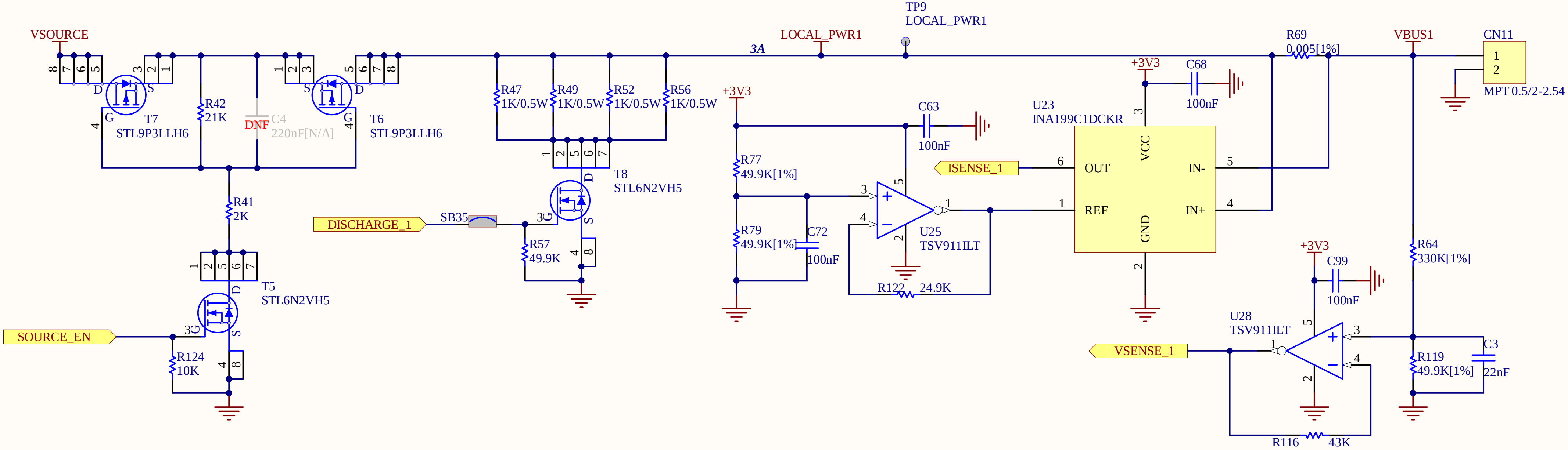
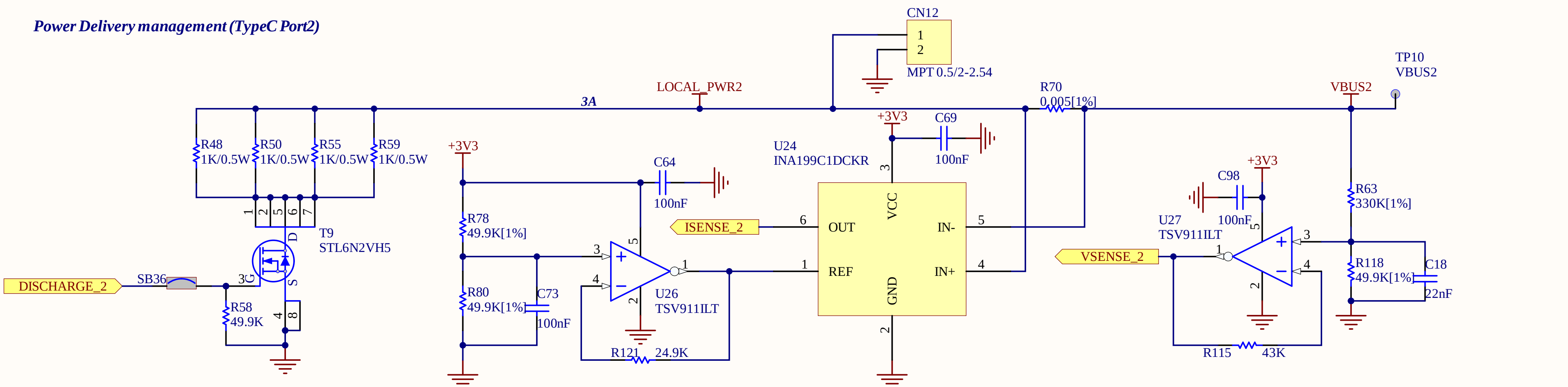
## *Power Delivery management (TypeC Port1)*




## *Power Delivery management (TypeC Port2)*




| | |
|---|---|
| Title: **USB_PD** | |
| Project: **STM32G0 EVAL USB-C daughter board** | |
| Variant: G0C1E | |
| Revision: C-03 | Reference: MB1352 |
| Size: A4 / Date: 23-MAR-2020 | Sheet: 8 of 10 |

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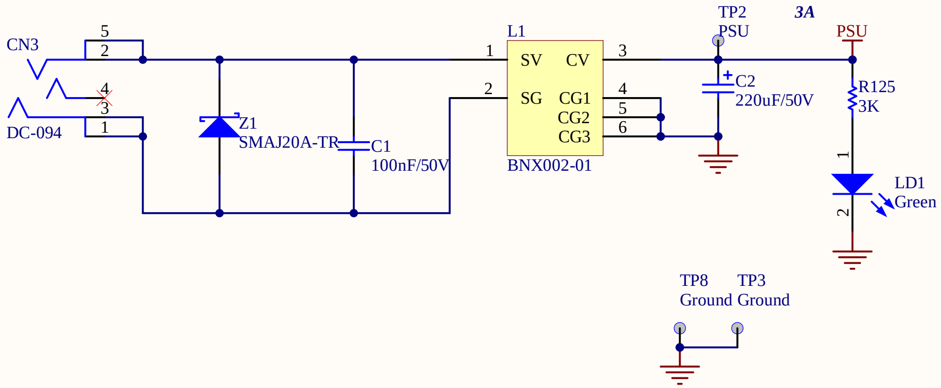
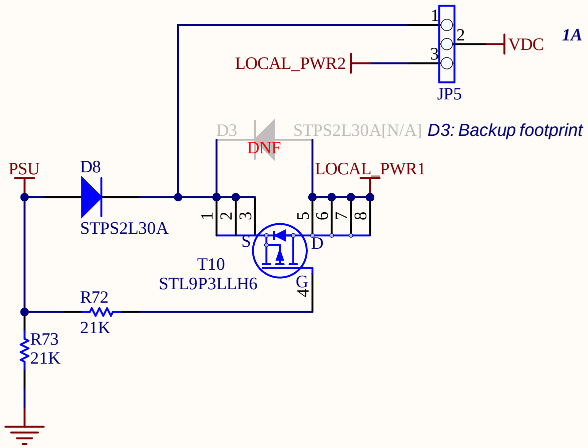
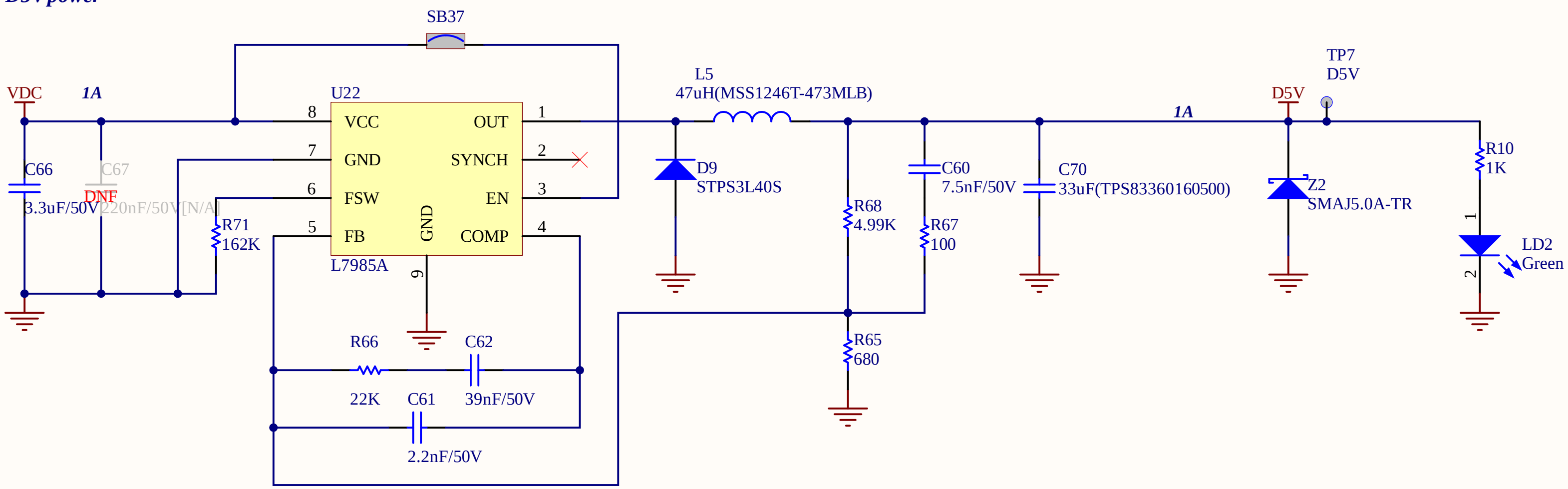
## *Power jack input*




## *Local power selection*




## *D5V power*




| | | |
|---|---|---|
| Title: **POWER** | | |
| Project: **STM32G0 EVAL USB-C daughter board** | | |
| Variant: G0C1E | | |
| Revision: C-03 | | Reference: MB1352 |
| Size: A4 | Date: 23-MAR-2020 | Sheet: 9 of 10 |

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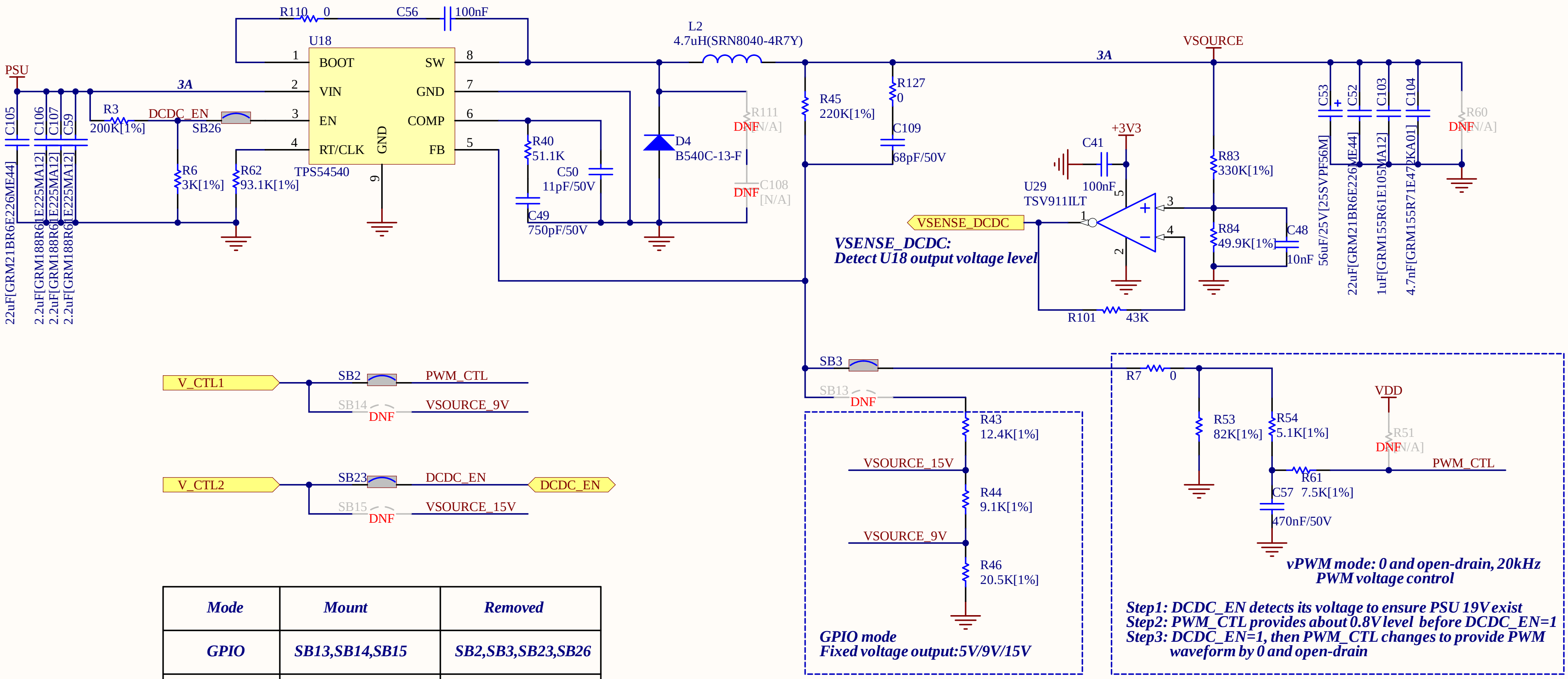
*Source Power*




| Mode | Mount | Removed |
|---|---|---|
| GPIO | SB13,SB14,SB15 | SB2,SB3,SB23,SB26 |
| PWM(Default) | SB2,SB3,SB23,SB26 | SB13,SB14,SB15 |

Title: **SOURCE POWER**
Project: **STM32G0 EVAL USB-C daughter board**
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Revision: C-03 | Reference: MB1352
Size: A4 | Date: 23-MAR-2020 | Sheet: 10 of 10

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