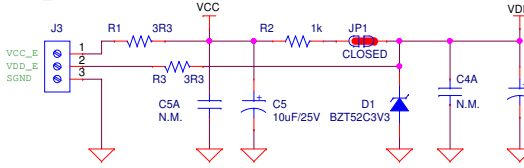
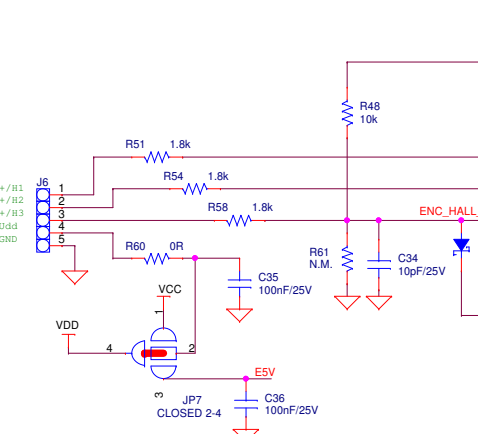
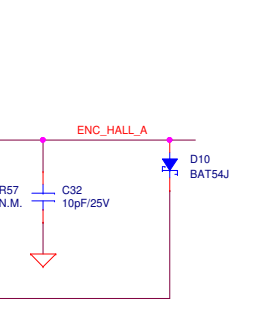
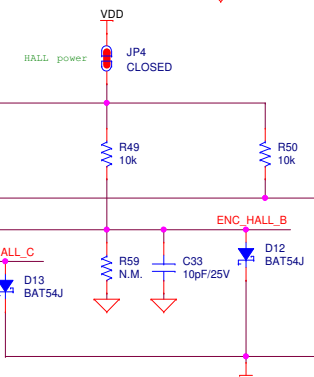
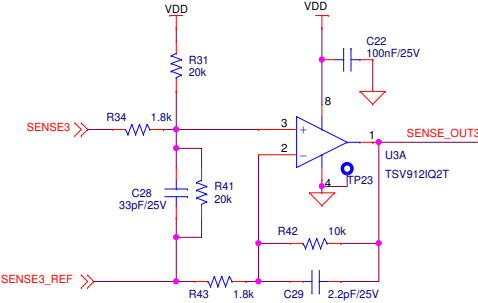
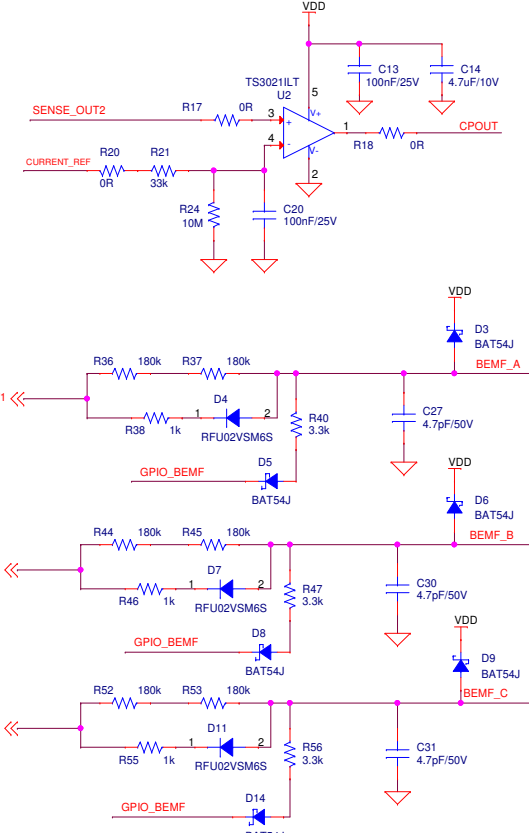
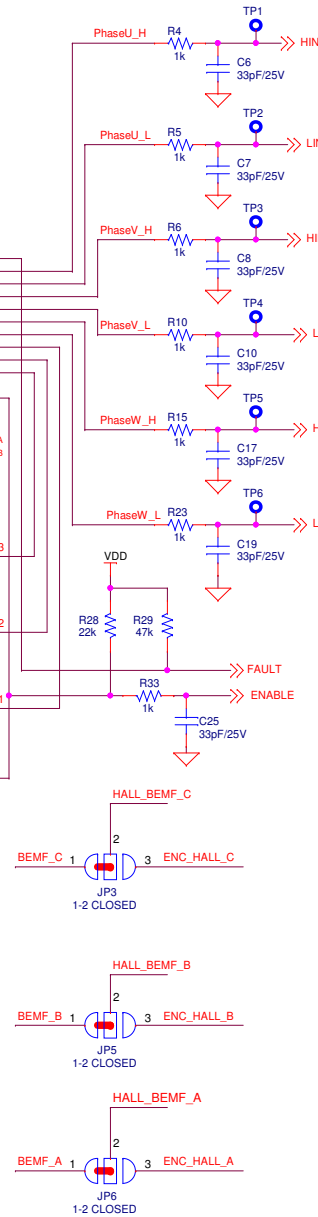
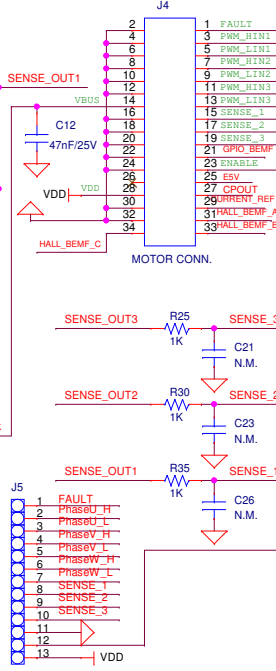
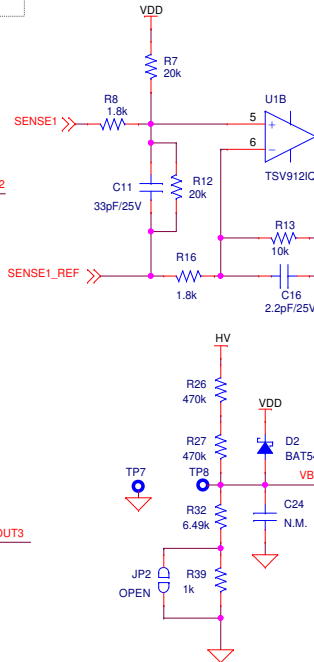
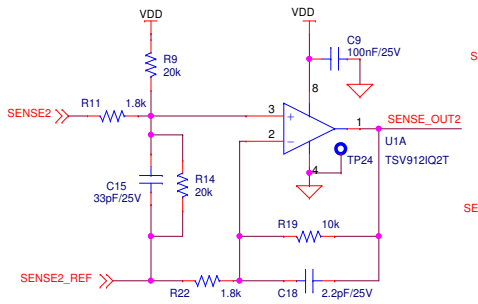


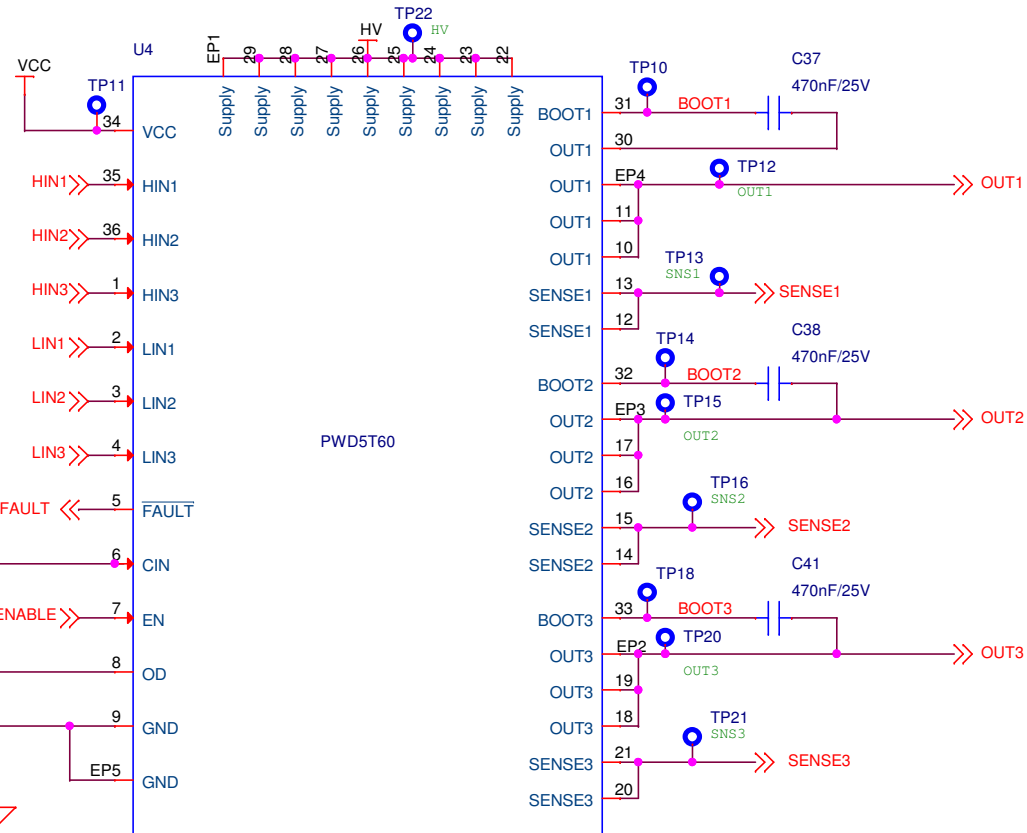
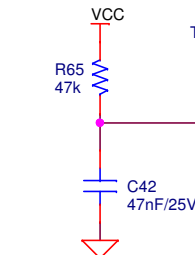
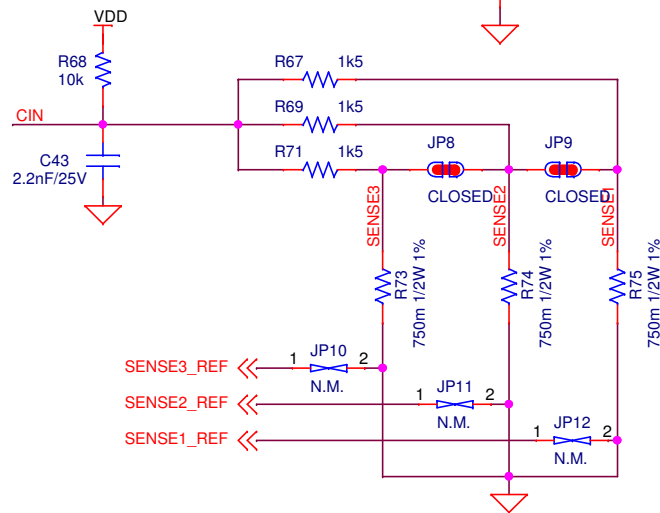
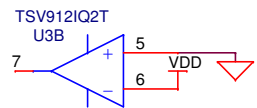
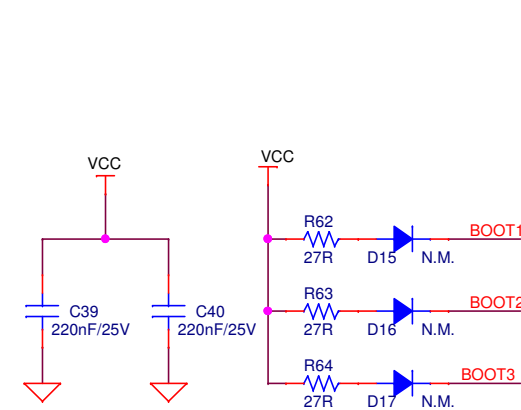
VCC_E min 9V max 20V
VDD_E 3V3



DEFAULT CONFIGURATION
6-step 1 shunt Sensorless Voltage Mode
VCC=15
JP1 closed (VDD=3V3)



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Sheet Title CONNECTORS			
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 life.augmented	Board Title EVLPWD5T60_R1				This drawing and the information herein are property of STMicroelectronics and are intended for evaluation purpose only
	Sheet Title OUTPUT STAGE				
	Doc No	Date	Rev	Sheet	
		Tuesday, March 14, 2023	1.0	2 / 2	