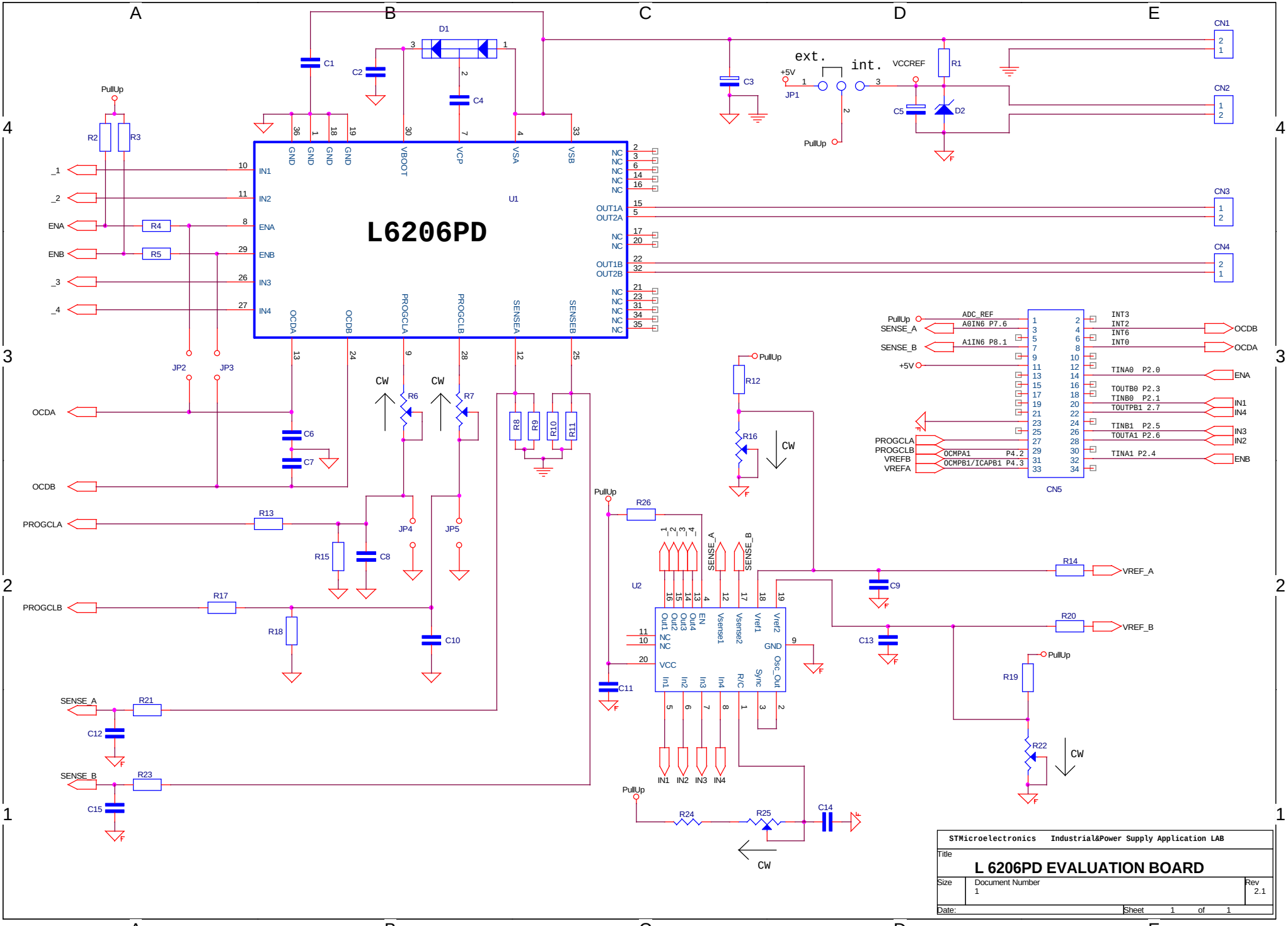
L6206PD
U1
D1
C1
C2
C3
C4
R1
R2
R3
R4
R5
R6
R7
R8
R9
R10
R11
R12
R13
R14
R15
R16
R17
R18
R19
R20
R21
R22
R23
R24
R25
R26
C5
C6
C7
C8
C9
C10
C11
C12
C13
C14
C15
D2
JP1
JP2
JP3
JP4
JP5
U2
CN1
CN2
CN3
CN4
CN5
PullUp
ext.
int.
+5V
VCCREF
CW
GND
VBOOT
VCP
VSA
VSB
IN1
IN2
ENA
ENB
IN3
IN4
OUT1A
OUT2A
OUT1B
OUT2B
OCDA
OCDB
PROGCLA
PROGCLB
SENSEA
SENSEB
SENSE_A
SENSE_B
VREF_A
VREF_B
ADC_REF
A0IN6 P7.6
A1IN6 P8.1
VREFB
VREFA
OCMPA1 P4.2
OCMPB1/ICAPB1 P4.3
INT3
INT2
INT6
INT0
TINA0 P2.0
TOUTB0 P2.3
TINB0 P2.1
TOUTPB1 2.7
TINB1 P2.5
TOUTA1 P2.6
TINA1 P2.4
STMicroelectronics Industrial&Power Supply Application LAB
L 6206PD EVALUATION BOARD
Document Number 1
Rev 2.1
Sheet 1 of 1

L 6206PD EVALUATION BOARD
1 Revision: 2.1
Bill Of Materials

Item	Quantity	Reference	Part
1	4	CN1,CN2,CN3,CN4	CON 2 pins
2	1	CN5	CON34 pins
3	1	C1	Kemet Electronics 220nF/100V CER
4	1	C2	Siemens Matsushita 220nF/100V POLIEST
5	1	C3	Panasonic FA 100uF/63V
6	1	C4	Siemens Matsushita 10nF/100V CER
7	3	C5,C8,C10	Panasonic KG 10uF/16V
8	2	C7,C6	100n
9	2	C9,C13	68nF SMD
10	1	C11	100nF SMD
11	2	C12,C15	470pF SMD
12	1	C14	2.2nF
13	1	D1	Bat46SW
14	1	D2	Zener BZX79C5V1
15	1	JP1	JUMPER 3x1
16	6	JP2,JP3,JP4,JP5	JUMPER
17	1	R1	Res. 750hom 1% 0.6W
18	3	R2,R3,R26	10k 5% 0.25W SMD
19	2	R5,R4	4.7k 5% 0.25W SMD
20	2	R7,R6	Spectrol74W 50k
21	4	R8,R9,R10,R11	0.4 Ohm 1W 1% DALE WSL-2512
22	2	R12,R19	20k 1% SMD
23	4	R13,R14,R17,R20	2.2k ohm SMD
24	2	R15,R18	750 ohm SMD
25	2	R16,R22	Spectrol74W 5k
26	2	R23,R21	1k SMD
27	1	R24	12k 0.25W
28	1	R25	Spectrol74W 50k
29	1	U1	L6206PD
30	1	U2	L6506 so20

Important Notes
JP1 : close in INT position for use with PractiSPIN ST7 board
C6, C7 : recommended change to 5.6nF for safe Overcurrent protection
R2, R3 : recommended change to 100k for safe Overcurrent protection
R4, R5 : recommended change to 100k if EN pins are driven
from the CN5 connector (for example with PractiSPIN ST7 board), for safe Overcurrent protection
R16, R22 : set the maximum current obtainable through PractiSPIN (see PractiSPIN documentation)
R1 recommended change to adequate value (depending on supply voltage)
to obtain 5V across D2
JP2, JP3 close to allow OverCurrent Protection
JP4, JP5: close for on-board OCD threshold programming through R7, R8
CN5 : VrefA and VrefB positions are inverted if compared to other EVAL62XX boards.