

STM32CubeProgrammer release v2.23.0

Introduction

This release note is updated periodically to keep abreast of the STM32CubeProgrammer (STM32CubeProg) evolution, problems, and limitations. Check the STMicroelectronics website at www.st.com/stm32softwaretools for the latest version. For the latest release summary, refer to [Table 1](#).

Table 1. STM32CubeProgrammer v2.23.0 release summary

| Type | Summary |
|---------------|---|
| Major release | <ul style="list-style-type: none"> • New device support: <ul style="list-style-type: none"> – STM32H543/553 devices – STM32U335/345 devices – STM32U356/366 devices • Maintenance: <ul style="list-style-type: none"> – Support key wrapping on STM32H5E4/5F4, STM32H5E5/5F5, and STM32U3B5/3C5 devices – Support SFlx for external memory on STM32H5E4/5F4 and STM32H5E5/5F5 devices – STM32V863/873 devices: <ul style="list-style-type: none"> ◦ OBkey generation and provisioning ◦ Certificate generation ◦ Debug authentication • New features: <ul style="list-style-type: none"> – Native Raspberry Pi support for the Linux Arm 64-bit command-line interface (CLI) package – Readable file paths – Launch after install – Sidekick AI Assistant |

Customer support

For more information or help concerning STM32CubeProgrammer, contact the nearest STMicroelectronics sales office or use the ST community at community.st.com. For a complete list of STMicroelectronics offices and distributors, refer to the www.st.com webpage.

Software updates

Software updates and all the latest documentation can be downloaded from the STMicroelectronics support webpage at www.st.com/stm32cubeprog.

1 General information

1.1 Overview

STM32CubeProgrammer is a tool that allows STM32 device programming through debug interfaces (JTAG and SWD) and bootloader interfaces (UART, USB, I²C, SPI, and CAN). The range of supported bootloader interfaces depends on the microcontroller connected. Refer to the *STM32 microcontroller system memory boot mode* application note (AN2606) for details.

The tool offers a wide range of features to program STM32 internal memories (flash memory, RAM, OTP, and others) and external memories, verify the programming content (checksum, verify during and after programming, compare with file), and automate STM32 programming.

The STM32CubeProgrammer package also offers the optional installation of the STM32 Trusted Package Creator tool, which is used to create secure firmware files for secure firmware install and update. For more information, refer to the *STM32 Trusted Package Creator tool software description* user manual (UM2238).

STM32CubeProgrammer supports STM32 32-bit microcontrollers and microprocessors based on the Arm[®] Cortex[®] processor.



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1.2 Host PC system requirements

Supported operating systems and architectures

- Windows[®] 10 32-bit (x86), Windows[®] 64-bit (x64), and Windows[®] 11 64-bit (x64)
- Linux[®]: Ubuntu[®] LTS 22.04, Ubuntu[®] LTS 24.04, and Fedora[®] 43
- macOS[®] 15 (Sequoia), macOS[®] 26 (Tahoe): x86_64 and AArch64 architectures
- Raspberry Pi[®] OS (Debian[®] 12 “Bookworm” or later)

Note: Windows is a trademark of the Microsoft group of companies.

Linux[®] is a registered trademark of Linus Torvalds.

Ubuntu[®] is a registered trademark of Canonical Ltd.

Fedora[®] is a trademark of Red Hat, Inc.

macOS[®] is a trademark of Apple Inc., registered in the U.S. and other countries and regions.

Raspberry Pi[®] is a trademark of Raspberry Pi Ltd.

Debian[®] is a registered trademark of Software in the Public Interest, Inc.

Software requirements

For STM32CubeProgrammer versions earlier than v2.6.0, the Java[®] SE Runtime Environment 1.8 (version 1.8.0_121 or newer) must be installed by Oracle[®] (only Java[®] 8 is supported).

Since STM32CubeProgrammer version v2.6.0, the tool can be installed without prerequisite JRE™ installation, because the STM32CubeProgrammer release package contains a JRE™ bundling.

Note: After the Oracle[®] announcement related to the “End of Public Updates for Oracle JDK 8”, access to OpenJDK is possible via adoptopenjdk.net.

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All other trademarks are the property of their respective owners.

1.3 Setup procedure

Refer to the *STM32CubeProgrammer software description* user manual (UM2237) available at www.st.com.

1.4 Licensing

STM32CubeProg is delivered under the SLA0048 software license agreement and its Additional License Terms.

2 STM32CubeProgrammer v2.23.0 release information

2.1 New features

- New device support:
 - STM32H543/553 devices:
 - Internal memory programming through debug and bootloader interfaces
 - Option byte programming through debug and bootloader interfaces
 - OTP programming through debug and bootloader interfaces
 - Core register programming through debug interfaces
 - Automatic mode
 - Serial Wire Viewer
 - Register viewer
 - Firmware encryption and signing
 - OBKey generation and provisioning
 - Certificate generation
 - Debug authentication
 - SFI through debug interfaces
 - STM32U335/345 devices:
 - Internal memory programming through debug and bootloader interfaces
 - Option byte programming through debug and bootloader interfaces
 - OTP programming through debug and bootloader interfaces
 - Core register programming through debug interfaces
 - Automatic mode
 - Serial Wire Viewer
 - Register viewer
 - Firmware encryption and signing
 - RDP regression with a password
 - STM32U356/366 devices:
 - Internal memory programming through debug and bootloader interfaces
 - Option byte programming through debug and bootloader interfaces
 - OTP programming through debug and bootloader interfaces
 - Core register programming through debug interfaces
 - Automatic mode
 - Serial Wire Viewer
 - Register viewer
 - Firmware encryption and signing
 - RDP regression with password
 - Secure firmware install (SFI) through debug interfaces
 - Key wrapping via debug interfaces
 - Maintenance:
 - Add key wrapping support for:
 - STM32H5Ex/5Fx devices
- Note:*
- In the rest of the document, STM32H5Ex/5Fx refers to STM32H5E4/5F4 and STM32H5E5/5F5 devices
 - STM32U3B5/3C5 devices
- Add SFIx support for external memory on STM32H5Ex/5Fx devices
 - STM32V863/873 devices:
 - OBKey generation and provisioning
 - Certificate generation
 - Debug authentication

- New features and enhancements:
 - Native Raspberry Pi support for the Linux Arm 64-bit command-line interface (CLI) package
 - Readable file paths
 - Launch after installation
 - AI Assistant: Sidekick is now available in STM32CubeProgrammer, with a dedicated access button at the top

Refer to the *STM32CubeProgrammer software description* user manual (UM2237) for details.

2.2 Fixed issues

Refer to the STM32 microcontroller wiki at wiki.st.com/stm32mcu/wiki/Category:STM32CubeProgrammer_errata.

Note: Access to the wiki content requires logging in.

2.3 Known problems and limitations

Refer to the STM32 microcontroller wiki at wiki.st.com/stm32mcu/wiki/Category:STM32CubeProgrammer_errata.

Note: Access to the wiki content requires logging in.

3 Previous release information

3.1 STM32CubeProgrammer v2.22.0 release information

3.1.1 New features

- Support for new devices:
 - Support for STM32V8 series microcontrollers:
 - Internal memory programming through debug and bootloader interfaces
 - Option bytes programming through debug and bootloader interfaces
 - OTP programming through debug and bootloader interfaces
 - Core register programming through debug interfaces
 - External memory programming through debug interfaces
 - Automatic mode
 - Serial Wire Viewer
 - Register viewer
 - Support for the STM32U3 microcontrollers with 2 Mbytes of flash memory:
 - Internal memory programming through debug and bootloader interfaces
 - Option bytes programming through debug and bootloader interfaces
 - OTP programming through debug and bootloader interfaces
 - Core register programming through debug interfaces
 - Automatic mode
 - Serial Wire Viewer
 - Register viewer
 - Firmware encryption/signing
 - RDP regression with password
 - SFI through debug and bootloader interfaces (USB and UART)
 - Support for [STM32C5 series](#) microcontrollers:
 - Internal memory programming through debug and bootloader interfaces
 - Option bytes programming through debug and bootloader interfaces
 - OTP programming through debug and bootloader interfaces
 - Core register programming through debug interfaces
 - Automatic mode
 - Serial Wire Viewer
 - Register viewer
 - Firmware encryption/signing
 - RDP regression with password
 - Support for STM32WBA2xxx microcontrollers:
 - Internal memory programming through debug and bootloader interfaces
 - Option bytes programming through debug and bootloader interfaces
 - OTP programming through debug and bootloader interfaces
 - Core register programming through debug interfaces
 - External memory programming through debug interfaces
 - Automatic mode
 - Serial Wire Viewer
 - Register viewer
 - Firmware encryption/signing
 - RDP regression with password
 - Support for the STM32H5 microcontrollers with 4 Mbytes of flash memory:

- Internal memory programming through debug and bootloader interfaces
- Option bytes programming through debug and bootloader interfaces
- OTP programming through debug and bootloader interfaces
- Core register programming through debug interfaces
- External memory programming through debug interfaces
- Automatic mode
- Serial Wire Viewer
- Register viewer
- Firmware encryption/signing
- OBkey generation/provisioning
- Certificate generation
- Debug authentication
- SFI through debug and bootloader interfaces (USB and UART)
- License generation
- Secure Manager install
- New features:
 - Debug authentication on Windows® 32 bits
 - Export of MCU memory map:
 - Simplified the memory map dump (option bytes, OTP, internal/external flash memory) in HEX output file format
 - Support for OBKey generation and signing with SHA-384 and ECDSA-P384 in STM32 Trusted Package Creator:
 - Added the support for SHA-384 and ECDSA-P384 encryption types

Refer to the *STM32CubeProgrammer software description* user manual ([UM2237](#)) for details.

3.1.2 Fixed issues

Refer to the STM32 microcontroller wiki at wiki.st.com/stm32mcu/wiki/Category:STM32CubeProgrammer_errata.

Note: Access to the wiki content requires logging in.

3.1.3 Known problems and limitations

Refer to the STM32 microcontroller wiki at wiki.st.com/stm32mcu/wiki/Category:STM32CubeProgrammer_errata.

Note: Access to the wiki content requires logging in.

3.2 STM32CubeProgrammer v2.21.0 release information

3.2.1 New features

- Support for the STM32WL3Rxx microcontrollers:
 - Internal memory programming
 - Option byte and OTP programming
 - Firmware authentication
- STM32MP21x microprocessors:
 - SSP support on Linux®
- STM32WBA6xxG microcontrollers:
 - Support for 1-Mbyte flash memory size
- Support for new devices in the [STM32C0 series](#):
 - STM32C011xx, STM32C031xx, STM32C051xx, STM32C071xx, STM32C091xx, and STM32C092xx microcontrollers
- Support for the factory reset of option bytes:
 - STM32WBA5xxx and STM32WBA6xxx microcontrollers
 - [STM32U375/385](#) microcontrollers
- Dynamic upgrade of components
- Support for SWD multi-drop
- Fast read new CLI command

Refer to the *STM32CubeProgrammer software description* user manual (UM2237) for details.

3.2.2 Fixed issues

Refer to the STM32 microcontroller wiki at wiki.st.com/stm32mcu/wiki/Category:STM32CubeProgrammer_errata.

Note: Access to the wiki content requires logging in.

3.2.3 Known problems and limitations

Refer to the STM32 microcontroller wiki at wiki.st.com/stm32mcu/wiki/Category:STM32CubeProgrammer_errata.

Note: Access to the wiki content requires logging in.

3.3 STM32CubeProgrammer v2.20.0 release information

3.3.1 New features

- Support for the STM32MP21x microprocessors:
 - External memory programming
 - OTP programming
 - PKCS#11
 - ST power management IC
- Support for MCE Noekeon encryption for [STM32N6 series](#) microcontrollers
- Extended PMIC support to more components
- Support of the Secure Manager on external flash memory for STM32H573xx microcontrollers
- Recovery button for [STM32H5 series](#) microcontrollers
- Support for Chinese and Japanese languages
- Support for incremental programming for STM32WB5xxx/35xx and STM32G0B0xx/B1xx/C1xx microcontrollers
- Support for macOS® on Apple® silicon

Refer to the *STM32CubeProgrammer software description* user manual (UM2237) for details.

3.3.2 Fixed issues

Refer to the STM32 microcontroller wiki at wiki.st.com/stm32mcu/wiki/Category:STM32CubeProgrammer_errata.

Note: Access to the wiki content requires logging in.

3.3.3 Known problems and limitations

Refer to the STM32 microcontroller wiki at wiki.st.com/stm32mcu/wiki/Category:STM32CubeProgrammer_errata.

Note: Access to the wiki content requires logging in.

3.4 STM32CubeProgrammer v2.19.0 release information

3.4.1 New features

- Support for the STM32WBA6xxx microcontrollers:
 - Internal memory programming
 - External memory programming
 - Option byte/OTP programming
 - RDP regression with password
 - SFI support
- Support for MCE Noekeon encryption for [STM32H7R3/7S3](#) and [STM32H7R7/7S7](#) microcontrollers
- Performance enhancement: support for incremental programming
- Support for endianness management in automatic serial numbering
- Added key wrapping using coupling and chaining bridge (CCB), supporting DUA key, for the [STM32U3 series](#) microcontrollers
- Support for flash programming via J-Link for the [STM32WB0 series](#) microcontrollers
- [X-CUBE-RSSe](#) and [STM32MPUSSP-UTIL](#) packages install and update from server
- Support for multiple boards for debug authentication

Refer to the *STM32CubeProgrammer software description* user manual (UM2237) for details.

3.4.2 Fixed issues

Refer to the STM32 microcontroller wiki at wiki.st.com/stm32mcu/wiki/Category:STM32CubeProgrammer_errata.

3.4.3 Known problems and limitations

Refer to the STM32 microcontroller wiki at wiki.st.com/stm32mcu/wiki/Category:STM32CubeProgrammer_errata.

3.5 STM32CubeProgrammer v2.18.0 release information

3.5.1 New features

- Added the support for the [STM32N6 series](#)
 - OTP programming
 - External memory programming
 - Serial boot via USB/UART
 - FSBL signature with `stm32header v2.3` through the signing tool
- Added the support for the [STM32U3 series](#)
 - Internal memory programming
 - Option byte/OTP programming
 - Core register programming
 - Regression with password (close/open)
 - SFI support
 - Key wrapping
 - Native services:
 - Key generation
 - Firmware encryption and signing
- Added the support for the [STM32WL3x](#) product line
 - Internal memory programming
 - Option byte/OTP programming
 - Firmware authentication
- Added the support for devices in the [STM32C0 series](#) with 64 Kbytes and 256 Kbytes of flash memory
 - Internal memory programming
 - Option byte/OTP programming
- Updated the support for the [STM32H7R3/7S3](#) and [STM32H7R7/7S7](#) microcontrollers
 - MCE encryption
- Updated the support for the STM32MP13xx, STM32MP15xx, STM32MP23xx, and STM32MP25xx microprocessors
 - PMIC NVM management
 - Support of STM32MPUSSP-UTIL
 - New *Secrets Gen* panel
- Updated the support for the [STM32U5 series](#)
 - Option byte recovery
- [X-CUBE-RSSe](#) support
- Synthetic option byte view
- Project mode: import/export settings
- J-Link WB stack install

Refer to the *STM32CubeProgrammer software description* user manual ([UM2237](#)) for details.

3.5.2 Fixed issues

Table 2. Main issues fixed in STM32CubeProgrammer v2.18.0

| ID | Summary |
|--------|---|
| 106672 | [STM32CubeProg] Install STM32CubeProgrammer in silent mode. |
| 124828 | [API][Qt] Request to update Qt® libraries for STM32CubeProgrammer API. |
| 127932 | [Automatic Mode] Save the configuration for automatic mode. |
| 140592 | The start address of Flash-Bank2 is incorrect for the STM32U575VGT6 1-Mbyte flash memory. |

| ID | Summary |
|--------|--|
| 148558 | Recovery button for STM32U5 Cortex [®] -M33 MCUs. |
| 156588 | STM32U575xx 1-Mbyte part number programming does not work properly. |
| 158636 | [STM32H5] Unable to read/write/erase the external flash memory when TZEN is enabled. |
| 159313 | [Installer] Ability to install STM32CubeProgrammer via CLI. |
| 160547 | [ELF] Change file corrupted warning to be more specific. |
| 162736 | [STM32CubeProg][CLI]unable to modify the "unlock_2b" option byte value when programming option byte list (script). |
| 165967 | [GUI][Bootloader] Change "Error: RDP is set to level 1" to account for target under reset. |
| 169161 | [CLI][Write] STM32CubeProgrammer interprets an integer data as a hexadecimal number. |
| 173328 | [Documentation] Add more details about the number of bits on each write to the flash memory operation. |
| 173662 | [SPI][D450] Unable to program bank2 without skipping flash memory erase. |
| 175637 | [SDM][STM32H5][DA] Getting "PSA_LIFECYCLE_UNKNOWN" after running DA with nonsecure intrusive debug L3. |
| 181058 | [D466][Option bytes] Missing option bytes for 16-Kbyte part. |
| 181262 | [WB] DFUinterface issue with v2.16. |
| 181382 | [STM32H7] Wrong register bits description in STM32CubeProgrammer. |
| 181704 | [GUI/CLI] Erase sectors not working properly in STM32H503CB and STM32H503KB. |
| 182020 | [Database][D480] Wrong number/indexes of sectors for 1-Mbyte part. |
| 182035 | [GUI/CLI][STM32G4][J-Link] STM32CubeProgrammer fails to download via J-Link. |
| 182053 | [CLI][Linux] Text stays in bold after checking a version. |
| 182758 | [Flash loader][STM32L4] [FLASH][SWD] Sector erase failure for second memory bank. |
| 184149 | [DA][STM32H7Rx/7Sx] Wrong debug authentication chain. |
| 185618 | [DA] STM32CubeProgrammer dual NRST pulse issue in DISCOVERY mode. |
| 186575 | [CLI] STM32CubeProgrammer fails to erase EEPROM sector. |
| 187833 | [STM32H5 256 Kbytes] Issue with erasing sectors in Bank 2. |
| 188096 | [RDP] Unexpected error message is displayed when changing RDP level. |
| 188098 | [D451] Default flash memory size is displayed via bootloader interface. |
| 188508 | [D451] Wrong number of sectors for 1-Mbyte part in dual-bank via bootloader interface. |
| 188642 | [STM32H5][GUI] STM32CubeProgrammer halts and resumes MCU. |
| 188714 | [0x455][STM32U5 256 Kbytes] Missing configuration in database for device ID 0x455 with 256 Kbytes of flash memory. |
| 188716 | [Bootloader] STM32CubeProgrammer fails to erase sectors when SWAP_BANK=1. |
| 188770 | [CLI] Wrong message when using <code>-elbl</code> option via USB. |
| 188772 | [Documentation][UM2237] Add a note to <code>-elbl</code> description. |
| 188857 | [Bootloader][DBANK] STM32CubeProgrammer memory erasure issue via bootloader interface when DBANK=1. |
| 189739 | [Flash loader][STM32CubeIDE 1.16.0] Unable to debug with STM32L562-DK. |
| 189999 | [D479] Missing WRP option bytes on 256-Kbyte device. |
| 190206 | [GUI][J-Link] GUI crashes when trying to connect after closing STM32CubeProgrammer. |
| 190543 | [STM32CubeProg][STM32G474] Option byte write issue with J-Link. |
| 191297 | [Rev ID][STM32H56x] Revision ID does not appear. |
| 191506 | [Signing tool] Request to align key ID handling with the PKCS#11 standard. |
| 191898 | OEM keys not present in the FastROM script. |

| ID | Summary |
|--------|---|
| 193922 | [STM32L4 256 Kbytes][0x435] Incorrect sector number and erasure issue in STM32CubeProgrammer. |

3.5.3 Known problems and limitations

- Updater:
 - Upgrade issue using the second option “*Use System Proxy Parameters*”.
- Integrity check:
 - STM32H7 microcontrollers: support integrity check for SFI:
 - Only the devices with a bootloader version higher than 0x91 are supported.
 - STM32CubeProgrammer does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H73xxx microcontrollers: support integrity check for SFI/SFIx:
 - Only the devices with a bootloader version higher than 0x93 are supported.
 - STM32CubeProgrammer does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H7Bxxx microcontrollers: support integrity check for SFI/SFIx:
 - Only the devices with a bootloader version equal to or higher than 0x92 are supported.
 - STM32CubeProgrammer does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
- Auto mode (Linux® platform):
 - The SPI interface quits after starting without a connection.
- Debug authentication is not supported on Windows® 32 bits.
- SFIx fails for large flash memory content on Linux® via the UART interface.
- Unable to generate the CRC on an `.elf` file with relocated binary.
- USB interoperability issue on macOS® due to ST-LINK limitation.
- STM32G0 microcontrollers:
 - Option byte issue: impossible to decrease the RDP from the BB to AA state.
 - The `.hex` file does not modify the WRP and PCROP option bytes of bank 2 for the devices with 512 Kbytes of dual-bank flash memory.
- STM32H573xx microcontrollers:
 - Unable to read, write, or erase the external flash memory when TZEN is enabled
- STM32H7 microcontrollers:
 - STM32CubeProgrammer does not support SFI
- STM32H7Rx/7Sx microcontrollers:
 - Erasing the external flash memory is not supported using IWDG.
 - GUI: the external memory edition is not supported via USB and UART while connected to the OpenBootloader.
 - Option bytes are not supported via OpenBootloader interfaces.
- STM32L4R5xx microcontrollers:
 - Issues occur when erasing sectors in bank 2 memory with the UART interface
- STM32N6 microcontrollers:
 - Flash memory programming via the J-Link port is not supported
- STM32U3 microcontrollers:
 - Limited support via JTAG (functional only with mode = normal and reset = HWrst)
- STM32U5 microcontrollers:
 - STM32U535/545 microcontrollers: SFIx is not supported.
 - STM32U59xxx microcontrollers: SFIx only supports the area E installation with OTFDEC region 1 (region 0 on TPC).
 - STM32U59xxJ, STM32U5AxxJ, STM32U5Fxxx, and STM32U5Gxxx microcontrollers: wrong sector size when DBANK is equal to zero.
 - STM32U5Fxxx and STM32U5Gxxx microcontrollers: SFIA is not supported through SPI.

- STM32WBA microcontrollers:
 - OpenBootloader via SPI: cannot connect to STM32CubeProgrammer when the baud rate is below 3000 Bd.
 - “Connect Under Reset” is not working when called by STM32CubeIDE.

3.6 STM32CubeProgrammer v2.17.0 release information

3.6.1 New features

- Added the support for the STM32MP25xx microprocessors in the [STM32MP2 series](#)
 - Flash memory and OTP programming, SSP support
- Added the support for devices in the [STM32WB0 series](#): STM32WB05 MCU, STM32WB05 coprocessor, STM32WB07 MCU, and STM32WB09 MCU product lines
 - Flash memory and OTP programming via debug and UART
 - Key generation, software signature, and OTP provisioning
- Added the support for new STM32C0xxxB microcontrollers in the [STM32C0 series](#) with 128 Kbytes of flash memory
 - Flash memory and OTP programming via debug and UART

Refer to the *STM32CubeProgrammer software description* user manual ([UM2237](#)) for details.

3.6.2 Fixed issues

Table 3. Main issues fixed in STM32CubeProgrammer v2.17.0

| ID | Summary |
|--------|---|
| 126305 | [GUI][STM32U5] RDP regression with password level 2 not working on all operating systems. |
| 129215 | [STM32CubeProg] Add new features: save and load OB configurations. |
| 136058 | [STM32U575ZI] STM32CubeProgrammer bug affecting flash memory programming when flash memory banks are swapped. |
| 143616 | [STM32CubeProg] Need to update the nswab_BANK description according to the new reference manual changes. |
| 152836 | [Flash loader][STM32CubeProg][STM32H5] When SWAP_BANK is enabled, bank 1 erasing is not functional. |
| 168475 | [STM32G4][GUI/CLI] Wrong description of BOOT_LOCK option byte. |
| 171718 | [STM32WL5x] An external flash loader generated with STM32CubeIDE does not work with STM32CubeProgrammer. |
| 172137 | Invalid .efl file if the path uses valid UTF-8. |
| 172961 | [DATABASE][D415] Programming failure via the bootloader interface in the second bank of a 1-Mbyte target. |
| 175709 | [UART] Sending an additional page to be erased. |
| 175852 | [Bootloader] Programming at the address 0x0Cxxxxxx via the bootloader interface. |
| 175981 | [DATABASE][D492] Wrong multiplier for SECW option bytes via the bootloader interface. |
| 178135 | STM32CubeProgrammer fails to program via the USB. |
| 179072 | [STM32C0][0x443] Missing configuration in database for device ID 0x443 with 16 Kbytes of flash memory. |
| 179216 | [API] Deallocate memory after read. |
| 180037 | Calling the obDisplay API crashes. |

3.6.3 Known problems and limitations

- Integrity check:
 - STM32H7 microcontrollers: support integrity check for SFI/SMI:
 - Only the devices with a bootloader version higher than 0x91 are supported.
 - STM32CubeProgrammer does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H73xxx microcontrollers: support integrity check for SFI/SMI/SFIx:
 - Only the devices with a bootloader version higher than 0x93 are supported.
 - STM32CubeProgrammer does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H7Bxxx microcontrollers: support integrity check for SFI/SMI/SFIx:
 - Only the devices with a bootloader version equal to or higher than 0x92 are supported.
 - STM32CubeProgrammer does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
- Auto mode (Linux® platform):
 - The SPI interface quits after starting without a connection.
- Debug authentication is not supported on Windows® 32 bits.
- SFIx fails for large flash memory content on Linux® via the UART interface.
- Unable to generate the CRC on an `.elf` file with relocated binary.
- USB interoperability issue on macOS® due to ST-LINK limitation.
- STM32G0 microcontrollers:
 - Option byte issue: impossible to decrease the RDP from the BB to AA state.
 - The `.hex` file does not modify the WRP and PCROP option bytes of bank 2 for the devices with 512 Kbytes of dual-bank flash memory.
 - Unable to change the option bytes when WWDG_SW and IWDG_SW are unchecked.
- STM32G4 microcontrollers:
 - STM32CubeProgrammer fails to download via J-Link
 - STM32G491xx microcontrollers: Issues occur when erasing sectors in bank 2 memory
- STM32H573xx microcontrollers:
 - Unable to read, write, or erase the external flash memory when TZEN is enabled
- STM32H7 microcontrollers:
 - STM32CubeProgrammer does not support combined SFI and SMI.
 - SMI programming fails.
- STM32H7Rx/7Sx microcontrollers:
 - SFI is not supported.
 - Erasing the external flash memory is not supported using IWDG.
 - GUI: the external memory edition is not supported via USB and UART while connected to the OpenBootloader.
 - Option bytes are not supported via OpenBootloader interfaces.
- STM32MP25xx microprocessors:
 - SSP secret generation is not supported
- STM32U5 microcontrollers:
 - Impossible to modify the `unlock_2b` option byte value when programming the OB list.
 - STM32U535/545 microcontrollers: SFIx is not supported.
 - STM32U575VGT6: the start address of Flash-Bank2 is incorrect.
 - STM32U59xxx microcontrollers: SFIx only supports the area E installation with OTFDEC region 1 (region 0 on TPC).
 - STM32U59xxJ, STM32U5AxxJ, STM32U5Fxxx, and STM32U5Gxxx microcontrollers: wrong sector size when DBANK is equal to zero
 - STM32U5Fxxx and STM32U5Gxxx microcontrollers: SFIA not supported via SPI.

- STM32WBA microcontrollers:
 - OpenBootloader via SPI: cannot connect to STM32CubeProgrammer when the baud rate is below 3000 Bd.
 - “Connect Under Reset” is not working when called by STM32CubeIDE.

3.7 STM32CubeProgrammer v2.16.0 release information

3.7.1 New features

- Added the support for the [STM32U0 series](#)
 - Flash memory/option byte/OTP programming via the debug and bootloader interfaces
 - RDP regression with password
 - Added the support for the STM32H7Rx/7Sx microcontrollers in the [STM32H7 series](#)
 - Flash memory/option byte/OTP programming via the debug and bootloader interfaces
 - Debug authentication with a password or certificate
 - SFI support
 - Added the support for the STM32H523/533 microcontrollers in the [STM32H5 series](#)
 - Flash memory/option byte/OTP programming via the debug and bootloader interfaces
 - Debug authentication with a password or certificate
 - Authentication key provisioning
 - Key generation
 - Firmware encryption and signing
 - Certificate generation
 - SFI/SFIx support
 - Added the SFI support for the STM32WBA5xxx microcontrollers
 - Upgraded STM32CubeProgrammer to Qt 6
 - Added the support for the J-Link probe (SEGGER) without security features
 - Enhanced the security for the STM32H562 and STM32H563/H573 microcontrollers: new RSSe integrated
- Refer to the *STM32CubeProgrammer software description* user manual ([UM2237](#)) for details.

3.7.2 Fixed issues

Table 4. Main issues fixed in STM32CubeProgrammer v2.16.0

| ID | Summary |
|--------|---|
| 120183 | [GitHub] Issue with DFU programming on STM32F767xx MCUs. |
| 147113 | [STM32H5] Generation of DA_Config.obk, with only a public key as input, for the purpose of generating a file. |
| 151099 | [STM32F0][WRP] Wrong naming of WRP; STM32CubeProgrammer writes values to unused nWRPx/WRP _x (x=1,2,3). |
| 151146 | [SWD][STLINK-V3] Wrong frequency displayed in CLI. |
| 157815 | [Device_ID=0x415][GUI] Mass erase and sector erase are not working properly via the ST-LINK interface. |
| 159985 | Issue in comparing noncontiguous files. |
| 161502 | [STM32CubeProg][D461 512K][STM32L496RET6] Issue to program a binary: erase a sector is not possible. |
| 162313 | [STM32G031GU6] Wrong flash memory size of 63 Kbytes instead of 16 Kbytes. |
| 162318 | [DATABASE][SWD] Wrong flash memory size for 512-Kbyte parts (Device_ID=0x413). |
| 162579 | [automode][OB] Extend CLI check on invalid values to auto mode. |
| 163127 | [OB][VERIFY] Wrong value read for HDP2EN in 32-bit operating system. |
| 163297 | [OB][STM32G4] Issue when displaying option bytes in CLI via UART bootloader. |

| ID | Summary |
|--------|---|
| 163444 | [API] Compilation error occurs when <code>getStorageStructure()</code> is called. |
| 163911 | [API] The <code>editSector()</code> function is not working properly. |
| 165977 | [GUI][UART][DTR/RTS] Support controlling boot configuration via DTR/RTS in GUI. |
| 166376 | [BOR][DATABASE] Wrong description for BOR_LEV option byte. |
| 166453 | [GUI][CAN bootloader] Baud rate dropout menu missing the 1-Mbit/s speed. |
| 168475 | [STM32G4][GUI/CLI] Wrong description of the BOOT_LOCK option byte. |
| 169892 | [STM32H5][SWD] The flash memory is read as 0x00 when the boot is done from the system memory. |
| 171611 | [STM32U5][SWD] Unable to program internal memory in a specific OB config for 2-Mbyte parts. |
| 171746 | The SFI option bytes are not aligned with the reference manual. |
| 172980 | [STM32U5][TPC] Typo in SFI OB <code>.csv</code> files for STM32U5. |

3.7.3 Known problems and limitations

- Integrity check:
 - STM32H7 microcontrollers: support integrity check for SFI/SMI:
 - Only the devices with a bootloader version higher than 0x91 are supported.
 - STM32CubeProgrammer v2.16.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H73xxx microcontrollers: support integrity check for SFI/SMI/SFIx:
 - Only the devices with a bootloader version higher than 0x93 are supported.
 - STM32CubeProgrammer v2.16.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H7Bxxx microcontrollers: support integrity check for SFI/SMI/SFIx:
 - Only the devices with a bootloader version higher than 0x92 are supported.
 - STM32CubeProgrammer v2.16.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
- Auto mode (Linux® platform):
 - The SPI interface quits after starting without a connection.
- STM32G0 microcontrollers:
 - Option byte issue: impossible to decrease the RDP from the BB to AA state.
 - The `.hex` file does not modify the WRP and PCROP option bytes of bank 2 for the devices with 512 Kbytes of dual-bank flash memory.
 - Unable to change the option bytes when WWDG_SW and IWDG_SW are unchecked.
- STM32H523/533 microcontrollers:
 - Software reset is not performed after setting product_State to 0x2E via USB.
- STM32H523/533, STM32H562, and STM32H563/573 microcontrollers:
 - Wrong RSSe default filename in the SFI/SFIx panel.
- STM32H573xx microcontrollers:
 - Unable to read, write, or erase the external flash memory when TZEN is enabled
- STM32H7 microcontrollers:
 - STM32CubeProgrammer does not support combined SFI and SMI.
 - SMI programming fails.
- STM32H7Rx/7Sx microcontrollers:
 - Impossible to program the full extent of the external memory in one single shot with the OpenBootloader via USB. A workaround is to program in several runs of 64 Mbytes maximum.
 - GUI: the external memory edition is not supported via USB and UART while connected to the OpenBootloader.
- STM32U5 microcontrollers:
 - STM32U535/545 microcontrollers: SFIx is not supported.
 - STM32U575VGT6: the start address of Flash-Bank2 is incorrect.
 - STM32U59xxx microcontrollers: SFIx only supports the area E installation with OTFDEC region 1 (region 0 on TPC).
 - STM32U59xxx and STM32U5Axxx microcontrollers: wrong sector size when DBANK = 0 (16 Kbits instead of 8 Kbits).
 - RDP regression with password [level 2] is not supported on all platforms.
- STM32WBA microcontrollers:
 - OpenBootloader via SPI: cannot connect to STM32CubeProgrammer when the baud rate is below 3000 Bd.
- Debug authentication is not supported on Windows® 32 bits.
- SFIx fails for large flash memory content on Linux® via the UART interface.

3.8 STM32CubeProgrammer v2.15.0 release information

3.8.1 New features

- Added the NOR flash memory and OTP fuse programming via USB and UART using OpenBootloader for the STM32MP13xx microprocessors
- Added the support for the PMIC programming via UART for the STM32MP15xx microprocessors
- Added limited FASTROM support
- Added the automatic determination of the stack install address for the [STM32WB series](#) and [STM32WB0 series](#)
- Enhanced security: the link to sw-center.st.com is updated from STM32CubeProgrammer v2.15.0

Refer to the *STM32CubeProgrammer software description* user manual ([UM2237](#)) for details.

3.8.2 Fixed issues

Table 5. Main issues fixed in STM32CubeProgrammer v2.15.0

| ID | Summary |
|--------|--|
| 110598 | [STM32CubeProg][STM32WB] Incorrect installation address when the RF stack is already in place. |
| 124075 | [GUI][Device memory] Request to remove the <i>partitions</i> view from <i>Device memory</i> . |
| 140915 | [STM32L0][EEPROM] Error message when erasing the EEPROM via the ST-LINK interface. |
| 145199 | [STM32CubeProg-FUS] [STM32WB15xx] Missing the wireless stack version. |
| 150022 | [STM32F0][STM32CubeProg] Confusing naming and use of nWRP. |
| 150428 | [STM32WB][DFU] The [Start Wireless Stack] button is grayed in DFU mode. |
| 153021 | The STM32WB firmware upgrade of a large stack shows an error while it is passed. |
| 153419 | [Option bytes][Device_ID = 0x480] FLASH_OTPBL_PRG is missing from the OB list. |
| 155082 | Wrong RSS version displayed on STM32CubeProgrammer/CLI. |
| 155918 | [STM32CubeProg] The connection error popup cannot be closed using the [X] close button. |
| 158082 | [STM32L4][DBANK] Wrong sector size when DBANK is disabled. |
| 158507 | [DATABASE][OB] Missing nDBOOT OB for 1-Mbit targets. |
| 159061 | [Device_ID = 0x467][DATABASE] Remove testing on dual bank for the 128-Kbit flash memory size. |
| 159172 | [STM32CubeProg][API] Bank number is always zero due to a wrong name size in the <code>DeviceDataStructure.h</code> file: 32 instead of 64. |
| 159589 | Error returned by TPC when used embedded in STM32CubeMX. |
| 160062 | [DATABASE][OB] Write protection option bytes are not shown via the bootloader interface. |
| 161652 | [SFI] Instability on STM32U5 microcontrollers with official release 2.14.0. |

3.8.3 Known problems and limitations

- Integrity check:
 - STM32H7 microcontrollers: support integrity check for SFI/SMI:
 - Only the devices with a bootloader version higher than 0x91 are supported.
 - STM32CubeProgrammer v2.15.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H73xxx microcontrollers: support integrity check for SFI/SMI/SFIx:
 - Only the devices with a bootloader version higher than 0x93 are supported.
 - STM32CubeProgrammer v2.15.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H7Bxxx microcontrollers: support integrity check for SFI/SMI/SFIx:
 - Only the devices with a bootloader version higher than 0x92 are supported.
 - STM32CubeProgrammer v2.15.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
- Auto mode (Linux® platform):
 - The SPI interface quits after starting without a connection.
- STM32G0 microcontrollers:
 - Option byte issue: impossible to decrease the RDP from the BB to AA state.
 - The `.hex` file does not modify the WRP and PCROP option bytes of bank 2 for the devices with 512 Kbytes of dual-bank flash memory.
- STM32H573xx microcontrollers:
 - Unable to read, write, or erase the external flash memory when TZEN is enabled
- STM32H7 microcontrollers:
 - STM32CubeProgrammer does not support combined SFI and SMI.
 - SMI programming fails.
- STM32U5 microcontrollers:
 - STM32U535xx and STM32U545xx microcontrollers: SFIx is not supported because of a limitation with RSSe.
 - STM32U575VGT6: the start address of Flash-Bank2 is incorrect.
 - STM32U58xxx and STM32U59xxx microcontrollers: for the SFIx area K, the size value must be 32-bit aligned inside the `.kcsv` file.
 - STM32U59xxx microcontrollers: SFIx only supports the area E installation with OTFDEC region 1 (region 0 on TPC).
 - STM32U59xxx and STM32U5Axxx microcontrollers: wrong sector size when DBANK = 0 (16 Kbits instead of 8 Kbits).
 - RDP regression with password [level 2] is not supported on all platforms.
- STM32WBA microcontrollers:
 - SFI is not supported.
 - OpenBootloader via SPI: cannot connect to STM32CubeProgrammer when the baud rate is below 3000 Bd.
- Debug authentication is not supported on Windows® 32 bits.
- SFIx fails for large flash memory content on Linux® via the UART interface.

3.9 STM32CubeProgrammer v2.14.0 release information

3.9.1 New features

- Updated the support for the entire [STM32H5 series](#):
 - Debug authentication with password or certificate
 - Authentication key provisioning
 - Key generation
 - Firmware encryption and signing
 - Certificate generation
 - SFI support, secure manager install, and module install/update
- Added support for microcontrollers in the [STM32L5 series](#) and [STM32WL series](#):
 - SFI integrity check
- Updated support for the [STM32U5 series](#):
 - SFI support for the [STM32U535/545](#), [STM32U575/585](#), [STM32U595/5A5](#), and [STM32U599/5A9](#) product lines
- Added support for the STM32U5Fxxx and STM32U5Gxxx microcontrollers:
 - SFI support: user interface and command-line interface
 - SFlx support:
 - Via debug: user interface and command-line interface
 - Via bootloader: command-line interface only

Refer to the *STM32CubeProgrammer software description* user manual ([UM2237](#)) for details.

3.9.2 Fixed issues

Table 6. Main issues fixed in STM32CubeProgrammer v2.14.0

| ID | Summary |
|--------|--|
| 129635 | [STM32MP15x] 128-Gbyte SD™ card programming error. |
| 133771 | [STM32U5] Unable to set the OEM1 and OEM2 keys through DFU. |
| 136574 | [STM32 MPU][GUI] Unable to program a <code>.tsv</code> file when some partitions are not selected. |
| 143511 | [STM32CubeProg][macOS][Ventura][USB][ST-LINK] STM32CubeProgrammer hangs during connect. |
| 144488 | [GUI] Offset on more than 32 bits is not handled properly. |
| 145562 | Periodic reading of bootloader version via ST-LINK. |
| 145606 | [Live Update] Disable checkbox on connect. |
| 145769 | [STM32CubeProg] Quiet mode is disabled when <code>-xdu</code> is used. |
| 146512 | [STM32CubeProg][STM32U5][OpenBootloader] Failing to run OpenBootloader correctly. |
| 146515 | [STM32CubeProg][CLI] SWV is working only on port 0. |
| 148404 | [SVD][STM32C0] Integrate STM32C0 SVD files. |
| 149042 | [Database] Unable to program target via bootloader interfaces (Device_ID = 0x467). |
| 149212 | [GUI] Deselect all buttons not working properly in external loaders tab. |
| 149330 | [STM32CubeProg][STM32G0B1CBTx D467] Wrong flash memory size for STM32G0B1 with 128 Kbits. |
| 149467 | [Database] Unable to program target via ST-LINK interfaces (Device_ID = 0x470) |
| 150019 | [STM32CubeProg][EL] Wrong display of custom EL sectors. |

3.9.3 Known problems and limitations

- Integrity check:
 - STM32H7 microcontrollers: support integrity check for SFI/SMI:
 - Only the devices with a bootloader version higher than 0x91 are supported.
 - STM32CubeProgrammer v2.14.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H73xxx microcontrollers: support integrity check for SFI/SMI/SFIx:
 - Only the devices with a bootloader version higher than 0x93 are supported.
 - STM32CubeProgrammer v2.14.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H7Bxxx microcontrollers: support integrity check for SFI/SMI/SFIx:
 - Only the devices with a bootloader version higher than 0x92 are supported.
 - STM32CubeProgrammer v2.14.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
- Auto mode (Linux® platform):
 - The SPI interface quits after starting without a connection.
- STM32G0 microcontrollers:
 - The `.hex` file does not modify the WRP and PCROP option bytes of bank 2 for the devices with 512 Kbytes of dual-bank flash memory.
 - The SWD `RegDump` command-line instruction does not work on STM32G081xx devices.
- STM32H503 microcontrollers:
 - A total of 64 bytes are programmed to the OTP area with STM32CubeProgrammer after password provisioning.
- STM32H7 microcontrollers:
 - STM32CubeProgrammer does not support combined SFI and SMI.
 - SMI programming fails.
- STM32MP15x microprocessors:
 - An error occurs when saving the OTP partition in a macOS® platform.
- STM32U5 microcontrollers:
 - STM32U535xx and STM32U545xx microcontrollers: SFIx is not supported because of a limitation with RSSe.
 - STM32U58xxx and STM32U59xxx microcontrollers: for the SFIx area K, the size value must be 32-bit aligned inside the `.kcsv` file.
 - STM32U59xxx microcontrollers: SFIx only supports the area E installation with OTFDEC region 1 (region 0 on TPC).
 - STM32U5F9xx and STM32U5G9xx microcontrollers: DK2 is not supported because there is no OTFDEC on HSP11.
 - RDP regression with password [level 2] is not supported on all platforms.
- STM32WBA microcontrollers:
 - SFI is not supported.
 - OpenBootloader via SPI: cannot connect to STM32CubeProgrammer when the baud rate is below 3000 Bd.
- Azure® RTOS USBX:
 - For STM32U575/585 microcontrollers, erasing sectors from "C" to "F" fails.
 - For STM32U575/585 microcontrollers, some extra data is added at memory programming.
- Debug authentication is not supported on Windows® 32 bits.
- SFIx fails for small flash memory content on supported bootloader interfaces with all platforms.

3.10 STM32CubeProgrammer v2.13.1 release information

3.10.1 New features

- No new feature in STM32CubeProgrammer v2.13.1.
Refer to the sections [Fixed issues](#) and [Known problems and limitations](#) for details.

3.10.2 Fixed issues

Enhancements with macOS®.

3.10.3 Known problems and limitations

- Integrity check:
 - STM32H7 microcontrollers: support integrity check for SFI/SMI:
 - Only the devices with a bootloader version higher than 0x91 are supported.
 - STM32CubeProgrammer v2.13.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H73xxx microcontrollers: support integrity check for SFI/SMI/SFIx:
 - Only the devices with a bootloader version higher than 0x93 are supported.
 - STM32CubeProgrammer v2.13.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H7Bxxx microcontrollers: support integrity check for SFI/SMI/SFIx:
 - Only the devices with a bootloader version higher than 0x92 are supported.
 - STM32CubeProgrammer v2.13.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
- Auto mode (Linux® platform):
 - The SPI interface quits after starting without a connection.
- STM32G0 microcontrollers:
 - The `.hex` file does not modify the WRP and PCROP option bytes of bank 2 for the devices with 512 Kbytes of dual-bank flash memory.
 - The SWD `RegDump` command-line instruction does not work on STM32G081xx devices.
- STM32H5 microcontrollers:
 - SFI and SSFI (secure manager) are not supported.
- STM32H503 microcontrollers:
 - A total of 64 bytes are programmed to the OTP area with STM32CubeProgrammer after password provisioning.
- STM32H563/573 microcontrollers:
 - The high-cycle data programming of flash memory is not supported.
- STM32H7 microcontrollers:
 - STM32CubeProgrammer does not support combined SFI and SMI.
 - SMI programming fails.
- STM32MP15x microprocessors:
 - It is not possible to program a `.tsv` file when some partitions are not selected.
 - An error occurs when saving the OTP partition in a macOS® platform.
- STM32U5 microcontrollers:
 - RDP regression with password is not working on macOS®.
 - It is impossible to set OEM keys through DFU.
 - STM32U59xxx microcontrollers: SFIx only supports the area E installation with OTFDEC region 1 (region 0 on TPC).
 - STM32U58xxx and STM32U59xxx microcontrollers: for the SFIx area K, the size value must be 32-bit aligned inside the `.kcsv` file.
- STM32WBA microcontrollers:
 - SFI is not supported.
 - OpenBootloader via SPI: cannot connect to STM32CubeProgrammer when the baud rate is below 3000 Bd.
- Azure® RTOS USBX:
 - For STM32U575/585 microcontrollers, erasing sectors from "C" to "F" fails.
 - For STM32U575/585 microcontrollers, some extra data is added at memory programming.
- Debug authentication is not supported on macOS® and Windows® 32 bits operating systems.
- The input of checksum files other than `.bin` files is not supported in the GUI.

- The maximum firmware size for SFX generation with STM32U599xx/595xx/5A5xx microcontrollers is limited to 3 Mbytes.

3.11 STM32CubeProgrammer v2.13.0 release information

3.11.1 New features

- Added support for the [STM32H5 series](#):
 - [STM32H563/573](#)
 - Debug authentication with password or certificate
 - Authentication key provisioning
 - Key generation
 - Firmware encryption and signing
 - Certificate generation
 - [STM32H503](#)
 - Debug authentication with password or certificate
 - Firmware encryption and signing
- Added support for the [STM32WBA series](#):
 - Flash memory and OTP programming, and option bytes
- Updated support for the [STM32U5 series](#):
 - [STM32U535/545](#):
 - SFI support (user interface and command-line interface)
Flash memory, option bytes, RDP regression with password, and OTP programming.
 - [STM32U595/5A5](#):
 - SFI update (user interface and command-line interface)
 - SFIx update
 - Via debug: user interface and command-line interface
 - Via bootloader: command-line interface only
- Added support for the [STLINK-V3PWR](#):
 - Debug
 - Bridge to bootloader interfaces (SPI, I²C, and CAN)
- Added new calculator features:
 - Number converter
 - File and flash memory checksum

3.11.2 Fixed issues

Table 7. Main issues fixed in STM32CubeProgrammer v2.13.0

| ID | Summary |
|--------|---|
| 65657 | Add the checksum display as done for the ST-LINK utility. |
| 115829 | [TrustedPackageCreator] Register naming mismatch between reference manual and .csv file. |
| 116552 | Possible attack on OEM1KEY and OEM2KEY. |
| 129709 | External flash memory programming issues in auto mode. |
| 132975 | [STM32G0] <i>Erase selected sectors</i> does not work properly on 256-Kbyte devices. |
| 135017 | [STM32CubeProg-GUI] Message must be updated while disabling RDP regression with password value. |
| 135436 | [API] Unhandled exception in <code>writeMemory()</code> . |
| 136195 | [STM32CubeProg][STM32C0] STM32C031C4T6: wrong flash memory size. |
| 136233 | [CLI] ST-LINK serial number is ignored in shared mode. |

| ID | Summary |
|--------|--|
| 137687 | [CLI] Missing data from <code>hex</code> file when using <code>safelib</code> . |
| 138051 | [STM32CubeProg] STM32L5 CLI displaying protected flash memory content. |
| 140627 | [STM32CubeProg][STM32G4 256 Kbytes][0x495] <i>Erase selected sectors</i> does not work properly on 256-Kbyte devices with dual-bank mode (reopen). |
| 140973 | [STM32CubeProg-Connection] Connection issues for STM32G031J4M6. |
| 141725 | [STM32CubeProg][STM32G0 0x466] User configuration option bytes programming failed. |
| 142009 | [[STM32CubeProg-Register_Viewer] NVIC_ISERx[1-4] addresses are incorrect in the register viewer. |
| 142286 | [STM32H5][STM32CubeProg] Cannot erase the flash memory when TZEN=0. |

3.11.3 Known problems and limitations

- Integrity check:
 - STM32H7 microcontrollers: support integrity check for SFI/SMI:
 - Only the devices with a bootloader version higher than 0x91 are supported.
 - STM32CubeProgrammer v2.13.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H73xxx microcontrollers: support integrity check for SFI/SMI/SFIx:
 - Only the devices with a bootloader version higher than 0x93 are supported.
 - STM32CubeProgrammer v2.13.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H7Bxxx microcontrollers: support integrity check for SFI/SMI/SFIx:
 - Only the devices with a bootloader version higher than 0x92 are supported.
 - STM32CubeProgrammer v2.13.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
- Auto mode (Linux® platform):
 - The SPI interface quits after starting without a connection.
- STM32G0 microcontrollers:
 - The `.hex` file does not modify the WRP and PCROP option bytes of bank 2 for the devices with 512 Kbytes of dual-bank flash memory.
 - The SWD `RegDump` command-line instruction does not work on STM32G081xx devices.
- STM32H5 microcontrollers:
 - SFI and SSFI (secure manager) are not supported.
- STM32H503 microcontrollers:
 - A total of 64 bytes are programmed to the OTP area with STM32CubeProgrammer after password provisioning.
- STM32H7 microcontrollers:
 - STM32CubeProgrammer does not support combined SFI and SMI.
 - SMI programming fails.
- STM32MP15x microprocessors:
 - It is not possible to program a `.tsv` file when some partitions are not selected.
 - An error occurs when saving the OTP partition in a macOS® platform.
- STM32U5 microcontrollers:
 - RDP regression with password is not working on macOS®.
 - It is impossible to set OEM keys through DFU.
 - STM32U59xxx microcontrollers: SFIx only supports the area E installation with OTFDEC region 1 (region 0 on TPC).
 - STM32U58xxx and STM32U59xxx microcontrollers: for the SFIx area K, the size value must be 32-bit aligned inside the `.kcsv` file.
- STM32WBA microcontrollers:
 - SFI is not supported.
 - OpenBootloader via SPI: cannot connect to STM32CubeProgrammer when the baud rate is below 3000 Bd.
- Azure® RTOS USBX:
 - For STM32U575/585 microcontrollers, erasing sectors from "C" to "F" fails.
 - For STM32U575/585 microcontrollers, some extra data is added at memory programming.
- Debug authentication is not supported on macOS® and Windows® 32 bits operating systems.
- The input of checksum files other than `.bin` files is not supported in the GUI.
- The maximum firmware size for SFIx generation with STM32U599xx/595xx/5A5xx microcontrollers is limited to 3 Mbytes.

3.12 STM32CubeProgrammer v2.12.0 release information

3.12.1 New features

- STM32 Trusted Package Creator: enhanced SFI UI
- **STM32MP1 series**: added SSP GUI
- **STM32WB series**: upgraded the wireless stack and enhanced the UI
- **STM32WL series**: added module support
- Script manager: added loops and conditional statements
- External flash memory programmer: added [B-U585I-IOT02A](#) for SFlx
- **32F746GDISCOVERY** and **STM32F7508-DK**: new external flash loaders

3.12.2 Fixed issues

Table 8. Main issues fixed in STM32CubeProgrammer v2.12.0

| ID | Summary |
|--------|---|
| 116538 | [STM32CubeProg] Address is always equal to 0x0 when saving .bin as .hex file. |
| 117842 | [STM32CubeProg - STM32L0] EEPROM banks are erased for one EEPROM bank selected. |
| 119632 | [STM32L5][TZEN - Regression] Error occurred when TZEN regression field is checked. |
| 122194 | [API][ST-LINK] No error message displayed when the MCU is not powered. |
| 122254 | [STM32CubeProg - Documentation] Confusing behavior when running <code>-log</code> option without file specification. |
| 123745 | [STM32CubeProg][STM32MP1] Even if SWD is not supported for STM32MP1, STM32CubeProgrammer should not crash when trying to connect via SWD/JTAG. |
| 123960 | [GUI] Switching from one section to another in the OB panel does not keep the OB box modified status. |
| 126294 | [STM32CubeProg] Device connection issue after increasing the RDP level. |
| 127191 | [STM32MP157] Unselecting TSV partitions times out while programming. |
| 127922 | [CLI] CMD failure (<code>Error cmd</code>) should interrupt the execution of the script. |
| 128988 | [STM32CubeProg-GUI] STM32G0B0xx device missing from device list with ID 0x467. |
| 130116 | [STM32CubeProg] Error while programming a .bin file with more than 128 Kbyte flash memory size into an STM32L081xZ device. |
| 130186 | [STM32CubeProg] "start address" in download page is grayed while filename extension .BIN is uppercase. |
| 130479 | [GUI] "Select MCU" & "Program HSM" buttons must be grayed for a programmed HSM. |
| 131027 | [STM32CubeProg - Signing tool] Issue with SC-HSM / PKCS#11. |
| 131245 | [Documentation][UM2237][SWV][CLI] Add explanation for <code>-startswv</code> . |
| 131307 | [SFI on STM32H735xx] SFI window hanging. |
| 131765 | [STM32CubeProg][Flash loader] STM32CubeProgrammer does not accept external loader file name containing more than one dot. |
| 131834 | [STM32WB][Stack update] Request to change the error message when trying to update stack with anti-rollback activated without previous stack load. |
| 131987 | [CLI][Error level] Program returned 0 error code when error occurred. |
| 132049 | [DDR][CLI] Error occurs when loading DDR tool via the USB. |
| 132213 | [STM32CubeProg - GUI] Mismatch between STM32WB35xx reference manual and STM32CubeProgrammer database. |
| 132528 | [STM32U5] Register view issue after RDP1 password set/disable. |
| 132716 | [Documentation] UM2237 Rev 19: Incorrect description of I2C configuration panel. |
| 133012 | [GUI][STM32U5] Wrong number of sectors for 1-Mbyte device. |

| ID | Summary |
|--------|---|
| 133019 | [STM32CubePRG][ST-LINK] STM32CubeProgrammer keeps reading BL version and displays it in VB3 every second. |
| 133020 | [STM32CubeProg][STM32L0][GUI] BL version is not displayed. |
| 133242 | [API][USB] Cannot list USB_DFU with custom VID&PID. |
| 134250 | [Documentation][UM2237] Add explanation for <code>-startstack</code> . |
| 134536 | [STM32CubeProg][TPC] <i>"Generate Hash"</i> option must be checked by default. |
| 134843 | [Documentation][UM2238] TPC wrong example for area 'K' pairs. |
| 135228 | [SFI] Update option bytes notation in CSV files. |
| 135237 | [STM32CubeProg][CLI] Update of Inventek Wi-Fi® firmware fails on B-L475E-IOT01 and B-L4S5I-IOT01 boards. |
| 135272 | [STM32CubeProg][STM32U5] Wrong flash memory sector size for STM32U575ZI (2 Mbytes). |
| 135805 | [API] Update <code>getExternalLoader</code> parameter description to match current implementation. |
| 136171 | [STM32CubeProg - Documentation][AN5054] Confusing <i>"multi install"</i> expression in secure programming using STM32CubeProgrammer application note. |
| 137358 | [STM32G4] Erase selected sectors does not work properly on devices with 256 Kbytes of flash memory. |

3.12.3 Known problems and limitations

- Integrity check:
 - STM32H7 microcontrollers: support integrity check for SFI/SMI:
 - Only the devices with a bootloader version higher than 0x91 are supported.
 - STM32CubeProgrammer v2.12.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H73xxx microcontrollers: support integrity check for SFI/SMI/SFIx:
 - Only the devices with a bootloader version higher than 0x93 are supported.
 - STM32CubeProgrammer v2.12.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H7Bxxx microcontrollers: support integrity check for SFI/SMI/SFIx:
 - Only the devices with a bootloader version higher than 0x92 are supported.
 - STM32CubeProgrammer v2.12.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
- Command-line interface:
 - Shared devices listing issue with the `-l` option.
 - Missing data from the `.hex` file when using `safelib`.
- Graphical user interface:
 - Missing data from the `.hex` file when using `save to file`.
- Auto mode (Linux® platform):
 - The SPI interface quits after starting without a connection.
- STM32G0 microcontrollers:
 - The `.hex` file does not modify the WRP and PCROP option bytes of bank 2 for the devices with 512 Kbytes of dual-bank flash memory.
 - The SWD `RegDump` command-line instruction does not work on STM32G081xx devices.
- STM32H7 microcontrollers:
 - STM32CubeProgrammer does not support combined SFI and SMI.
 - SMI programming fails.
- STM32L4 microcontrollers:
 - The full chip erase fails in SPI mode for devices with 1 Mbyte of flash memory.
- STM32L4+ microcontrollers:
 - It is not possible to write the WRP1B and WRP2B option bytes for devices with 2 Mbytes of flash memory.
- STM32MP15x microprocessors:
 - It is not possible to program a `.tsv` file when some partitions are not selected.
 - An error occurs when saving the OTP partition in a macOS® platform.
- STM32U5 microcontrollers:
 - RDP regression with password is not working on macOS®.
 - It is impossible to set OEM keys through DFU.
- STM32WB1xxx microcontrollers:
 - The register dump command is not supported.
- Azure® RTOS USBX:
 - For STM32U575/585 microcontrollers, erasing sectors from "C" to "F" fails.
 - For STM32U575/585 microcontrollers, some extra data is added at memory programming.
 - For STM32F4 microcontrollers, rewriting at a same memory address fails.

3.13 STM32CubeProgrammer v2.11.0 release information

3.13.1 New features

- Tool updater: can be used to upgrade [STM32CubeProg](#) from v2.10.0 to v2.11.0 for all supported operating systems (Windows®, Linux®, and macOS®)
- SFI:
 - SFIx support for STM32U585xx microcontrollers via SWD/JTAG
 - New SFI/SFIx graphical user interface (GUI)
 - STM32 Trusted Package Creator SFI GUI enhancement
 - Support for integrity check with the [STM32H7 series](#) and [STM32U5 series](#) microcontrollers
- [STM32U5 series](#): the GUI supports the RDP regression with password
- New bootloader support for STM32L476xx microcontrollers
- STM32MP13xx microprocessors:
 - Flash memory load via USB-DFU/UART
 - SSP (command-line interface only)
 - OTP: CLI/GUI

3.13.2 Fixed issues

Table 9. Main issues fixed in STM32CubeProgrammer v2.11.0

| ID | Summary |
|--------|---|
| 90858 | [STM32WB][CLI] Optimize FUS, STACK, and USER app upgrading automatically. |
| 104409 | STM32H735xx: SFI is very slow over SWD. |
| 109240 | Extra option bytes displayed for STM32G030C6: PCROP1A_STRT, PCROP1A_END, PCROP1A_RDP, PCROP1B_STRT, PCROP1B_END, and BOOT_LOCK, SEC_SIZE. |
| 110205 | Fix the reading of CHIPID (DBGMCU_DBG_AUTH_DEVICE) on STM32U5 microcontrollers. |
| 110614 | [CLI] Error level not reported correctly for the CRC safety feature using STM32CubeProgrammer_CLI. |
| 117163 | Launch problem when running the STM32CubeProgrammer GUI from another path. |
| 119604 | [STM32G0][OB] Sector erase cannot be achieved and an STM32CubeIDE debug error occurs when nSWAP_BANK=0. |
| 120935 | STPC v2.9 cannot generate a correct .sfi file for SFI. |
| 121452 | [OB] Incorrect SRAM2b secure address for STM32WB1xxx. |
| 121713 | Typo: “Memory & file edition”. |
| 123018 | [STM32H7][CM4] BOOT_CM4_ADD cannot be modified. |
| 123740 | [DFU] The single-bank configuration is not correctly managed when connecting with DFU. |
| 123913 | [CLI] It is not possible to write the LOCK byte. |
| 123923 | [UART] A timeout error occurs while disabling the readout protection. |
| 124081 | Odd behavior in the log when connecting a NUCLEO-F030R8 using the GUI. |
| 124446 | Request to add documentation for the <code>getAuthID</code> command. |
| 124763 | [OB] Wrong description of nBOOT1. |
| 125039 | [DFU] Strange behavior when trying to program in single-bank mode. |
| 127099 | [STM32L4+] Fail to program STM32L4+ twice in a row. |
| 127311 | The tool closes automatically when trying to load a file the path of which contains Chinese characters. |
| 127978 | Typo in option bytes: IWGDSTDBY. |

3.13.3 Known problems and limitations

- No error message is displayed when the microcontroller is not powered.
- A segmentation fault occurs when the STM32CubeProgrammer CLI is executed with a wrong serial number.
- Integrity check:
 - STM32H7 microcontrollers: support integrity check for SFI/SMI:
 - Only the devices with a bootloader version higher than 0x91 are supported.
 - STM32CubeProgrammer v2.11.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H73xxx microcontrollers: support integrity check for SFI/SMI/SFIx:
 - Only the devices with a bootloader version higher than 0x93 are supported.
 - STM32CubeProgrammer v2.11.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - STM32H7Bxxx microcontrollers: support integrity check for SFI/SMI/SFIx:
 - Only the devices with a bootloader version higher than 0x92 are supported.
 - STM32CubeProgrammer v2.11.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
- STM32H7 microcontrollers:
 - STM32CubeProgrammer does not support combined SFI and SMI.
 - The Cortex[®]-M4 boot address for dual-core STM32H7 microcontrollers is not modified with the SWD interface.
- STM32U5 microcontrollers:
 - RDP regression with password is not working on macOS[®].
 - SFIx is not working for bootloader interfaces.
 - An RSS CMD error is returned on area C during SFI.
 - An incorrect RSSe version is displayed.
 - The SFI process is successful while generating errors.
 - RDP2 and BootLock fail to be programmed in one shot over SWD.
 - The SFI installation process ends with a segmentation fault (Linux[®]/macOS[®]).
- STM32G0 microcontrollers:
 - Extra data is added during OTP programming in DFU.
 - STM32G0B0xx device is missing from the device list with ID 0x467.
- STM32WB1xxx microcontrollers:
 - The register dump command is not supported.
- Azure[®] RTOS USBX:
 - For STM32U575/585 microcontrollers, erasing sectors from "C" to "F" fails.
 - For STM32U575/585 microcontrollers, some extra data is added at memory programming.
 - For STM32F4 microcontrollers, rewriting at a same memory address fails.

3.14 STM32CubeProgrammer v2.10.0 release information

3.14.1 New features

- Added the updater tool for automatic software updates.
The updater allows the users to make automatic updates of the software and its associated packages. It is available in all supported operating systems: Windows[®], Linux[®], and macOS[®].
- Added the support for OpenBootloader via UART for the [STM32U5 series](#).
- Added the possibility to unlock the STM32WL dual-core microcontrollers after a wrong option byte programming through a new button in the GUI or a command in the CLI.
- Added the support for the STM32U59x/5Ax microcontrollers.
- Added the SFI, SMI, and SFIx integrity check for the STM32H73xxx microcontrollers.

3.14.2 Fixed issues

Table 10. Main issues fixed in STM32CubeProgrammer v2.10.0

| ID | Summary |
|--------|---|
| 82999 | [STM32WB series] Make feature, RF Stack type, and version visible on GUI. |
| 83752 | Inconsistent use of bin option for STM32MP1 microprocessors. |
| 97890 | Integrate the modified <code>MT25TL01G_STM32H750B-DISCO.stldr</code> . |
| 102454 | Add spent time information for option byte configuration in log file. |
| 103609 | [Signing_Tool] Allow the change of the <code>-of</code> option on an already signed stm32 file. |
| 108742 | [STM32L151RD] Wrong flash memory size: 128 Kbytes instead of 384 Kbytes. |
| 111228 | Add <i>Check for updates</i> feature. |
| 112974 | STM32MPU157F-EV1 Android™ OS flash programming problem. |
| 113279 | Confusing behavior with NUCLEO-U575ZI-Q at 1.8 V with the SWD frequency max. |
| 116342 | Add the support of <code>.s19</code> files. |
| 118242 | STM32CubeProgrammer fails to program an image bigger than 2.8 Kbytes and shows a wrong image size. |
| 118288 | OpenBootloader via SPI receives a checksum as flash memory page number in <code>Erase Memory</code> command from STM32CubeProgrammer. |
| 118871 | [FlashLoader] Update the external flash loader <code>0x450.stldr</code> . |
| 119243 | [STM32G0 series][GUI] Wrong RAM_PARITY_CHECK description. |
| 119940 | RDU fails on STM32G071xx with RDP set to BB and active bootlock on last cut. |
| 120160 | <code>nRST_STOP</code> , <code>nRST_STDBY</code> , and <code>nRST_SHDW</code> option byte values are modified when scrolling the user configuration option bytes TAB. |
| 120954 | The <code>.bin</code> file cannot be programmed successfully on STM32L4R9xx microcontrollers. |
| 121620 | TFA decryption fails. |

3.14.3 Known problems and limitations

- SFI, SMI, and SFIx integrity check for the STM32H73xxx microcontrollers:
 - Only the devices with a bootloader version higher than 0x93 are supported.
 - STM32CubeProgrammer v2.10.0 does not support the devices with older bootloader versions. Users must use STM32CubeProgrammer v2.9.0 to use devices with old bootloader versions.
 - Users who want to use SFI on STM32H75xxx and STM32H7Bxxx must use STM32CubeProgrammer v2.9.0 because of the broken compatibility with the integrity check mechanism.
 - Refer to the application note [AN5054](#) for details.
- STM32CubeProgrammer does not support combined SFI and SMI for STM32H7 microcontrollers.
- Azure® RTOS USBX:
 - For STM32U575/585 microcontrollers, erasing sectors from "C" to "F" fails.
 - For STM32U575/585 microcontrollers, some extra data is added at memory programming.
 - For STM32F4 microcontrollers, rewriting at a same memory address fails.
- STM32 Trusted Package Creator on macOS® fails to detect the HSM card reader.
- STM32 Trusted Package Creator on Linux® does not detect HSM only when the TPC CLI is launched in `sudo` mode.
- STM32WB55xx microcontrollers / FUS_StackWB: After key update, the progress bar remains red even after the programming ended successfully.
- Address is always equal to 0x0 when saving a binary as `.hex` file.
- STM32CubeProgrammer fails to program any option byte or memory when `WWDG_SW` (or `IWDG_SW`) is unchecked.

3.15 STM32CubeProgrammer v2.9.0 release information

3.15.1 New features

- Updated the programming mechanism for the STM32U575/585 microcontrollers via a flash loader usage in TZEN = 0, and a new programming mode in TZEN = 1 (fast/reliable)
- Added double authentication and user keys provisioning via JTAG for the [STM32WB series](#) (feature only available with bootloader in previous versions)
- Added a scripting mode including the existing STM32CubeProgrammer CLI commands and a new macro for data manipulation
- Added the support for the PKCS#11 protocol in the signing tool ([STM32MP1 series](#)).
- Added the support for Microsoft® Azure® RTOS USBX DFU stack

3.15.2 Fixed issues

Table 11. Main issues fixed in STM32CubeProgrammer v2.9.0

| ID | Summary |
|--------|---|
| 110368 | STM32L5 RSSe binary must be renamed as it handles both the JTAG and bootloader. |
| 110531 | [FUS] Upgrade problem with STM32CubeProgrammer of <code>stm32wb5x_FUS_fw.bin</code> version v1.2.0 on STM32WB55RE (512 Kbytes). |
| 111044 | Programming issue using STM32L486xx microcontrollers connected via USB. |
| 111417 | Changing secure area errors. |
| 112266 | Debug in Low-power mode does not display the right state of Stop bit. |
| 112484 | New ST-LINK firmware release V3J8M3 should limit bandwidth to 1 MHz on GUI. |
| 112545 | CLI does not read all requested upload sizes. |
| 113605 | [CLI] Erase all not possible using UART. |
| 113806 | [OB] Error message when configuring WRP option byte. |
| 114171 | STM32CubeProgrammer fails to program large segmented <code>.hex</code> file to external flash memory. |
| 114893 | [STM32G4] Bit SEC_SIZE1[8] cannot be programmed. |
| 115651 | Issue with option bytes IWDG and WWDG for STM32G07xxx devices. |
| 116070 | Programmer failed to load bin on STM32U5 with newer ST-LINK firmware version. |

3.15.3 Known problems and limitations

- *Script Manager* on Linux®: STM32CubeProgrammer does not recognize a `.prg` file when the directory name contains capital letters.
- STM32 Trusted Package Creator on macOS® fails to detect the HSM card reader.
- STM32 Trusted Package Creator on Linux® does not detect HSM only when the TPC CLI is launched in `sudo` mode.
- STM32WB55xx microcontrollers / FUS_StackWB: After key update, the progress bar remains red even after the programming ended successfully.
- Address is always equal to 0x0 when saving a binary as `.hex` file.
- STM32CubeProgrammer fails to program any option byte or memory when WWDG_SW (or IWDG_SW) is unchecked.
- [STM32L151RD](#): wrong flash memory size displayed (128 Kbytes instead of 384 Kbytes).

3.16 STM32CubeProgrammer v2.8.0 release information

3.16.1 New features

- Added the support for the STM32U575/585 microcontrollers
- Added the support for SFI via the JTAG interface for the STM32U575/585 microcontrollers
- Added the support for SFI via the bootloader interface (USB/UART/I²C/SPI) for the STM32U5 series
- Added the new *FUS-Operator* support for the STM32WB1xxx microcontrollers
- Added the anti-rollback support for the STM32WB1xxx and STM32WB5xxx microcontrollers
- Added the support of FUS version display for the STM32WB1xxx and STM32WB5xxx microcontrollers
- Added the support for the *Live Grid Update* feature
- Added the support for the *Blank check* feature
- Added the support for the *Memory/File* and *File/File* compare features
- Added the support for the bootloader version display feature
- Added the support for the *Fill memory* feature
- Added the support for UART interface RTS/CTS signals
- Added the support for *Connect while Watchdog enabled*

3.16.2 Fixed issues

Table 12. Main issues fixed in STM32CubeProgrammer v2.8.0

| ID | Summary |
|--------|---|
| 64267 | Fix issues with FUS via USART boot mode. |
| 72832 | Fix issue with MCU ID for STM32L151VB and STM32L151CB. |
| 74327 | [STM32F7] Unable to read/write data when option byte WWDG_SW is unchecked. |
| 76440 | [STM32WB] Extra bytes are programmed. |
| 81646 | STM32G431RB WRP modification. |
| 93987 | [STM32G0][GUI] Remove NRST_MODE. |
| 100150 | Programming of option bytes list is not completed when SEC_SIZE option byte is programmed. |
| 101017 | [STM32H7] Completes data with 00 bytes at the end. |
| 102103 | [STM32G0] Bit field for BORR and BORF are swapped. |
| 104089 | [STM32G0] Only half of the flash memory is shown under “Erase & Programming”, and missing DUAL_BANK and (n)SWAP_BANK option bytes for STM32G0[B-C][0-1]xE microcontrollers. |
| 104320 | [STM32WB] The stack “Firmware delete” button is greyed out when connected through USB DFU. |
| 104707 | [STM32U5] RDP regression is not possible when IWDG_SW/WWDG_SW option byte is set to 1. |
| 104832 | [STM32G0] NRST_MODE option byte has been removed for STM32G0x1xx microcontrollers. |
| 105628 | [STM32U5] HDP option byte is not visible. |

3.16.3 Known problems and limitations

- [STM32G030C6](#): extra option bytes displayed (PCROP1A_STRT, PCROP1A_END, PCROP1A_RDP, PCROP1B_STRT, PCROP1B_END, BOOT_LOCK, SEC_SIZE)
- [STM32L151RD](#): wrong flash memory size displayed (128 Kbytes instead of 384 Kbytes)
- [STM32U575/585](#) microcontrollers:
 - Bootloader version is not displayed
 - SFI via SWD fails when a USB cable is connected (a workaround is to use a USB wall charger)
 - On macOS®, after setting RDP to 0x55 and TZEN to 0x1, a connection via SWD crashes the tool
 - Regression with PWD from L1 to L0.5 fails
- STM32 Trusted Package Creator tool: HSM cannot be detected on macOS®
- Frequency parameters are not respected
- Azure® RTOS USBX is not supported

3.17 STM32CubeProgrammer v2.7.0 release information

3.17.1 New features

- Added the support for the STM32WB15xx microcontrollers in the [STM32WB series](#)
- Added the support for the microcontrollers with 64 Kbytes of flash memory in the [STM32G0 series](#)
- Added *HardFault Analyzer* support
- Added *Register viewer* support

3.17.2 Fixed issues

Table 13. Main issues fixed in STM32CubeProgrammer v2.7.0

| ID | Summary |
|-------|--|
| 58716 | Error when trying to mass erase STM32L0 128K devices from the CLI. |
| 61638 | DFU error with the STM32F746xx microcontrollers. |
| 64229 | Impossible to erase sector 128 and upper on STM32L476RG with STM32CubeProgrammer. |
| 73928 | [STM32CubeProgrammer] [CLI][STM32H7] STM32H745I-DISCO: Unable to read big data from the external Q-SPI flash memory. |
| 78496 | STM32F756 DFU <i>Application Example</i> does not work with STM32CubeProgrammer. |
| 80586 | [STM32CubeProgrammer][STM32L0][CLI] Verification progress bar ends at 24%. |
| 85736 | Empty area in HEX file is not well detected by STM32CubeProgrammer. |
| 85898 | Option byte IRHEN must be deleted from STM32CubeProgrammer. |
| 89140 | [STM32CubeProgrammer v2.4.0][CLI]: Failure to recover when using the <code>rdu</code> option. |
| 90060 | [STM32L4] STM32L496: Cannot connect using DFU IAP. |
| 91608 | Programming issue using the NUCLEO-L053R8 board connected via the USB. |
| 92200 | [CLI] I ² C Host sends out 2 extra bytes in the <code>NoStretchErase</code> command. |
| 92774 | [UART] STM32CubeProgrammer cannot program STM32H7 microcontrollers between 0x0810 0040 and 0x0810 0800. |
| 93858 | STM32H757 bootloader UART – <code>.hex</code> download verify error on the Cortex®-M4. |
| 93887 | STM32F765xG (1 Mbyte): Not possible to program the flash memory using STM32CubeProgrammer v2.5.0. |
| 94636 | [STM32CubeProgrammer-IAP] DFU programming failed. |
| 96040 | [GUI] When “ <i>No STM32 target found</i> ” STM32CubeProgrammer cannot be closed. |
| 96295 | STM32L471: Not able to successfully connect to or program using the UART. |
| 96905 | [Prg-DB] Wrong flash memory size for STM32L151xx devices. |

| ID | Summary |
|-------|--|
| 97365 | USB DFU connection issue using the NUCLEO-L152RE board. |
| 98346 | STM32L073: STM32CubeProgrammer v2.6.0 is not able to successfully connect to STM32L073xx microcontrollers using DFU. |
| 98682 | Programming issues using STM32L471VET6. |
| 99401 | [STM32G4 128K][GUI] Wrong WRP1A/B_END and WRP1A/B_STRT values for 128 Kbytes. |
| 99963 | STM32CubeProgrammer_API document update for STM32MP1 microprocessors. |

3.17.3 Known problems and limitations

- Installer: Insignificant message is returned during the installation when other instances are already installed.
- On some macOS[®] machines, the STM32CubeProgrammer GUI fails to launch (must be launched in CLI mode).
- STM32WB1xxx: Incorrect PCROP area management via SWD.
- STM32WB1xxx: STM32CubeProgrammer is closed when trying to apply FUS upgrade via the UART on macOS[®]. Only tty is supported.
- STM32WB5xxx: Making FUS upgrade via the SWD interface from recent to older version returns success message.
- Programming of option byte list is not completed when the SEC_SIZE option byte is programmed on some STM32 microcontroller or microprocessor series.
- STM32WB1xxx: STM32Key Provisioning/Double signature is not tested.

3.18 STM32CubeProgrammer v2.6.0 release information

3.18.1 New features

- Added the support for dual-core microcontrollers in the STM32WL series
- Extended the support for the STM32G0 series to the new STM32G0Bxxx and STM32G0Cxxx microcontrollers
- Added SFI support via JTAG/UART/SPI for STM32WL5xxx microcontrollers
- Added SFI support via I²C/SPI for STM32L5 series microcontrollers
- Added SFIx support via bootloader for STM32L5 series microcontrollers
- Added Sigfox[™] credential provisioning support for STM32WL5xxx microcontrollers
- Added multiple flash loader support for STM32 microcontrollers
- Added the SWV feature update (color support)
- Added the support for the server client HSM feature
- Added DFU IAP using custom PID/VID support for all STM32 products
- Added installer JRE bundle (OpenJDK)

3.18.2 Fixed issues

Table 14. Main issues fixed in STM32CubeProgrammer v2.6.0

| ID | Summary |
|-------|---|
| 57835 | Connect under reset with hardware reset is not working with SensorTile in low-power mode. |
| 58716 | Error when trying to mass erase STM32L0 128-Kbyte devices from CLI. |
| 62639 | RDP regression is not functional via BootLoader interfaces for STM32F030R8. |
| 64267 | Issues with FUS via USART boot mode. |
| 70556 | [STM32CubeProgrammer] Bug with STM32F765IGT6 (1-Mbyte flash memory). |
| 76987 | [STM32CubeProg-STM32L5] STM32Cubeprog crash when communicating with the DFU app. |

| ID | Summary |
|-------|--|
| 80586 | [STM32CubeProg-STM32L0][CLI] Verification progress bar ends at 24%. |
| 81647 | User DFU functionality with STM32CubeProgrammer. |
| 85313 | [STM32CubeProg-STM32G0][GUI] Remove the BOR level section. |
| 85898 | Option Byte IRHEN must be deleted from STM32CubeProgrammer. |
| 85960 | STM32G431: STM32CubeProgrammer automatic mode download issue. |
| 86576 | [PRG] Fail to write OTP with STM32G4. |
| 90060 | [STM32CubeProgrammer][STM32L4] STM32L496: cannot connect using DFU IAP. |
| 91608 | Programming issue using the NUCLEO-L053R8 board connected via USB. |
| 92038 | STM32CubeProgrammer fails to open on macOS® Catalina v 10.15.6. |
| 92280 | [STM32CubeProg-STM32H7][GUI] <i>Erasing & Programming</i> panel hangs (white panel). |
| 92477 | [STM32CubeProg-STM32L1][GUI] Incorrect memory size displayed and cannot make the erase. |
| 92641 | [STM32CubeProgrammer-Option Bytes] Cannot set STM32H743 IO_HSLV, VDDIO_HSLV option bit. |
| 92674 | [STM32CubePrg][STM32L0][DIE417] Cannot connect to board under reset when low-power mode is activated. |
| 92828 | Seems no compatible OpenJFX version for Ubuntu® 20.04 for OpenSTLinux. |
| 93013 | [CubePRG] <code>CubeProgrammer_API.h</code> not up to date in last STM32CubeProgrammer: missing 100 bytes in <code>debugConnectParameters</code> struct. |
| 93887 | [CubePRG][0x451] Not possible to program STM32F765xG (flash 1 Mbyte) using STM32CubeProgrammer v2.5.0. |
| 93987 | [STM32CubeProg-STM32G0][GUI] Remove NRST_MODE. |
| 94517 | [CubePRG] Cannot write 8 bytes in flash memory using <code>-w64</code> STM32CubeProgrammer CLI command. |

3.18.3 Known problems and limitations

- STM32MP1 microprocessor `get_certificate` operation is not complete in the UART mode.
- Production programming issues occur when using multiple ST-LINK in parallel.
- Installer: Insignificant message is returned during the installation when other instances are already installed.
- On some macOS® machines, the STM32Cubeprogrammer GUI fails to launch (must be launched in CLI mode).
- The SFI operation via UART is not achieved and returns an error on macOS® machines.
- STM32WB55: When trying to upgrade more than one stack, the operation can be done only with a second try.
- SFlx operation for STM32L5: An exception appears while programming when the `-e1b1` command is not the first one in the command line.

3.19 STM32CubeProgrammer v2.5.0 release information

3.19.1 New features

- Added the support for STM32G491xC and STM32G491xE microcontrollers
- Added the support for STM32H72xxx and STM32H73xxx microcontrollers
- Added SFI support for STM32H72xxx and STM32H73xxx microcontrollers
- Added SFIx support for STM32H72xxx and STM32H73xxx microcontrollers
- Added SFI support via UART for STM32L5 series microcontrollers
- Added SFI support via USB for STM32L5 series microcontrollers
- Added support of Serial Wire Viewer (SWV)
- Board automatic recognition
- Revision ID display

3.19.2 Fixed issues

Table 15. Main issues fixed in STM32CubeProgrammer v2.5.0

| ID | Summary |
|-------|--|
| 59191 | [STM32WB] Unable to remove or install the RF stack over UART bootloader + RSS. |
| 60618 | Erase of EEPROM memory of STM32L051 using the <i>Erase selected sectors</i> option. |
| 62173 | Cannot connect to STM32F072 DFU system bootloader. |
| 65682 | [UART] Cannot update option bytes with the UART. |
| 66596 | [UART] STM32L010 can be programmed. |
| 67646 | CLI missing <code>-w64</code> command + OTP area not programmed via SWD. |
| 68736 | [STM32F7] Unable to erase multiple sectors for dual-bank flash memory. |
| 68990 | [secure boot] Key generation not functional with <code>STM32MP_KeyGen_CLI</code> on Linux®. |
| 70592 | Start address box is activated after programming <code>.hex</code> file. |
| 71108 | [STM32H7] Flash programming through SWD in SFI mode finishes with errors. |
| 73495 | [STM32F072] Cannot remove read protection through DFU bootloader. |
| 79494 | Fail to program STM32L0 MCUs |
| 79912 | [STM32F446] Flash memory size register reading with bootloader interface. |
| 82752 | [UART][CLI] Upload size is bigger than flash memory size. |
| 82867 | <i>Firmware Upgrade Service</i> panel does not disappear on disconnect from compatible device. |
| 83296 | [STM32L5][SFI]: SFI fails if RDP is set to 0 or 1 in <code>.csv</code> file or if start SFI with <code>TZEN=1</code> . |
| 83346 | <code>GetCubeprogrammer_API.lib</code> compiled with x64. |
| 88504 | Issues with programming 512-Kbyte memory with 362-Kbyte file. |
| 89436 | Failure to program binary to STM32L4 Nucleo board with the CAN interface using bootloader. |

3.19.3 Known problems and limitations

- For the STM32L5 series, the Option Byte programming GUI is not intuitive enough. Refer to the *STM32L552xx and STM32L562xx advanced Arm[®]-based 32-bit MCUs* reference manual (RM0438) for permitted accesses.
- For STM32L4Pxxx and STM32L4Qxxx devices, Option Byte programming via bootloader interfaces presents some limitations.
- For STM32H7Axxx and STM32H7Bxxx devices, Option Byte programming via bootloader interfaces (USB) presents some limitations.
- STM32L5 series programming presents limitation in macOS[®] when `TZEN=1` and `RDP=0x55`.
- Display issues depending on the monitor used can occur with Linux[®].
- SFI-HSM V2 *get certificate* fails with STM32L462xx devices.
- Parallel flash programming fails in macOS[®] for microprocessors in the STM32MP1 series.
- Issues can be reported with some specific `.elf` file in CRC calculation safety feature.
- STM32MP1 microprocessor programming via UART presents limitations to program the whole boot chain.
- STM32MP1 microprocessor *get certificate* operation is not completed via a UART connection.

3.20 STM32CubeProgrammer v2.4.0 release information

3.20.1 New features

- Added the support of the graphic user interface (GUI) for the firmware upgrade service (FUS) and stack upgrade for the entire STM32WB series
- Support of HSM V2 on all STM32 microcontrollers and microprocessors supporting SFI/SSP

3.20.2 Fixed issues

Table 16. Main issues fixed in STM32CubeProgrammer v2.4.0

| ID | Summary |
|-------|--|
| 77015 | STM32CubeProgrammer cannot connect ST-LINK without the mass storage feature. |
| 78350 | STM32CubeProgrammer issue with STM32G0 when programming binary (size 18448 bytes). |

3.20.3 Known problems and limitations

- For the STM32L5 series, the connection via the ST-LINK protocol is allowed only when mode is set to *hotplug* with `TZEN=1`.
- For the STM32L5 series, the Option Byte programming GUI is not intuitive enough. Refer to the *STM32L552xx and STM32L562xx advanced Arm[®]-based 32-bit MCUs* reference manual (RM0438) for permitted accesses.
- For STM32L4Pxxx and STM32L4Qxxx devices, Option Byte programming via bootloader interfaces presents some limitations.
- For STM32H7Axxx and STM32H7Bxxx devices, Option Byte programming via bootloader interfaces (USB) presents some limitations.
- STM32L5 series programming presents limitation in macOS[®] when `TZEN=1` and `RDP=0x55`.
- With some small-screen resolutions, the graphical interface of STM32CubeProgrammer presents anomalies such as inaccessible buttons.
- Display issues depending on the monitor used can occur with Linux[®].
- SFIx on STM32H743/753 devices fails via the debug interface.
- SFI-HSM V2 *get certificate* fails with STM32L462xx devices.
- Parallel flash programming fails in macOS[®] for microprocessors in the STM32MP1 series.

3.21 STM32CubeProgrammer v2.3.0 release information

3.21.1 New features

- Added the support of the STM32L4Pxxx and STM32L4Qxxx microcontrollers
- Added the support of the STM32H7Axxx and STM32H7Bxxx microcontrollers
- Added the beta support of the STM32WL series microcontrollers
- Added the official support of the STM32L5 series microcontrollers
- Added the support of HSM V1 SFI/SFIx for STM32H7Axxx microcontrollers
- Added the support of HSM V1 SFI/SFIx for STM32L5 series microcontrollers

3.21.2 Fixed issues

Table 17. Main issues fixed in STM32CubeProgrammer v2.3.0

| ID | Summary |
|-------|--|
| 63887 | STM32CubeProgrammer does not program Option Byte from an <code>.hex</code> file for STM32F446xx. |
| 64229 | STM32Cubeprogrammer does not erase sector 128 and upper on STM32L476RG. |
| 66609 | STM32CubeProgrammer programming request of OTP byte via STM32CubeProgrammer. |
| 67025 | STM32Cubeprogrammer cannot connect ST-LINK without the mass storage feature. |
| 77015 | Support flash loader for STM32F769-EVAL:MT25QL512. |

3.21.3 Known problems and limitations

- For the STM32L5 series, the connection via the ST-LINK protocol is allowed only when mode is set to *hotplug* with `TZEN=1`.
- For the STM32L5 series, the Option Byte programming GUI is not intuitive enough. Refer to the *STM32L552xx and STM32L562xx advanced Arm®-based 32-bit MCUs* reference manual (RM0438) for permitted accesses.
- For STM32L4Pxxx and STM32L4Qxxx devices, Option Byte programming via bootloader interfaces presents some limitations.
- For STM32H7Axxx and STM32H7Bxxx devices, Option Byte programming via bootloader interfaces (USB) presents some limitations.
- STM32L5 series programming presents limitation in macOS® when `TZEN=1` and `RDP=0x55`.
- With some small-screen resolutions, the graphical interface of STM32CubeProgrammer presents anomalies such as inaccessible buttons.
- Display issues depending on the monitor used can occur with Linux®.

3.22 STM32CubeProgrammer v2.2.1 release information

3.22.1 New features

No new feature is reported for this release. Minor release v2.2.1 is dedicated to issue correction (refer to [Fixed issues](#)).

3.22.2 Fixed issues

Table 18. Main issue fixed in STM32CubeProgrammer v2.2.1

| ID | Summary |
|-------|--|
| 74031 | STM32CubeProgrammer issue programming STM32H7 Rev V via DFU. |

3.22.3 Known problems and limitations

- For the STM32L5 series, the connection via the ST-LINK protocol is allowed only when mode is set to *hotplug*.
- For the STM32L5 series, the Option Byte programming GUI is not intuitive enough. Refer to the *STM32L552xx and STM32L562xx advanced Arm®-based 32-bit MCUs* reference manual (RM0438) for permitted accesses.
- For the STM32L5 series, Option Byte programming via bootloader interfaces presents some limitations.
- STM32CubeProgrammer does not work under Ubuntu® 18.04.
- With some small-screen resolutions, the graphical interface of STM32CubeProgrammer presents anomalies such as inaccessible buttons.
- Display issues depending on the monitor used can occur with Linux®.

3.23 STM32CubeProgrammer v2.2.0 release information

3.23.1 New features

- Added the support of the STM32L5 series
- Added the support of HSMv2
- Added the support of IAP for the USB-DFU interface
- STM32WB firmware upgrade via the ST-LINK interface
- Added the support of OTP for the STM32L5 series
- Added the support of SSP for the STM32MP1 series

3.23.2 Fixed issues

Table 19. Main issues fixed in STM32CubeProgrammer v2.2.0

| ID | Summary |
|-------|--|
| 58587 | STM32CubeProgrammer does not support STM32L433RC-P and STM32L433RB. |
| 61375 | STM32L073 - How to erase the Data EEPROM? |
| 61731 | CLI Device not supported but programmed. RDP not programmed. |
| 68802 | USB DFU for STM32L452 and STM32L476 device IDs is unknown while connecting with the USB. |
| 68916 | STM32CubeProgrammer does not recognize STM32F413 USB DFU. |
| 69927 | IAP DFU is not working with STM32CubeProgrammer: <code>Error Unknown</code> or unsupported device (<code>DevID = 0x0000</code>). |
| 71074 | STM32CubeProgrammer v2.1.0 defect - DFU mode sector erase fails on STM32H743 2MB Rev V. |

3.23.3 Known problems and limitations

- For the STM32L5 series, the connection via the ST-LINK protocol is allowed only when mode is set to *hotplug*.
- For the STM32L5 series, the Option Byte programming GUI is not intuitive enough. Refer to the *STM32L552xx and STM32L562xx advanced Arm®-based 32-bit MCUs* reference manual (RM0438) for permitted accesses.
- For the STM32L5 series, Option Byte programming via bootloader interfaces presents some limitations.
- STM32CubeProgrammer does not work under Ubuntu® 18.04.
- With some small-screen resolutions, the graphical interface of STM32CubeProgrammer presents anomalies such as inaccessible buttons.

3.24 STM32CubeProgrammer v2.1.0 release information

3.24.1 New features

- Added support of STM32G4 series
- Added support of STM32G03x/STM32G04x microcontrollers
- Added support of dual-core microcontrollers in the STM32H7 series
- Added support of secure firmware install (SFI)

3.24.2 Fixed issues

Table 20. Main issues fixed in STM32CubeProgrammer v2.1.0

| ID | Summary |
|-------|---|
| 62057 | Download file is always executed after simple download with ST-LINK/V2 and STLINK-V3. |
| 64155 | Impossible to erase sector 128 and upper on STM32L476RG with STM32CubeProgrammer. |

3.24.3 Known problems and limitations

- The use of the UART bootloader prevents from increasing the RDP level and programming the second bank of Option Bytes, or from enabling the two user secure areas simultaneously on STM32H7 microcontrollers.
- Programming over USB bootloader is not reliable with USB2.0 for some devices.
- External memory programming is only available with ST-LINK.
- Installing multiple instances of the same version of the tool in the same directory under Windows® leads to issues when uninstalling.
- UART flash programming of devices in the STM32MP1 series may fail with big partitions.
- Programming issues are observed with the STM32H7 series when the STLINK-V3 I²C interface is used.
- GUI issues are sometimes observed with the OTA programming of devices in the STM32WB Series.
- Linux® 32 bits is not supported.
- STM32 Trusted Package Creator: only the CLI version is supported on macOS®.
- Mass Erase is not working with the SPI bootloader interface on the STM32WB series.
- Mass Erase is not working with the I²C bootloader interface on the STM32G4 series; errors can occur when writing via the SPI interface.
- Shared mode: board detection failure is observed after multiple refresh operations.
- Writing 8-bit data in RAM on STM32L496G devices is not possible.

3.25 STM32CubeProgrammer v2.0.0 release information

3.25.1 New features

- Added support of STM32MP1 series
- Added support of STM32WB series
- Added support of *Automatic Mode* for programming devices in a loop
- Added support of OTA programming for the STM32WB series

3.25.2 Fixed issues

Table 21. Main issues fixed in STM32CubeProgrammer v2.0.0

| ID | Summary |
|-------|---|
| 58879 | Internal flash programming issue with STM32L433RC (SMPS version) and STM32L433RB. |

| ID | Summary |
|-------|---|
| 60257 | <i>stlinkv3.rules</i> is missing in the <i>drivers\rules\</i> folder. |

3.25.3 Known problems and limitations

- The use of the UART bootloader prevents from increasing the RDP level and programming the second bank of Option Bytes, or from enabling the two user secure areas simultaneously on STM32H7 microcontrollers.
- Programming over USB bootloader is not reliable with USB2.0 for some devices.
- External memory programming is only available with ST-LINK.
- Installing multiple instances of the same version of the tool in the same directory under Windows® leads to issues when uninstalling.
- UART flash programming of devices in the STM32MP1 series may fail with big partitions.
- Programming issues are observed with the STM32H7 series when the STLINK-V3 I²C interface is used.
- GUI issues are sometimes observed with STM32WB series OTA programming.
- Linux® 32 bits is not covered.
- STM32 Trusted Package Creator: only the CLI version is supported on macOS®.

3.26 STM32CubeProgrammer v1.4.0 release information

3.26.1 New features

- Added STM32CubeProgrammer C++ API
- Added support of secure firmware install on [STM32L462CEU6F](#)

3.26.2 Fixed issues

Table 22. Main issues fixed in STM32CubeProgrammer v1.4.0

| ID | Summary |
|-------|--|
| 55454 | Programming a STM32F765NIH6 via USART1 can be done. |
| 56817 | Cannot program internal flash memory of STM32F722ZE and STM32F730R8 via USB. |

3.26.3 Known problems and limitations

- Read/write operations fail with the CAN interface.
- The use of the UART bootloader prevents from increasing the RDP level and programming the second bank of Option Bytes, or from enabling the two user secure areas simultaneously on STM32H7 microcontrollers.
- Programming over USB bootloader is not reliable with USB2.0 for some devices.
- External memory programming is only available with ST-LINK.
- Installing multiple instances of the same version of the tool in the same directory under Windows® leads to issues when uninstalling.
- The STLinkV3-I2C bridge may not work correctly with STM32F4 series, STM32F7 series, and STM32H7 series.

3.27 STM32CubeProgrammer v1.3.0 release information

3.27.1 New features

- Added support of STM32G07x and STM32G08x microcontrollers
- Added support of STM32L010 microcontrollers
- Flash memory size displayed with debug interface
- Extended ST-LINK server interface support to Linux® and macOS®
- User interface enhancements
- Added contextual menu in main panel for programming, verification, and saving
- Added support of *.binary* files
- Added support of Unicode® file path
- New panel for external loaders; possibility to search and filter with the loader or board name

3.27.2 Fixed issues

Table 23. Main issues fixed in STM32CubeProgrammer v1.3.0

| ID | Summary |
|-------|---|
| 54212 | STM32CubeProg could not display complete MCU list. |
| 54700 | Issue with file path including Chinese characters (double-byte characters). |
| 55156 | Error with hex file programming with option "run after programming". |

3.27.3 Known problems and limitations

- Read/write operations fail with the CAN interface.
- The use of the UART bootloader prevents from increasing the RDP level and programming the second bank of Option Bytes, or from enabling the two user secure areas simultaneously on STM32H7 microcontrollers.
- Programming over USB bootloader is not reliable with USB2.0 for some devices.
- External memory programming is only available with ST-LINK.
- Installing multiple instances of the same version of the tool in the same directory under Windows® leads to issues when uninstalling.
- The STLinkV3-I2C bridge may not work correctly with STM32F4 series, STM32F7 series, and STM32H7 series.

3.28 STM32CubeProgrammer v1.2.1 release information

3.28.1 New features

- Full-chip erase enabled for STM32L0 series and STM32L1 series
- Enhanced connection to STM32L0 series and STM32L1 with STLINK-V3
- Added support of Quad-SPI flash loaders:
 - N25Q128A_STM32F7508-DISCO
 - MX25L512G_STM32F7308-DISCO
 - MT25TL01G_STM32H743I-EVAL
 - MT25TL01G_STM32H747-EVAL

3.28.2 Fixed issues

Table 24. Main issues fixed in STM32CubeProgrammer v1.2.1

| ID | Summary |
|-------|---|
| 53000 | [GUI-memory edition] UART memory editions do not work |
| 53496 | [Launcher-java10] The tool is not launched in Windows10 64 bits with Java10 |
| 54292 | [USB] Connection time increases after every disconnect/connect with DFU interface |

3.28.3 Known problems and limitations

- Read/write operations fail with the CAN interface.
- The use of the UART bootloader prevents from increasing the RDP level and from programming the second bank of Option Bytes, or from enabling the two user secure areas simultaneously on STM32H7 microcontrollers.
- Programming over USB bootloader is not reliable with USB2.0 for some devices.
- External memory programming is only available with ST-LINK.
- Installing multiple instances of the same version of the tool in the same directory under Windows® leads to issues when uninstalling.
- STLinkV3-I2C bridge may not work correctly with STM32F4 series, STM32F7 series, and STM32H7 series.

3.29 STM32CubeProgrammer v1.2.0 release information

3.29.1 New features

- Add support of STLINK-V3
- Add support of STM32L41x microcontrollers
- Listing of the connected ST-LINK probes using the `--list` command
- Digitally signed USB DFU driver for STM32 bootloader
- Add support of ST-LINK server interface

3.29.2 Known problems and limitations

- Read/write operations fail with the CAN interface.
- The use of the UART bootloader prevents from increasing the RDP level and from programming the second bank of Option Bytes, or from enabling the two user secure areas simultaneously on STM32H7 microcontrollers.
- Programming over USB bootloader is not reliable with USB2.0 for some devices.
- External memory programming is only available with ST-LINK.
- Installing multiple instances of the same version of the tool in the same directory under Windows® leads to issues when uninstalling.
- STLinkV3-I2C bridge may not work correctly with STM32F4 series, STM32F7 series, and STM32H7 series.

3.30 STM32CubeProgrammer v1.1.0 release information

3.30.1 New features

- Add support of STM32F7x0 Value line and STM32H750 Value line
- Add support of M29W128GL external flash memory programming on STM32H743I-EVAL
- Dump device memory into an hex/srec/bin file
- Add Core debug commands in command-line interface
- Add support of data EEPROM programming on STM32L0 series and STM32L1 series

3.30.2 Known problems and limitations

- The use of the UART bootloader prevents from increasing the RDP level and from programming the second bank of option bytes, or from enabling the two user secure areas simultaneously on STM32H7 microcontrollers.
- Programming over USB bootloader is not reliable with USB2.0 for some devices.
- External memory programming is only available with ST-LINK.
- Installing multiple instances of the same version of the tool in the same directory under Windows® leads to issues when uninstalling.

3.31 STM32CubeProgrammer v1.0.0 release information

3.31.1 New features

- STM32 flash programming and erasing over ST-LINK debug probe (JTAG/SWD) and over bootloader interfaces UART and USB DFU
- STM32 option bytes detailed display with description of each bit field
- Option bytes programming over ST-LINK debug probe (JTAG/SWD) and over bootloader interfaces UART and USB DFU
- External memories programming over ST-LINK debug probe (JTAG/SWD) for STM32 microcontroller evaluation and discovery boards
- Read, display and programming of binary files, ELF files, Intel hex files and Motorola Srecord files
- Read and display of STM32 microcontroller memory content
- Command line and graphical user interface
- Generation of secure firmware using the STM32 Trusted Package Creator tool

3.31.2 Known problems and limitations

- The use of the UART bootloader prevents from increasing the RDP level and from programming the second bank of option bytes, or from enabling the two user secure areas simultaneously on STM32H7 microcontrollers.
- Programming over USB bootloader is not reliable with USB2.0 for some devices.
- The erase command is not supported with data EEPROM on STM32L0 and STM32L1.
- External memory programming is only available with ST-LINK.
- Installing multiple instances of the same version of the tool in the same directory under Windows® leads to issues when uninstalling.

Revision history

Table 25. Document revision history

| Date | Revision | Changes |
|-------------|----------|--|
| 24-Nov-2017 | 1 | Initial release. |
| 12-Apr-2018 | 2 | Part number changed to STM32CubeProg. |
| 19-Jul-2018 | 3 | Added information related to STM32CubeProg 1.1.0. |
| 07-Sep-2018 | 4 | Added information related to STM32CubeProg 1.2.0. |
| 15-Oct-2018 | 5 | Added information related to STM32CubeProg 1.2.1. |
| 15-Nov-2018 | 6 | Added information related to STM32CubeProg 1.3.0. |
| 20-Dec-2018 | 7 | Added information related to STM32CubeProg 1.4.0. |
| 25-Feb-2019 | 8 | Added information related to STM32CubeProg 2.0.0. |
| 23-Apr-2019 | 9 | Added information related to STM32CubeProg 2.1.0. |
| 11-Oct-2019 | 10 | Added information related to STM32CubeProg 2.2.0. |
| 08-Nov-2019 | 11 | Added information related to STM32CubeProg 2.2.1. |
| 20-Dec-2019 | 12 | Added information related to STM32CubeProg 2.3.0. |
| 24-Feb-2020 | 13 | Added information related to STM32CubeProg 2.4.0. |
| 24-Jul-2020 | 14 | Added information related to STM32CubeProg 2.5.0. |
| 18-Nov-2020 | 15 | Added information related to STM32CubeProg 2.6.0. Updated <i>Software requirements</i> . |
| 12-Mar-2021 | 16 | Added information related to STM32CubeProg 2.7.0. |
| 22-Jul-2021 | 17 | Added information related to STM32CubeProg 2.8.0. |
| 29-Nov-2021 | 18 | Added information related to STM32CubeProg 2.9.0. |
| 04-Mar-2022 | 19 | Added information related to STM32CubeProg 2.10.0. |
| 28-Jun-2022 | 20 | Added information related to STM32CubeProg 2.11.0. |
| 25-Nov-2022 | 21 | Added information related to STM32CubeProg 2.12.0. |
| 27-Feb-2023 | 22 | Added information related to STM32CubeProg 2.13.0. Updated the bootloader interfaces in <i>Overview</i> . |
| 31-Mar-2023 | 23 | Added information related to STM32CubeProg 2.13.1. |
| 10-Jul-2023 | 24 | Added information related to STM32CubeProg 2.14.0. Updated <i>Supported operating systems and architectures</i> . |
| 12-Nov-2023 | 25 | Added information related to STM32CubeProg 2.15.0. Updated <i>Licensing</i> . |
| 10-Jan-2024 | 26 | Added enhanced security for software updates in the <i>New features</i> of STM32CubeProg 2.15.0. |
| 19-Mar-2024 | 27 | Added information related to STM32CubeProg 2.16.0. Updated <i>Supported operating systems and architectures</i> . |
| 25-Jun-2024 | 28 | Added information related to STM32CubeProg 2.17.0. |
| 22-Nov-2024 | 29 | Added information related to STM32CubeProg 2.18.0. Updated <i>Supported operating systems and architectures</i> . |
| 27-Feb-2025 | 30 | Added information related to STM32CubeProg 2.19.0. Updated <i>Supported operating systems and architectures</i> . Updated <i>Fixed issues</i> and <i>Known problems and limitations</i> with a reference to the corresponding wiki site. Added an entry for the updater upgrade issue in STM32CubeProg 2.18.0 <i>Known problems and limitations</i> . |
| 03-Jul-2025 | 31 | Added information related to STM32CubeProg 2.20.0. |

| Date | Revision | Changes |
|-------------|----------|--|
| 27-Oct-2025 | 32 | Added information related to STM32CubeProg 2.21.0. Updated <i>Supported operating systems and architectures</i> . |
| 23-Feb-2026 | 33 | Added information related to STM32CubeProg 2.22.0. Updated <i>Supported operating systems and architectures</i> . Removed external memory programming from the support for the STM32WL3Rxx microcontrollers in <i>STM32CubeProgrammer v2.21.0 release information</i> . |
| 25-Jun-2026 | 34 | Added information related to STM32CubeProg 2.23.0. Updated <i>Supported operating systems and architectures</i> . |

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