

ACCORDO5

Infotainment Processor for Full HD Display Audio



The most cost effective automotive solution for phone projection applications

ST's Accordo5 offers a cost-effective solution for display audio applications up to Full HD resolution, supporting all of the smartphone mirroring standards. Accordo5 processors feature single or dual ARM cores, Graphics accelerator and DSPs to process all multimedia management, connectivity and audio decoding functions for in-vehicle systems. Advanced security and encryption features are hardware accelerated and Isolated ARM cortex M3 manages CAN connectivity in real time.

KEY FEATURES & BENEFITS

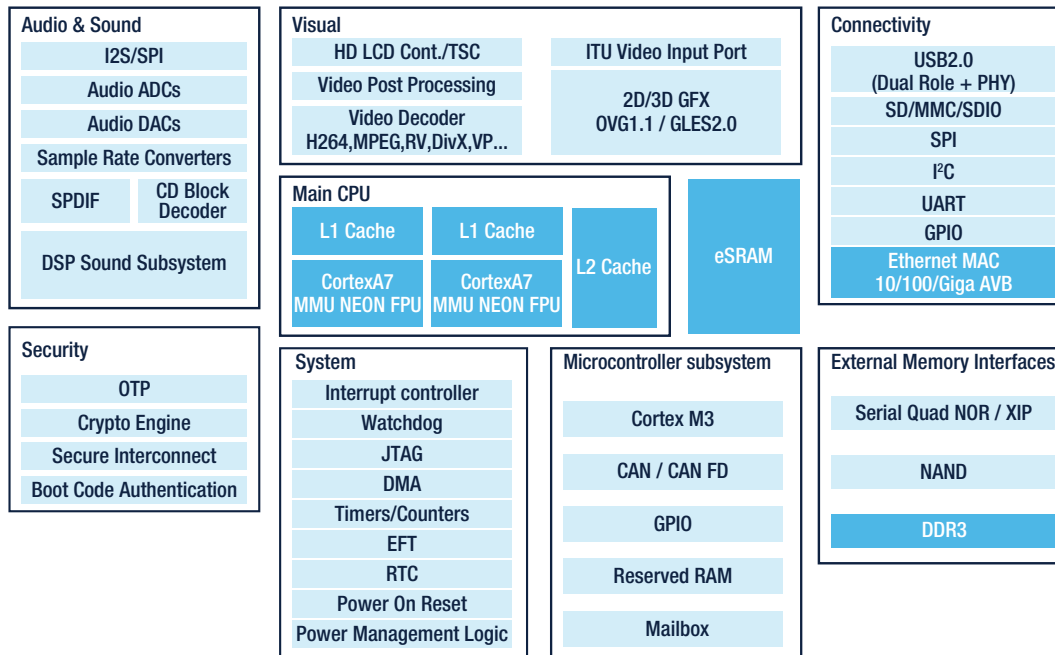
- Single or dual ARM Cortex A7 up to 650 MHz
- Full HD display with 4 layers overlay
- 3D GPU, OpenGL ES2.0, OpenVG1.1
- Multi-standard Video decoder (including H264)
- Flexible sound subsystem with integrated Audio DSP
- Isolated and secure CAN subsystem powered by ARM Cortex M3
- Video input Port for Rear Video Camera
- Integrated power management logic
- Secure boot and hardware accelerated crypto
- Gb ETH with AVB MAC

- USB 2.0, SD/SDIO, CAN/CAN-FD, SPI, I2C, UART
- Industry's most optimized system Bill-Of-Materials

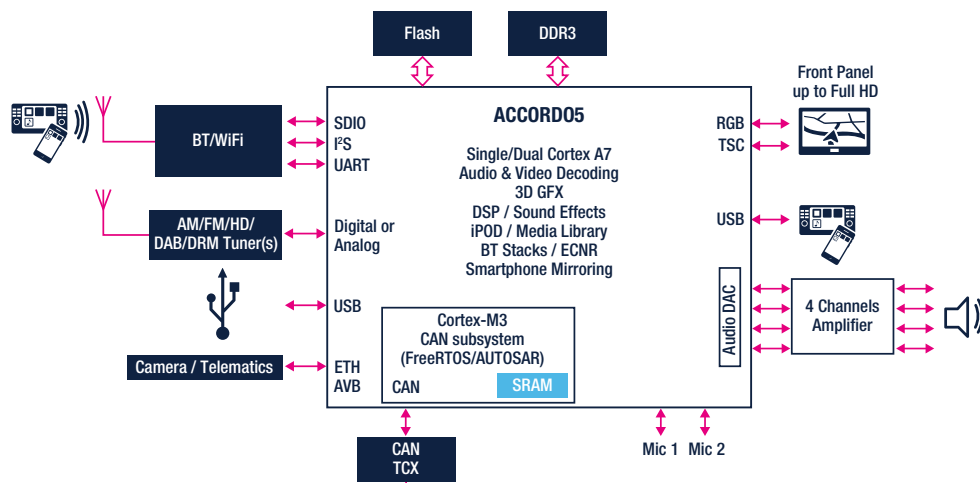
SOFTWARE OFFERING

- Linux OS kernel + BSP
- Standard Yocto tools
- FreeRTOS for Cortex M3
- Bootloader toolset
- MCAL for AUTOSAR support
- TouchGFX and 3rd party HMI design tools supported
- Pre-integrated open-source & 3rd party MW for easy implementation
- Complete infotainment turnkey solutions with large 3rd party SW ecosystem

Accordo5 block DIAGRAM



Example Solution



Operating conditions

- ARM_VDD: 1.20V $\pm 5\%$
- VDD_IO_3V3: 3.3V $\pm 10\%$
- VDD_IO_1V8: 1.8V $\pm 10\%$
- VDDQ (DDR3L): 1.35V $\pm 5\%$
- Operating Temperature Range: -40/+85 C
- Automotive Grade AEC-Q100 grade 3

Package information

- 529-ball LFBGA (19 x 19 x 1.7 mm) package with 0.8 mm pitch

Part number	A7 cores	Memory Width
STA1275	1	16-bit
STA1295	2	32-bit



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