

SPC570Sx Errata sheet

SPC570Sx devices errata JTAG ID = 0x110A 2041

Introduction

This errata sheet describes functional and electrical problems of the SPC570Sx devices.

The reference documentation is reported in *Chapter 1: Reference documents*.

Device identification for cut 2.1:

- JTAG ID = 0x110A 2041
- MIDR1 register:
 - MAJOR_MASK[3:0]: 4'b0001
 - MINOR_MASK[3:0]: 4'b0001

The list of errata is contained in a Microsoft® Excel workbook file attached to this document Locate the paper clip symbol on the left side of the PDF window, and click it. Double-click on the Excel file to open it.

Reference documents SPC570Sx

1 Reference documents

Table 1. Reference documents

Doc name	Title	Revision
RM0349	SPC570Sx 32-bit Power Architecture® microcontroller for automotive ASILD applications	6
DS9607	32-bit Power Architecture [®] microcontroller for automotive ASILD applications	7

SPC570Sx Revision history

Revision history

Table 2. Document revision history

Date	Revision	Changes
06-Aug-2014	1	Initial release
16-Dec-2014	2	Added following functional errata: - ERR008526 - ERR008561 - ERR008573 - ERR008602 - ERR008627 - ERR008731
30-Mar-2015	3	Added following functional errata: - PS1244 - PS1254 Removed the following functional errata: - ERR006850 - ERR007126 - ERR006904
29-May-2015	4	Added following functional errata: - PS1040 - PS1045 - PS1071 - PS1439 - PS1440 - PS1566 - PS1681 - ERR008915
24-Sep-2015	5	Removed the following functional errata: - PS1071 - PS1244 - PS1254
10-May-2017	6	Added errata: - DAN-0043391
10-May-2017	7	Typo error
11-Feb-2021	8	Added errata: - PS2859 - DAN-0044555 - DAN-0045775 - DAN-0054519 - DAN-0054520
12-Apr-2022	9	Added errata: - ERR009764

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