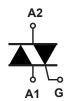


16 A Triac for LED light dimmer





TO-220AB Ins.



Product status link

T1605G-6I

Product summary			
Order code T1605G-6I			
Package TO-220AB Ins.			
V _{DRM} /V _{RRM}	600 V		
I _{GT}	5 mA		
I _H	5 mA		

Features

- Three quadrants with logic level gate
- Super low holding current I_H = 5 mA
- Optimized thermal performance with low power dissipation
- Optimized turn-off commutation for lighting loads
- Insulated package TO-220AB:
 - Insulated voltage: 2500 V_{RMS}
 - Complies with UL 1554 (file ref. 81734)

Application

- Lighting:
 - Universal light dimmers
 - LED light dimmers
- Heating
- Overvoltage crowbar protection

Description

The T1605G-6I Triac in TO-220AB insulated can be used for the on/off or phase angle control function in general purpose AC switching where high commutation capability is required.

Its super low holding current I_H enables deep dimming for LED light dimmers without flickering nor jittering.

Package environmentally friendly ECOPACK2, RoHS (2011/65/EU) and halogen free compliant.

TO-220AB insulated package is UL-94, V0 flammability resin compliance.

This component is recognized by UL. Representative samples of this component have been evaluated by UL and meet applicable UL requirements for UL 1557 standard (File Ref. 81734).



1 Characteristics

Table 1. Absolute maximum ratings (limiting values)

Symbol	Parameters		Value	Unit	
I _{T(RMS)}	RMS on-state current (full sine wave)	T _C = 85 °C	16	А	
I	Non repetitive surge peak on-state current,	t _p = 16.7 ms	T _j = 25 °C	140	_
I _{TSM}	(full cycle, T _j initial = 25 °C)	t _p = 20 ms	T _j = 25 °C	132	Α
l ² t	I ² t value for fusing	t _p = 10 ms	T _j = 25 °C	116	A ² s
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$, $t_r \le 100 \text{ ns}$ $f = 50 \text{ Hz}$		T _j = 25 °C	50	A/µs
V _{DRM} /V _{RRM}	Repetitive peak off-state voltage	T _j = 125 °C	600	V	
V _{DSM} /V _{RSM}	Non repetitive surge peak off-state voltage $t_p = 20 \text{ ms}$		T _j = 25 °C	700	V
I _{GM}	Peak gate current	Peak gate current $t_p = 20 \mu s$		4	Α
P _{G(AV)}	Average gate power dissipation		T _j = 125 °C	1	W
T _{stg}	Storage junction temperature range		-40 to +150	°C	
Tj	Operating junction temperature range		-40 to +125	°C	

Table 2. Electrical characteristics ($T_j = 25$ °C, unless otherwise specified)

Symbol	Parameters	Quadrant		Value	Unit
l			Min.	0.25	mA
l _{GT}	V_D = 12 V, R_L = 33 Ω	1 - 11 - 111	Max.	5	- IIIA
V _{GT}				1.3	V
V_{GD}	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega, T_j = 125 \text{ °C}$	1 - 11 - 111	Min.	0.2	V
I _H ⁽¹⁾	I _T = 500 mA, gate open			5	mA
I.	I _G = 1.2 I _{GT}	1 - 111	May	10	A
ΙL	IG - 1.2 IGT	II	Max.	15	mA
dV/dt ⁽¹⁾	V _D = 67% V _{DRM} , gate open	T _j = 125 °C	Min.	10	V/µs
(dl/dt)c ⁽¹⁾	(dV/dt)c = 0.1 V/µs	T _j = 125 °C	Min.	2.5	A/ms

^{1.} For both polarities of A2 referenced to A1

Table 3. Static electrical characteristics

Symbol	Test conditions	Tj		Value	Unit
V _{TM} ⁽¹⁾	I _{TM} = 22.5 A, t _p = 380 μs	25 °C	Max.	1.55	V
V _{TO} ⁽¹⁾	threshold on-state voltage	125 °C	Max.	0.83	V
R _D ⁽¹⁾	Dynamic resistance	125 °C	Max.	28	mΩ
I _{DRM} /I _{RRM}	V _{DRM} = V _{RRM} = 600 V	25 °C	Max.	5	μA
		125 °C	IVIAX.	1	mA

^{1.} For both polarities of A2 referenced to A1

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Table 4. Thermal resistance

Symbol	Parameters		Value	Unit
R _{th(j-c)}	Max. junction to case (AC)	Max.	2.1	°C/W
R _{th(j-a)}	Junction to ambient	Тур.	60	C/VV

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1.1 Characteristics (curves)

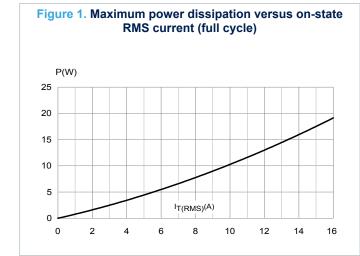


Figure 2. RMS on-state current versus case temperature (full cycle) $I_{T(RMS)}(A)$ 18 16 14 12 10 8 6 4 2 T_c(°C) 0 0 25 50 75 100 125

Figure 3. Relative variation of thermal impedance versus pulse duration

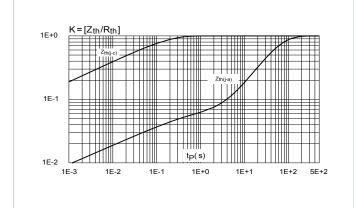


Figure 4. On-state characteristics (maximum values)

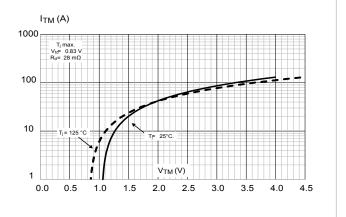


Figure 5. Surge peak on-state current versus number of cycles

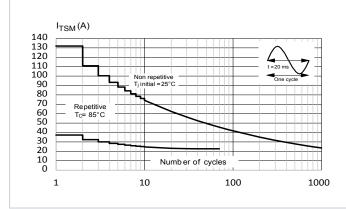
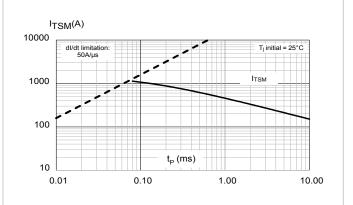


Figure 6. Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp < 10 ms



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Figure 7. Relative variation of critical rate of decrease of main current versus junction temperature

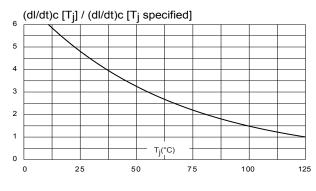


Figure 8. Relative variation of gate trigger current and latching current versus junction temperature (typical values)

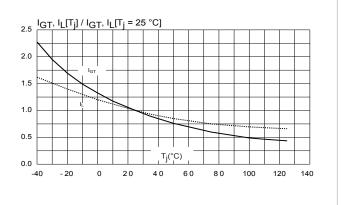
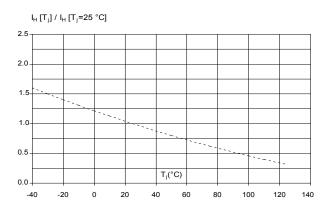


Figure 9. Relative variation of holding current versus junction temperature (typical values)



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Package information

To meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions, and product status are available at: www.st.com. ECOPACK is an ST trademark.

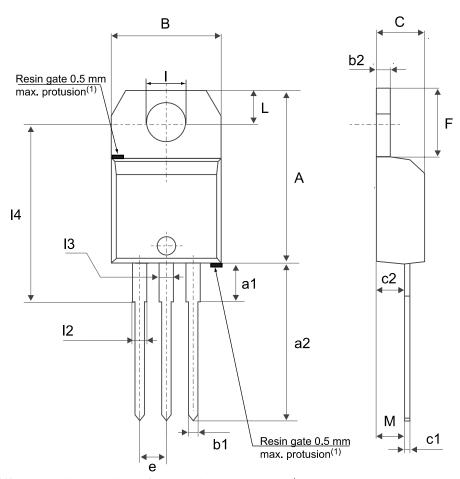
2.1 TO-220AB insulated package information

Epoxy meets UL 94,V0

Cooling method: by conduction (C)
Recommended torque value: 0.55 N·m

Maximum torque value: 0.70 N·m

Figure 10. TO-220AB insulated package outline



(1)Resin gate position accepted in one of the two positions or in the symmetrical opposites.

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Table 5. TO-220AB insulated package mechanical data

Dimensions							
Ref.		Millimeters		Inches ⁽¹⁾			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
А	15.20		15.90	0.5984		0.6260	
a1		3.75			0.1476		
a2	13.00		14.00	0.5118		0.5512	
В	10.00		10.40	0.3937		0.4094	
b1	0.61		0.88	0.0240		0.0346	
b2	1.23		1.32	0.0484		0.0520	
С	4.40		4.60	0.1732		0.1811	
c1	0.49		0.70	0.0193		0.0276	
c2	2.40		2.72	0.0945		0.1071	
е	2.40		2.70	0.0945		0.1063	
F	6.20		6.60	0.2441		0.2598	
I	3.73		3.88	0.1469		0.1528	
L	2.65		2.95	0.1043		0.1161	
12	1.14		1.70	0.0449		0.0669	
13	1.14		1.70	0.0449		0.0669	
14	15.80	16.40	16.80	0.6220	0.6457	0.6614	
М		2.6			0.1024		

^{1.} Inch dimensions are for reference only.

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3 Ordering information

Figure 11. Ordering information scheme

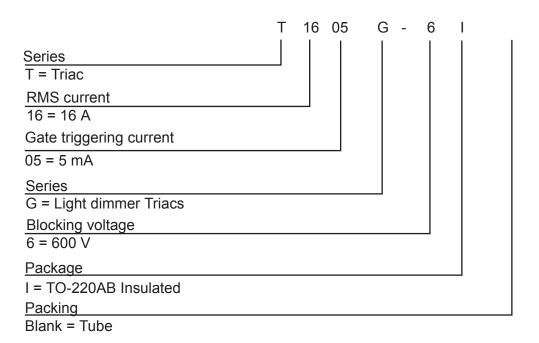


Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode
T1605G-6I	T1605G-6I	TO-220AB-Ins.	2.3 g	50	Tube

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Revision history

Table 7. Document revision history

Date	Revision	Changes
07-Oct-2020	1	Initial release.
28-Oct-2020	2	Updated Table 6.
27-Sep-2021	3	Updated cover image pin name, Figure 7 and Figure 9.
10-Jul-2025	4	Updated Section Features.

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