

## 250 W, 50 V HF/VHF DMOS transistor

Datasheet - target specification

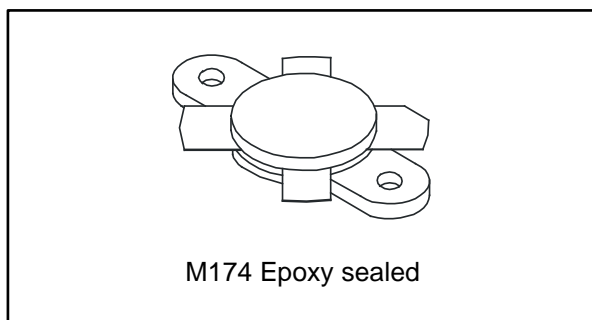
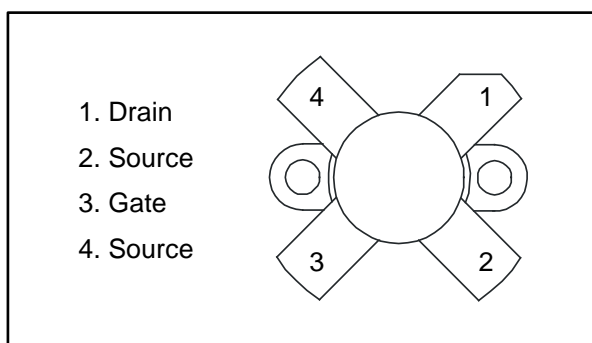


Figure 1: Pin connection



### Description

The SD2951-10 is an N-channel MOS field-effect RF power transistor, intended for use in 50 V dc large signal applications up to 230 MHz. It offers 25% lower  $R_{DS(ON)}$  than the industry standard, with 40% higher  $P_{SAT}$  than ST's SD2931-10 device. The SD2951-10 is housed in the low thermal non-pedestal package, offering 25% lower thermal resistance than the industry standard, thus rendering it the "best-in-class" transistor for ISM applications, where reliability and ruggedness are critical factors.

Table 1: Device summary

Order code	Marking	Package	Packing <sup>(1)</sup>
SD2951-10	SD2951-10	M174	Plastic tray

#### Notes:

<sup>(1)</sup>For additional details, please refer to Marking, packing and shipping specifications.

### Features

- Excellent thermal stability
- Common source configuration
- $P_{OUT} = 250$  W with 14 dB gain @ 175 MHz
- Low  $R_{DS(on)}$
- Load mismatch 65:1 all phases @ 250 W - 50 V - 175 MHz / 1 msec - 10%

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# 1 Electrical data

## 1.1 Maximum rating

( $T_{CASE} = 25\text{ °C}$ )

Table 2: Absolute maximum rating

Symbol	Parameter	Value	Unit
$V_{(BR)DSS}$	Drain source voltage	145	V
$V_{DGR}$	Drain-gate voltage ( $R_{GS} = 1\text{ M}\Omega$ )	145	V
$V_{GS}$	Gate-source voltage	$\pm 20$	V
$T_J$	Max. operating junction temperature	200	°C
$T_{STG}$	Storage temperature	-65 to +150	°C

## 2 Electrical characteristics

(T<sub>CASE</sub> = 25 °C)

Table 3: Static

Symbol	Test conditions		Min.	Typ.	Max.	Unit
V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V	I <sub>DS</sub> = 100 mA	145	155		V
I <sub>DSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 50 V			50	μA
I <sub>GSS</sub>	V <sub>GS</sub> = 20 V	V <sub>DS</sub> = 0 V			250	nA
V <sub>GS(Q)</sub> <sup>(1)</sup>	V <sub>DS</sub> = 10 V	I <sub>D</sub> = 250 mA	2.0		4.0	V
V <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 10 A			3.0	V
G <sub>FS</sub> <sup>(1)</sup>	V <sub>DS</sub> = 8 V	I <sub>D</sub> = 5 A	7			mho
C <sub>ISS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 50 V    f = 1 MHz		645		pF
C <sub>OSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 50 V    f = 1 MHz		194		pF
C <sub>RSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 50 V    f = 1 MHz		5		pF

**Notes:**

<sup>(1)</sup>V<sub>GS(Q)</sub> and G<sub>FS</sub> sorted with alpha/numeric code marked on unit.

Table 4: Dynamic

Symbol	Test conditions	Min.	Typ.	Max.	Unit
P <sub>OUT</sub>	V <sub>DD</sub> = 50 V; I <sub>DQ</sub> = 250 mA; f = 175 MHz	200	250	-	W
G <sub>PS</sub>	V <sub>DD</sub> = 50 V; I <sub>DQ</sub> = 250 mA; P <sub>OUT</sub> = 200 W; f = 175 MHz	14	15.8		dB
h <sub>D</sub>	V <sub>DD</sub> = 50 V; I <sub>DQ</sub> = 250 mA; P <sub>OUT</sub> = 200 W; f = 175 MHz	55	65		%

Table 5: V<sub>GS</sub> and G<sub>FS</sub> sorts

V <sub>GS</sub> code	Value	G <sub>FS</sub> code	Value
1	1.5 - 2.0	A	7 - 8
2	2.0 - 2.5	B	8 - 9
3	2.5 - 3.0	C	9 - 10
4	3.0 - 3.5	D	10 - 11
5	3.5 - 4.0		

### 3 Impedance

Figure 2: Impedance data schematic

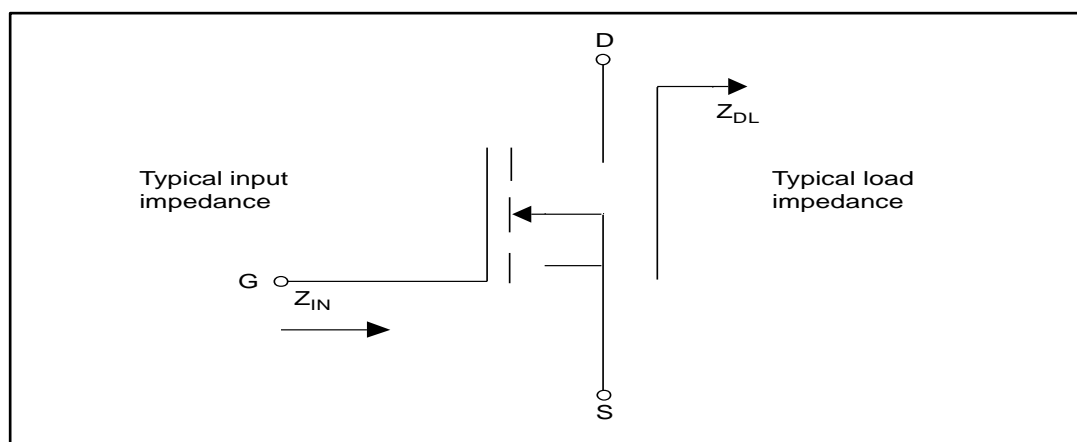


Table 6: Impedance data

f	$Z_{IN} (\Omega)$	$Z_{DL} (\Omega)$
30 MHz	TBD	TBD
175 MHz	TBD	TBD

## 4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com). ECOPACK® is an ST trademark.

### 4.1 M174 package information

Figure 3: M174 package dimensions

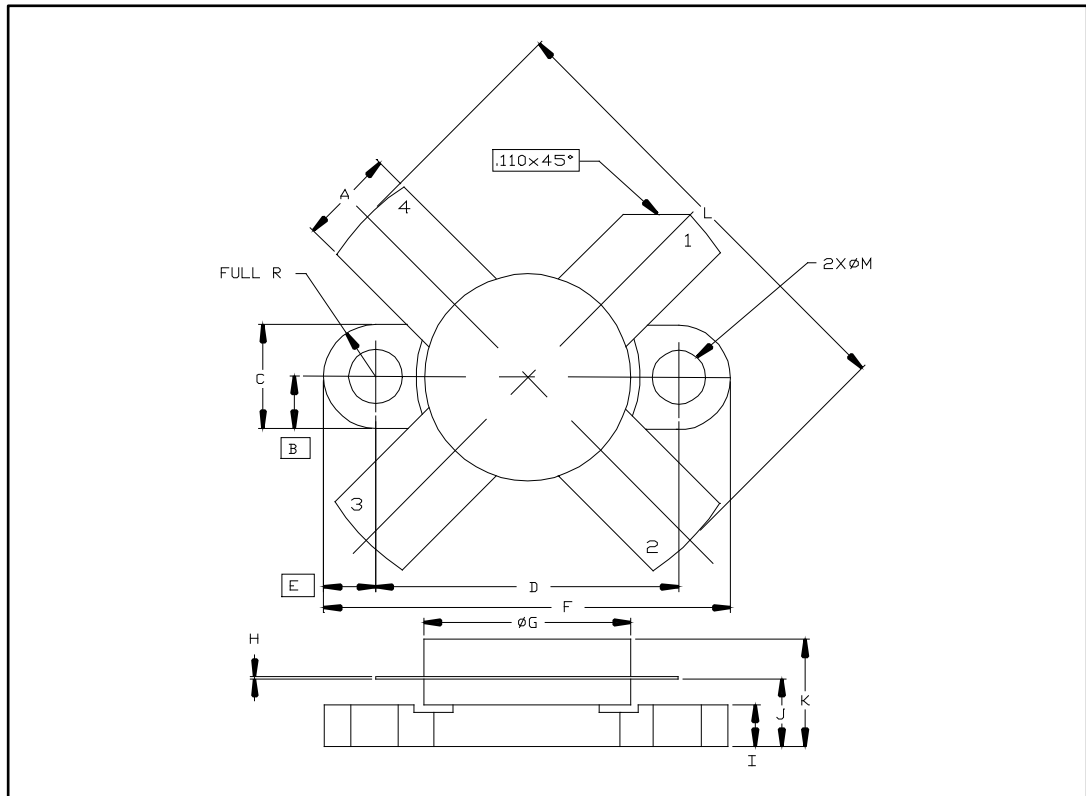


Table 7: M174 (.500 DIA 4/L N/HERM W/FLG)

Dim.	mm.			inch		
	Min.	Typ.	Max	Min.	Typ.	Max
A	5.56		5.584	0.219		0.230
B		3.18			0.125	
C	6.22		6.48	0.245		0.255
D	18.28		18.54	0.720		0.730
E		3.18			0.125	
F	24.64		24.89	0.970		0.980
G	12.57		12.83	0.495		0.505
H	0.08		0.18	0.003		0.007
I	2.11		3.00	0.083		0.118
J	3.81		4.45	0.150		0.175
K			7.11			0.280
L	25.53		26.67	1.005		1.050
M	3.05		3.30	0.120		0.130

## 5 Marking, packing and shipping specifications

Table 8: Packing and shipping specifications

Order code	Packaging	Pcs per tray	Dry pack humidity	V <sub>GS</sub> and G <sub>FS</sub> code	Lot code
SD2951-10	Plastic tray	25	< 10%	Not mixed	Not mixed

Figure 4: Marking layout

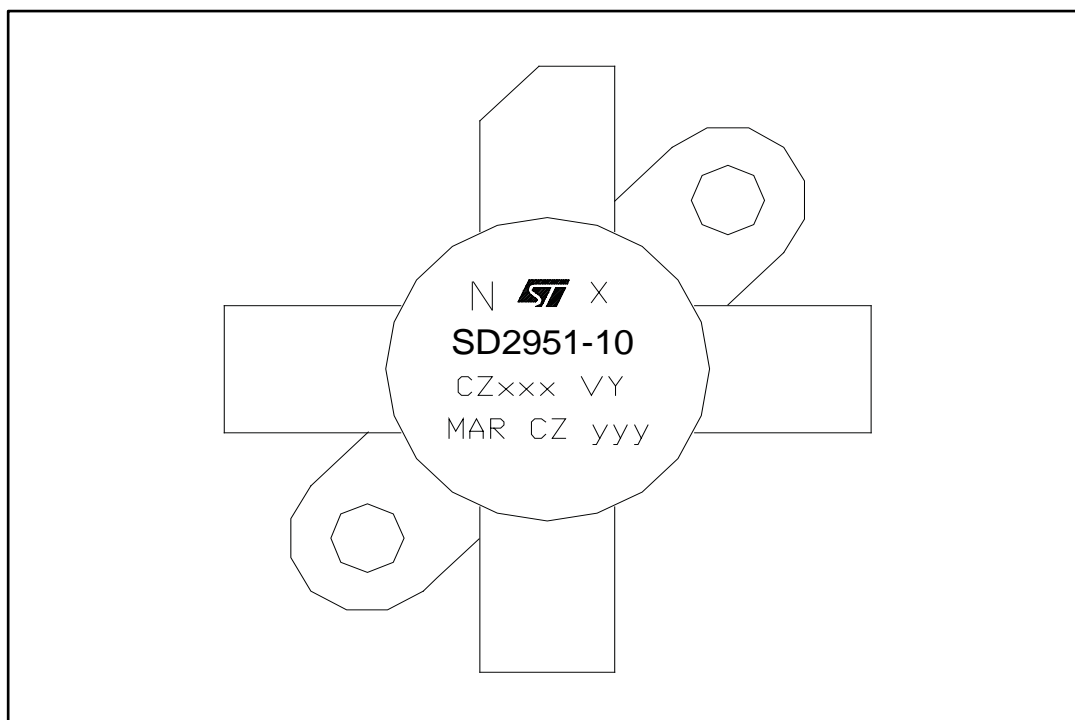


Table 9: Marking specifications

Symbol	Description
N	V <sub>GS</sub> sort
X	G <sub>FS</sub> sort
CZ	Assembly plant
xxx	Last 3 digits of diffusion lot
VY	Diffusion plant
MAR	Country of origin
CZ	Test and finishing plant
y	Assembly year
yy	Assembly week



## 6 Revision history

Table 10: Document revision history

Date	Revision	Changes
27-Aug-2013	1	Initial release.

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