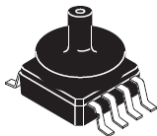


High temperature accuracy integrated silicon pressure sensor for measuring vacuum/gauge pressure, on-chip signal conditioned, temperature compensated and calibrated

SMALL OUTLINE PACKAGE

MPXV5050VC6T1
CASE 482A-01

Features

- 2.5% Maximum Error over 0° to 85°C
- Ideally suited for Microprocessor or Microcontroller-Based Systems
- Temperature Compensated from Over -40° to +125°C
- Patented Silicon Shear Stress Strain Gauge
- Durable Thermoplastic (PPS) Surface Mount Package
- Easy-to-Use Chip Carrier Option
- Ideal for Automotive and Non-Automotive Applications

Application

- Vacuum Pump Monitoring

Description

The MPXV5050V series sensor integrates on-chip, bipolar op amp circuitry and thin film resistor networks to provide a high output signal and temperature compensation. The small form factor and high reliability of on-chip integration make the ST pressure sensor a logical and economical choice for the system designer.

The MPXV5050V series piezoresistive transducer is a state-of-the-art, monolithic, signal conditioned, silicon pressure sensor. This sensor combines advanced micromachining techniques, thin film metallization, and bipolar semiconductor processing to provide an accurate, high level analog output signal that is proportional to applied pressure.

1 Ordering information

Table 1. Ordering information

Device Name	Case No.	# of Ports			Pressure Type			Device Marking
		None	Single	Dual	Gauge	Differential	Absolute	
Small Outline Package								
MPXV5050VC6T1	482A		•		Vacuum/Gauge			MPXV5050V

2 Operating characteristics

Table 2. Operating Characteristics ($V_S = 5.0$ Vdc, $T_A = 25^\circ\text{C}$ unless otherwise noted, $P1 > P2$)

Characteristic		Symbol	Min	Typ	Max	Unit
Pressure Range		P_{OP}	-50	—	0	kPa
Supply Voltage ⁽¹⁾		V_S	4.75	5.0	5.25	Vdc
Supply Current		I_o	—	7.0	10	mAdc
Full Scale Output ⁽²⁾ @ $V_S = 5.0$ Volts	(0 to 85°C)	V_{FSO}	4.488	4.6	4.713	Vdc
Full Scale Span ⁽³⁾ @ $V_S = 5.0$ Volts	(0 to 85°C)	V_{FSS}	—	4.5	—	Vdc
Accuracy ⁽⁴⁾	(0 to 85°C)	—	—	—	±2.5	% V_{FSS}
Sensitivity		V/P	—	90	—	mV/kPa
Response Time ⁽⁵⁾		t_R	—	1.0	—	ms
Warm-Up Time ⁽⁶⁾		—	—	20	—	ms
Offset Stability ⁽⁷⁾		—	—	±0.5	—	% V_{FSS}
Pressure Offset ⁽⁸⁾	(0 to 85°C)	V_{off}	0	0.100	0.213	Vdc

1. Device is ratiometric within this specified excitation range.
2. Full Scale Output (V_{FSO}) is defined as the output voltage at the maximum or full rated pressure.
3. Full Scale Span (V_{FSS}) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure.
4. Accuracy is the deviation in actual output from nominal output over the entire pressure range and temperature range as a percent of span at 25°C due to all sources of error including the following:
 Linearity: Output deviation from a straight line relationship with pressure over the specified pressure range.
 Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to and from the minimum or maximum operating temperature points, with zero differential pressure applied.
 Pressure Hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from minimum or maximum rated pressure at 25°C.
 TcSpan: Output deviation over the temperature range of 0° to 85°C, relative to 25°C.
 TcOffset: Output deviation with minimum pressure applied, over the temperature range of 0° to 85°C, relative to 25°C.
5. Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.
6. Warm-up Time is defined as the time required for the product to meet the specified output voltage after the pressure has been stabilized.
7. Offset Stability is the product's output deviation when subjected to 1000 cycles of Pulsed Pressure, Temperature Cycling with Bias Test.
8. Offset (V_{off}) is defined as the output voltage at the minimum rated pressure.

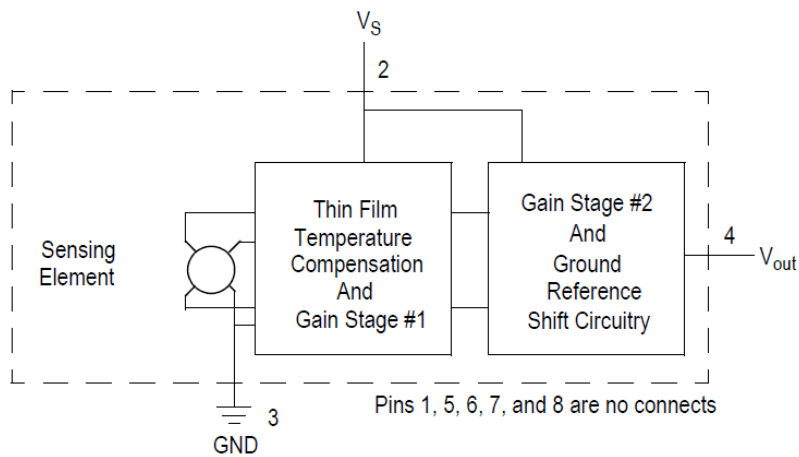
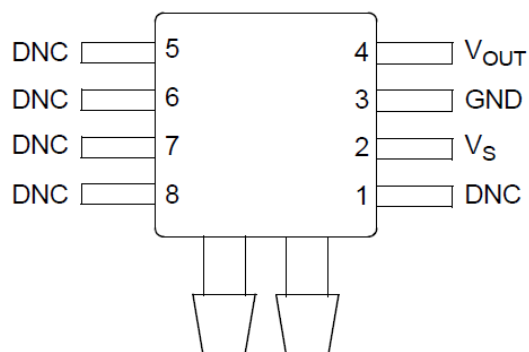
3 Maximum ratings

3.1 Maximum ratings

Table 3. Maximum Ratings⁽¹⁾

Rating	Symbol	Value	Units
Maximum Pressure (P1 > P2)	P_{max}	200	kPa
Storage Temperature	T_{stg}	-40 to +125	°C
Operating Temperature	T_A	-40 to +125	°C

1. Exposure beyond the specified limits may cause permanent damage or degradation to the device.
 Figure 1 shows a block diagram of the internal circuitry integrated on a pressure sensor chip.

Figure 1. Fully integrated pressure sensor schematic

Figure 2. Full device pinout (top view)


3.2 Pin functions

Table 4. Pin functions

Pin	Name	Function
1	DNC	Do not connect to external circuitry or ground.
2	V _S	Voltage supply
3	GND	Ground
4	V _{OUT}	Output voltage
5	DNC	Do not connect to external circuitry or ground.
6	DNC	Do not connect to external circuitry or ground.
7	DNC	Do not connect to external circuitry or ground.
8	DNC	Do not connect to external circuitry or ground.

4 On-chip temperature compensation and calibration

Figure 3 illustrates the gauge sensing chip in the basic Super Small Outline chip carrier (Case 482A).

Figure 4 shows a typical application circuit (output source current operation).

Figure 5 shows the sensor output signal relative to pressure input. Typical minimum and maximum output curves are shown for operation over 0 °C to 85 °C temperature range. The output will saturate outside of the rated pressure range.

A fluorosilicone gel isolates the die surface and wire bonds from the environment, while allowing the pressure signal to be transmitted to the silicon diaphragm. The MPXV5050 series pressure sensor operating characteristics, internal reliability and qualification tests are based on use of dry air as the pressure media. Media other than dry air may have adverse effects on sensor performance and long-term reliability. Contact the factory for information regarding media compatibility in your application.

Figure 3. Cross Sectional Diagram SSOP (not to scale)

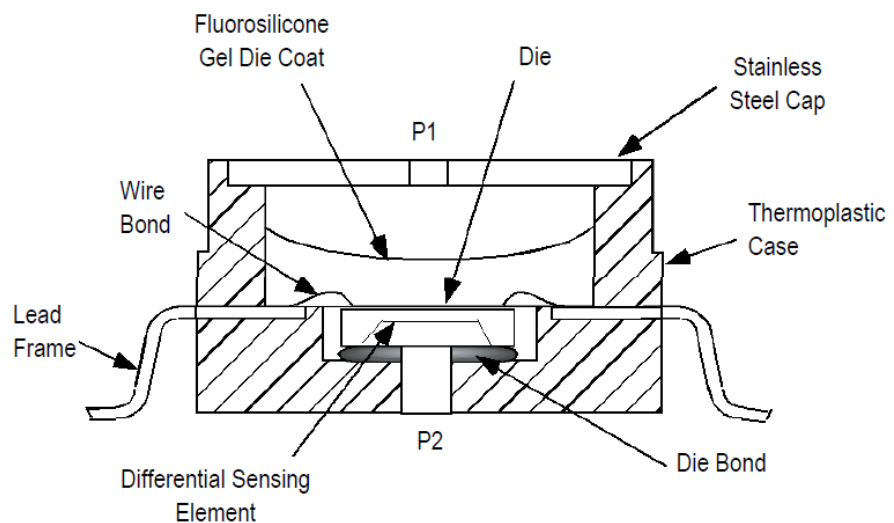


Figure 4. Typical Application Circuit (Output Source Current Operation)

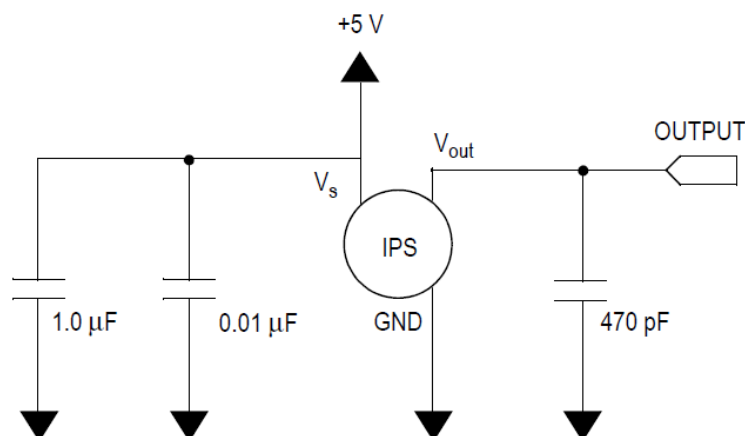
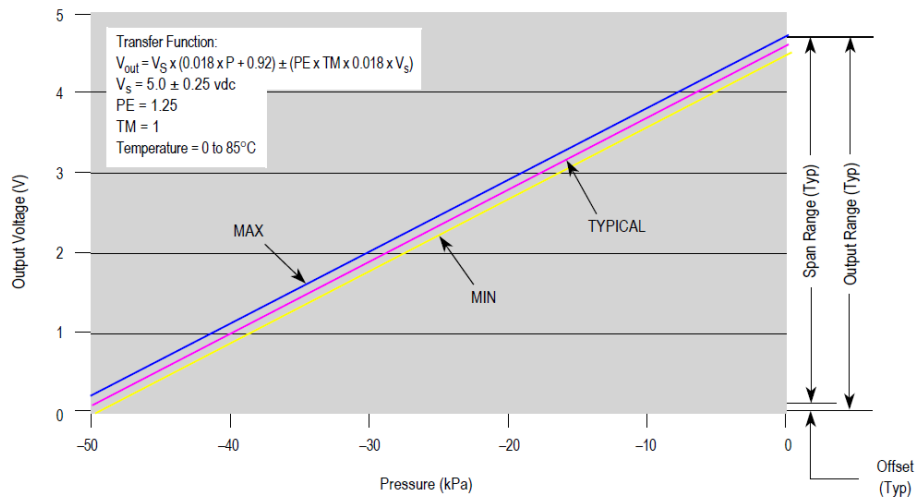
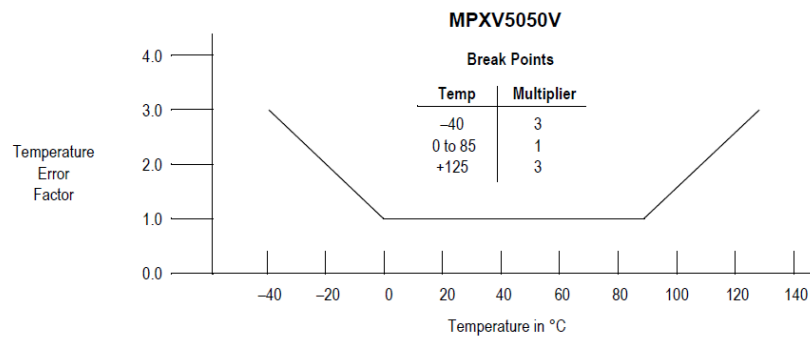
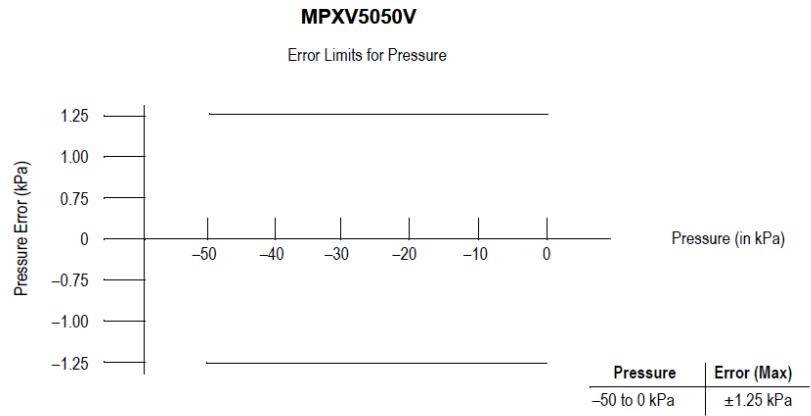


Figure 5. Output vs. Gauge Pressure

Figure 6. Transfer Function (MPXV5050V)

Nominal Transfer Value: $V_{OUT} = V_S \times (0.018 \times P + 0.92)$
 $\pm (\text{Pressure Error} \times \text{Temp Multi} \times 0.018 \times V_S)$
 $V_S = 5.0 \pm 0.25 \text{ V}$

Figure 7. Temperature Error Band


NOTE: The Temperature Multiplier is a linear response from 0°C to -40°C and from 85°C to 125°C.

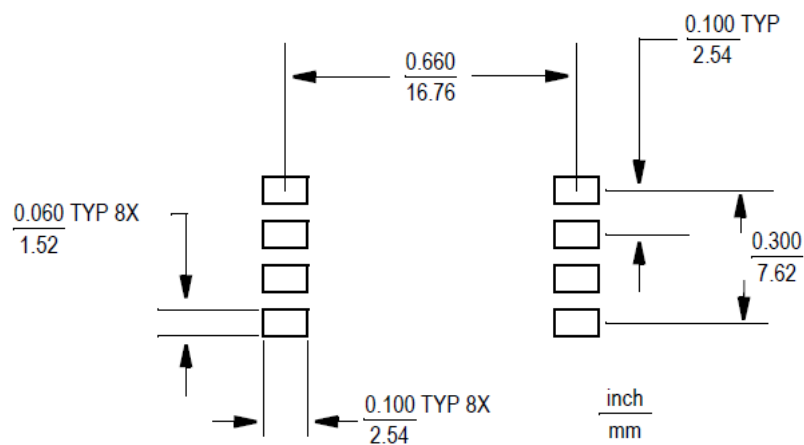
Figure 8. Pressure Error Band


5 Surface mounting information

5.1 Minimum recommended footprint for small outline package

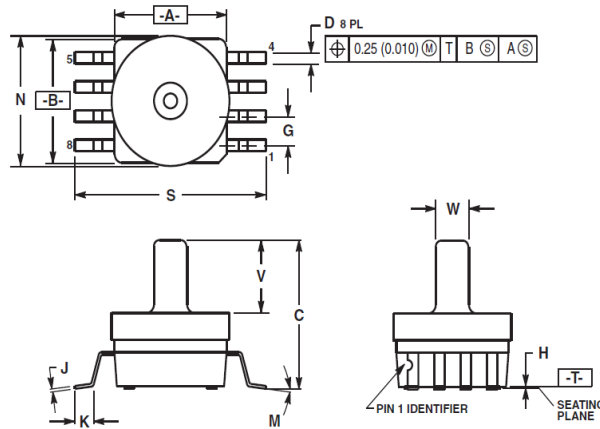
Surface mount board layout is a critical portion of the total design. The footprint for the semiconductor package must be the correct size to ensure proper solder connection interface between the board and the package. With the correct pad geometry, the packages will self-align when subjected to a solder reflow process. It is always recommended to fabricate boards with a solder mask layer to avoid bridging and/or shorting between solder pads, especially on tight tolerances and/or tight layouts.

Figure 9. SOP Footprint (Case 482A)



6 Package dimensions

Figure 10. CASE 482A-01 ISSUE A SMALL OUTLINE PACKAGE



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
 4. MAXIMUM MOLD PROTRUSION 0.15 (0.006).
 5. ALL VERTICAL SURFACES 5° TYPICAL DRAFT.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.415	0.425	10.54	10.79
B	0.415	0.425	10.54	10.79
C	0.500	0.520	12.70	13.21
D	0.038	0.042	0.96	1.07
G	0.100 BSC		2.54 BSC	
H	0.002	0.010	0.05	0.25
J	0.009	0.011	0.23	0.28
K	0.061	0.071	1.55	1.80
M	0"	0"	0"	0"
N	0.444	0.448	11.28	11.38
S	0.709	0.725	18.01	18.41
Y	0.245	0.255	6.22	6.48
W	0.115	0.125	2.92	3.17

Revision history

Table 5. Document revision history

Date	Revision	Changes
07-Jul-2026	1	Initial release from ST, rebranded NXP document

Contents

1	Ordering information	2
2	Operating characteristics	3
3	Maximum ratings	4
3.1	Maximum ratings	4
3.2	Pin functions	5
4	On-chip temperature compensation and calibration	6
5	Surface mounting information	9
5.1	Minimum recommended footprint for small outline package	9
6	Package dimensions	10
	Revision history	11
	List of tables	13
	List of figures	14

List of tables

Table 1.	Ordering information.	2
Table 2.	Operating Characteristics ($V_S = 5.0$ Vdc, $T_A = 25^\circ\text{C}$ unless otherwise noted, $P1 > P2$).	3
Table 3.	Maximum Ratings ⁽¹⁾	4
Table 4.	Pin functions	5
Table 5.	Document revision history	11

List of figures

Figure 1.	Fully integrated pressure sensor schematic	4
Figure 2.	Full device pinout (top view)	4
Figure 3.	Cross Sectional Diagram SSOP (not to scale)	6
Figure 4.	Typical Application Circuit (Output Source Current Operation).	6
Figure 5.	Output vs. Gauge Pressure	7
Figure 6.	Transfer Function (MPXV5050V).	7
Figure 7.	Temperature Error Band	7
Figure 8.	Pressure Error Band	8
Figure 9.	SOP Footprint (Case 482A)	9
Figure 10.	CASE 482A-01 ISSUE A SMALL OUTLINE PACKAGE	10

IMPORTANT NOTICE – READ CAREFULLY

STMicroelectronics NV and its subsidiaries (“ST”) reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice.

In the event of any conflict between the provisions of this document and the provisions of any contractual arrangement in force between the purchasers and ST, the provisions of such contractual arrangement shall prevail.

The purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST’s terms and conditions of sale in place at the time of order acknowledgment.

The purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of the purchasers’ products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

If the purchasers identify an ST product that meets their functional and performance requirements but that is not designated for the purchasers’ market segment, the purchasers shall contact ST for more information.

ST and the ST logo are trademarks of ST. For additional information about ST trademarks, refer to www.st.com/trademarks. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2026 STMicroelectronics – All rights reserved