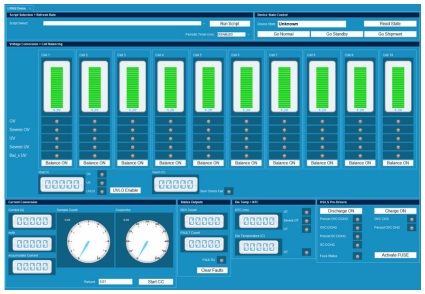
STSW-L9962GUI

Data brief

L9962 board GUI



Product status link
STSW-L9962GUI

Features

- PC GUI for L9962 battery management system (BMS) evaluation
- Control interface for EVL9962 evaluation board
- Easy to use and intuitive tabbed interface (Demo, Main, NVM, Fault, Register Load)
- Battery stack monitoring: 10 cells voltage, Vbat, Vsum
- Current measurement and Coulomb counting (mA, mAh, Coulomb count)
- NTC voltage and die temperature acquisition
- HS/LS pre-driver control (charge, discharge, fuse switches) and fault indication
- Status monitoring: FAULTN, RDY, fault counters and clear faults functions
- I2C register map access (read/write, Read All, Write All)
- Script engine (Register Load) for custom command sequences and automated tests
- Logging of sequence execution and IO data to CSV files
- Based on NUCLEO-G071RB STM32 Nucleo-64 development board
- Offline Windows installer with integrated USB driver installation
- MCU firmware update for NUCLEO-G071RB provided as part of the package.

Description

The STSW-L9962GUIis a PC software package for the evaluation of the L9962 battery management system (BMS) device. Through an intuitive graphical user interface (GUI), the user can control the EVL9962 evaluation board and access all the main features of the L9962.

The GUI communicates with an STM32G071RB microcontroller hosted on the NUCLEO-G071RB STM32 Nucleo-64 development board via a standard USB serial communication port. The STM32G071 then interfaces directly with the L9962 device via I2C and GPIO.

The L9962 GUI provides two complementary ways to interact with the device:

- High-level “L9962 Demo” tab, which offers an application-oriented view of the BMS (cell voltages, battery voltage and sum, NTC and die temperature, current conversion and Coulomb counting, HS/LS pre-driver control, device state selection and fault/status outputs).
- Low-level register map access through the Main, NVM, Fault and Register Load tabs.

Using the Demo tab, the user can quickly configure the device by means of pre-defined or custom scripts and monitor all key system parameters, such as cell voltages, battery voltage, battery sum voltage, current, NTCs and die temperature, as well as control cell balancing and external pre-driver stages. The Main and NVM tabs expose all non-fault and NVM-related registers as editable fields with read/write and Read All/Write All options, while the Fault tab offers full visibility of the fault registers and provides Read All, Write All and Clear Faults functions. The Register Load tab implements a powerful script engine that allows the user to create, save, load and execute custom sequences of commands (read, write, delay, for-loops, conditional statements, inform, pause, etc.). Script execution can be logged to CSV files, enabling traceability and offline analysis of the device behavior. The L9962 software package also includes the binary firmware image for the NUCLEO-G071RB board and sample scripts to enable key parameters such as cell voltage acquisition, battery current measurement and temperature sensing. For details about the L9962 BMS device electrical and functional characteristics, refer to the L9962 datasheet.

1 Supported boards

Table 1. Supported boards name

Development board	Description
NUCLEO-G071RB	STM32 Nucleo-64 development board with STM32G071RB MCU, used as communication bridge between PC and L9962 (USB to I2C/GPIO)
Evaluation board	**Description**
EVL9962	Evaluation board based on L9962 BMS device

Revision history

Table 2. **Document revision history**

Date	Version	Changes
25-Feb-2026	1	Initial release.

Contents

List of tables

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