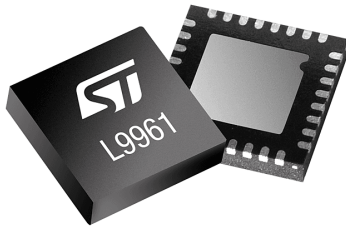


L9961 register maps



Features

This software package contains a complete register map definition for the L9961 device. It provides a consistent, code-oriented description of the device's registers, bit fields, and access properties, aligned with the register map also provided in the Excel sheet.

The package is intended to help developers:

- Access device registers by name
- Manipulate individual bit fields safely
- Generate or parse register values
- Keep firmware, documentation, and hardware definitions synchronized

Package Contents

The package includes:

- **L9961_regmap_BIG.h**
C structure and bitfield definitions for **big-endian/MSB-first** bit ordering
- **L9961_regmap_LITTLE.h**
C structure and bitfield definitions for **little-endian / LSB-first** bit ordering
- **L9961_regmap_DEFINE.h**
Flat #define constants for:
 - Register addresses
 - Bit offsets
 - Bit widths
 for every field in the device map

Excel register map file

A spreadsheet reference documenting:

- Register names
- Addresses
- Field names
- Access types such as **RO**, **RW**, **WO**, and **RLW**
- Bit offsets/widths
- Reset values and reset sources

1 Supported devices

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Table 1. Supported L9965x family devices

Device	Description
L9961	Cell monitor and pack monitor IC

Revision history

Table 2. Document revision history

Date	Version	Changes
03-Jul-2026	1	Initial release.



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