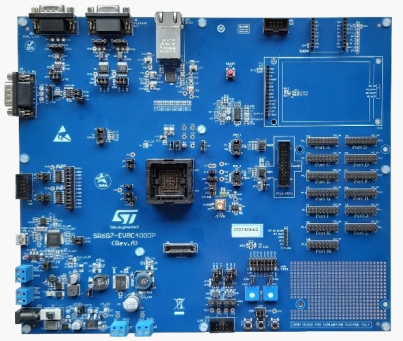
SR6G7-EVBC4000P

Data brief

SR6G7C4 Stellar G7 BGA 292 evaluation board



Product status link
SR6G7-EVBC4000P

Product summary	
Order code	SR6G7-EVBC4000P
Reference	SR6G7C4 evaluation board
Package	BGA 292

Features

- Socket based evaluation board for SR6G7C4 Stellar G7 automotive MCU in BGA 292 package
- All MCU signals readily accessible at a port-ordered group of 0.1” pitch headers
- Selectable clock source:
  - 40 MHz crystal main oscillator
  - 8 MHz oscillator
  - 32 kHz oscillator
  - Clock input through SMA connector
- Debug interfaces:
  - MIPI10 connector for JTAG main DAP interface
  - ARM® JTAG 20 connector for main DAP interface
  - MIPI10 connector for secondary DAP interface
  - Mictor40 connector for AURORA trace interface
- LFAST: 2x6-pin header connector
- USB to UART: 2xUART channels (USB MINI-B)
- 4x CAN-FD interfaces: 2 channels with DB9 + 2 channels with a 2x4 header connector
- 4x LINFlexD interfaces with a 2x4 header connector
- 2x FlexRay: 1xDB9 connector
- Ethernet 0: RGMII 1 Gb/s with RJ45 connector
- I²C interface: 2 channels
- User section: 3 push buttons; 8 LEDs; 2 potentiometers
- Extension module port (option):
  - 1x external module connector (DSPI, I²C, UART, GPIO, ANx)
  - 1x LCD DisplayPort
- 12 V external power supply

Description

The SR6G7-EVBC4000P is the evaluation board of the SR6G7C4 Stellar G7 automotive MCU in BGA 292 package enabling the access to all the functionalities of the product.

Being based on socket, it can be the best solution to start prototyping any automotive application.

The board provides automotive Ethernet interfaces, FlexRay channels, CAN FD channels, LINFlexD, UART, I2C and SPI standard communication interface, as well as LEDs, buttons and potentiometers for user controls.

ST’s StellarStudio, an Eclipse-based integrated development environment, provides a comprehensive framework to design, build, and deploy embedded applications. StellarStudio is available for free download from www.st.com and includes multiple free application examples ready to use on the SR6G7-EVBC4000P board.

Revision history

Table 1. **Document revision history**

Date	Revision	Changes
27-Jun-2025	1	Initial release.
16-Jul-2025	2	Removed watermark ST restricted.

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