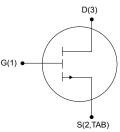


700 V, 165 m Ω typ., 10 A, e-mode PowerGaN transistor

Features





S(2,TAB)

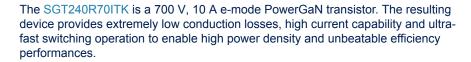
- Order code
 V_{DS} $R_{DS(on)}$ max.
 I_D Series

 SGT240R70ITK
 700 V
 240 mΩ
 10 A
 G-HEMT
- Enhancement mode normally off transistor
- Very high switching speed
- · High power management capability
- Extremely low capacitances
- Zero reverse recovery charge
- ESD safeguard

Applications

- Adapters for tablets, notebook and AIO
- DC-DC converters
- USB type-C PD adapters and quick chargers

Description





Product status link	
SGT240R70ITK	

Product summary			
Order code SGT240R70ITK			
Marking 240R70I			
Package	DPAK		
Packing Tape and reel			



1 Electrical ratings

 T_C = 25 °C unless otherwise specified.

Table 1. Absolute maximum ratings

Symbol	Parameter	Value	Unit	
V	Drain-source voltage	700 ⁽¹⁾	V	
V _{DS}	Drain-source voltage (transient, t _p < 200 μs)	800	_ v	
V _{GS}	Gate-source voltage	-6 to 7	V	
I _D	Drain current (continuous) at T _C = 25 °C	10	Α	
I _{DM}	Pulse drain current (t _p = 10 μs)	18	Α	
P _{TOT}	Total power dissipation at T _C = 25 °C	77	W	
T _{stg}	Storage temperature range	FF to 450	°C	
TJ	Operating junction temperature range			

^{1.} Recommended continuous maximum bus voltage during switching operations should not exceed 450 V.

Table 2. Thermal data

Symbol	Parameter	Value	Unit
R _{thJC}	Thermal resistance, junction-to-case	1.62	°C/W
R _{thJA} ⁽¹⁾	Thermal resistance, junction-to-ambient	55	°C/W

1. When mounted on a standard 1 inch² area of FR-4 PCB with 2-oz copper.

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2 Electrical characteristics

 T_C = 25 °C unless otherwise specified.

Table 3. Static

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
	I _{DSS} Drain-source leakage current	V _{GS} = 0 V, V _{DS} = 700 V		0.4	20	
DSS		V _{GS} = 0 V, V _{DS} = 700 V, T _J = 150 °C		5		μA
I _{GSS}	Gate-source leakage current	V _{DS} = 0 V, V _{GS} = 6 V		50		μA
V _{GS(th)}	Gate threshold voltage	$V_{DS} = V_{GS}$, $I_D = 11 \text{ mA}$	1.2	1.7	2.5	V
R _{DS(on)} Static	0	V _{GS} = 6 V, I _D = 3 A		165	240	
	Static drain-source on-resistance	V _{GS} = 6 V, I _D = 3 A, T _J = 150 °C		360		mΩ

Table 4. Dynamic

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C _{iss}	Input capacitance		-	79	-	pF
C _{oss}	Output capacitance	V _{GS} = 0 V, V _{DS} = 400 V, f = 100 kHz	-	25	-	pF
C _{rss}	Reverse transfer capacitance		-	0.2	-	pF
C _{o(er)} ⁽¹⁾	Equivalent output capacitance energy related	V _{GS} = 0 V, V _{DS} = 0 to 400 V	-	36	-	pF
C _{o(tr)} ⁽²⁾	Equivalent output capacitance time related	VGS - 0 V, VDS - 0 10 400 V	-	52	-	pF
R_g	Intrinsic gate resistance	f = 5 MHz, I _D = 0 A	-	6	-	Ω
V _{plat}	Gate plateau voltage	V _{DS} = 400 V, I _D = 3 A	-	2.5	-	V
Qg	Total gate charge		-	2	-	nC
Q_{gs}	Gate-source charge	$V_{GS} = 0 \text{ to } 6 \text{ V}, V_{DS} = 400 \text{ V}, I_D = 3 \text{ A}$	-	0.2	-	nC
Q _{gd}	Gate-drain charge	rain charge		0.7	-	nC
Qrr	Reverse recovery charge		-	0	-	nC
Q _{oss}	Output charge	V _{GS} = 0 V, V _{DS} = 400 V	-	21	-	nC

C_{o(er)} is a constant capacitance value that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to the stated value

Table 5. Switching times

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)}	Turn-on delay time			2	-	ns
t _r	Rise time	V_{DS} = 400 V, I_{D} = 6 A, V_{GS} = 6 V,	-	5	-	ns
t _{d(off)}	Turn-off delay time	$R_{G(on)} = 10 \Omega$, $R_{G(off)} = 2 \Omega$, L = 318 µH		4	-	ns
t _f	Fall time		-	6	-	ns

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^{2.} $C_{O(tr)}$ is a constant capacitance value that gives the same charging time as C_{OSS} while V_{DS} is rising from 0 to the stated value.



Table 6. Reverse conduction

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{SD}	Source-drain reverse voltage	$V_{GS} = 0 \text{ V}, I_{SD} = 3 \text{ A}$	-	2.6	-	V

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Revision history

Table 7. Document revision history

Date	Revision	Changes
08-Oct-2025	1	First release.

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