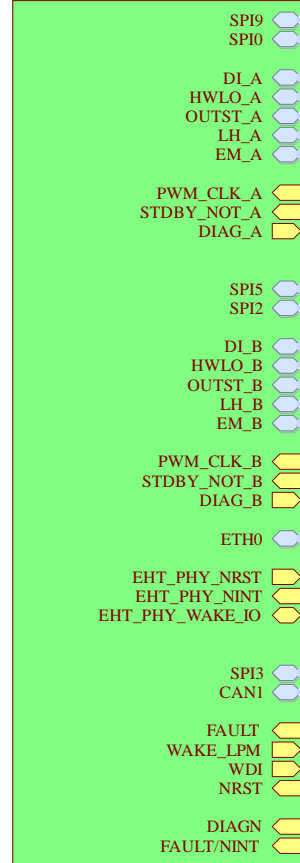
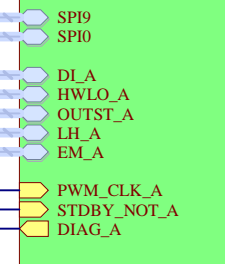


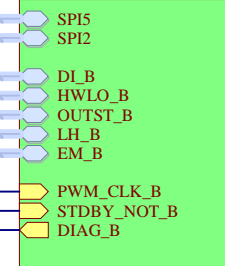
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MCU.SchDoc



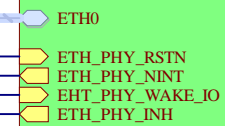
U_GroupA
GroupA.SchDoc



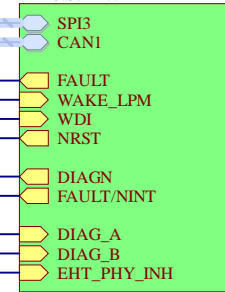
U_GroupB
GroupB.SchDoc



U_Ethernet
Ethernet.SchDoc




U_PMIC
PMIC.SchDoc



U_Connectors
Connectors.SchDoc



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Title: ST PDU - TOP level				
Size: A4	Author: EMEA - ADG - M&A	Revision: 2.3	STMicroelectronics Pobrezni 3 186 00 Prague 8 Czech Republic	 life.augmented
Date: 27/06/25	Time: 11:08:42	Sheet 1 of 14		
Reference: *	Id: *			
File: TOP_level.SchDoc		Created by: *		

A

B

C

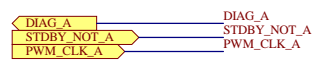
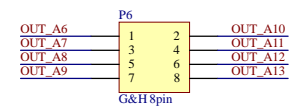
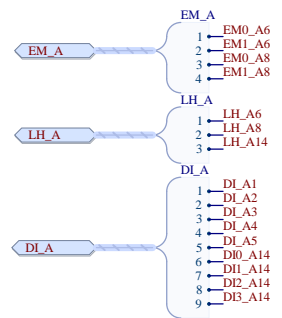
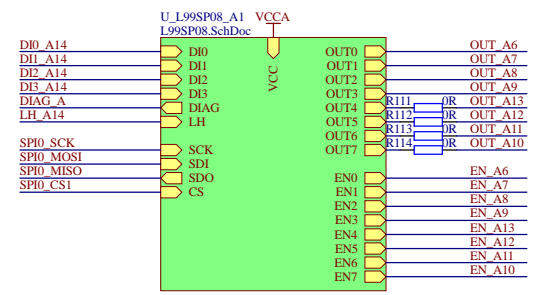
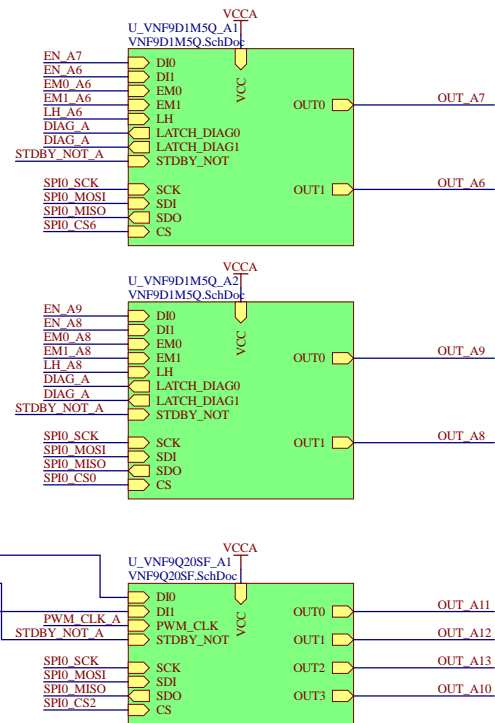
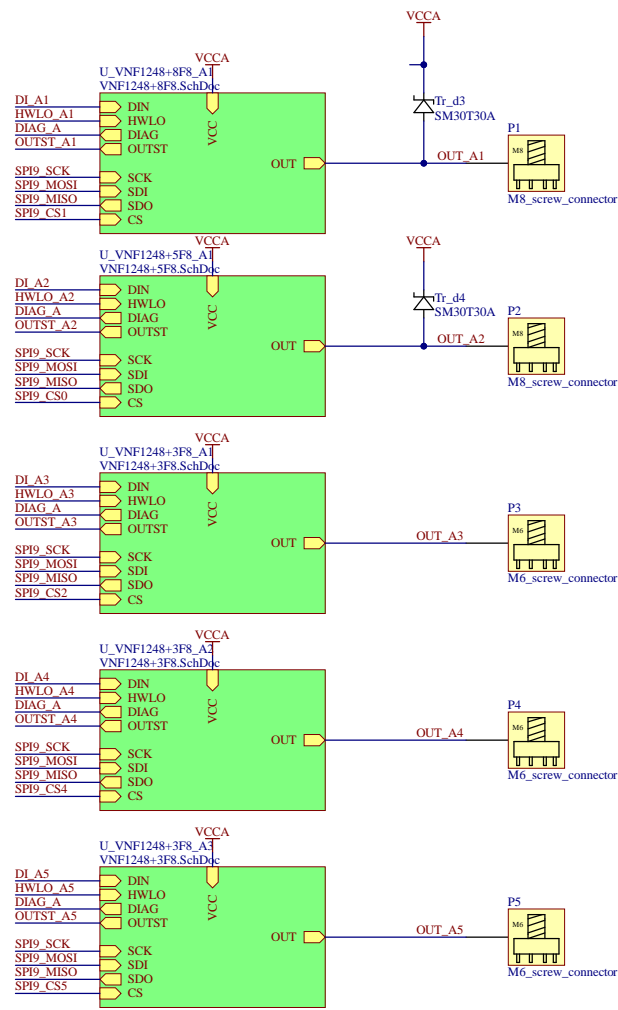
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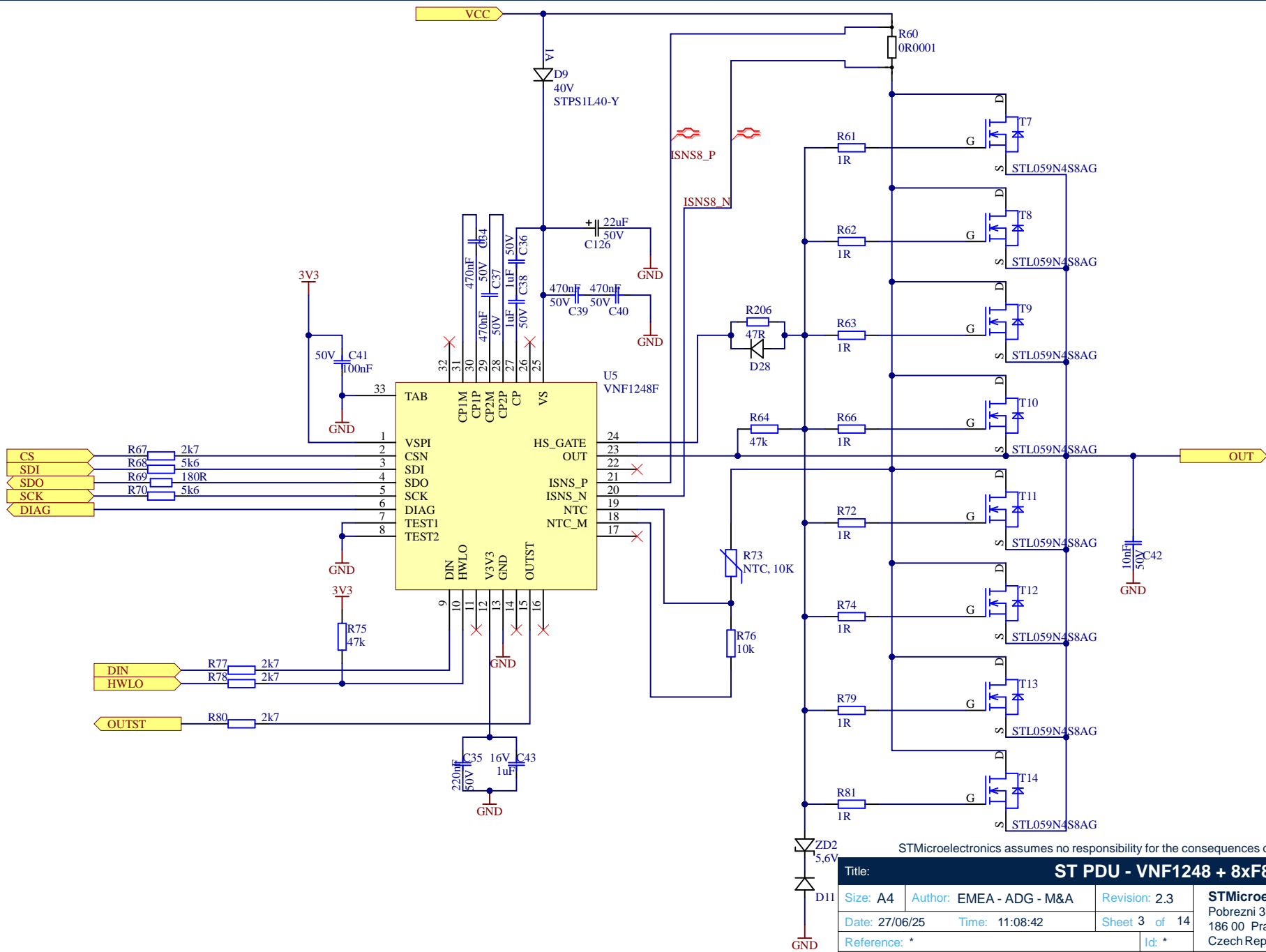
A

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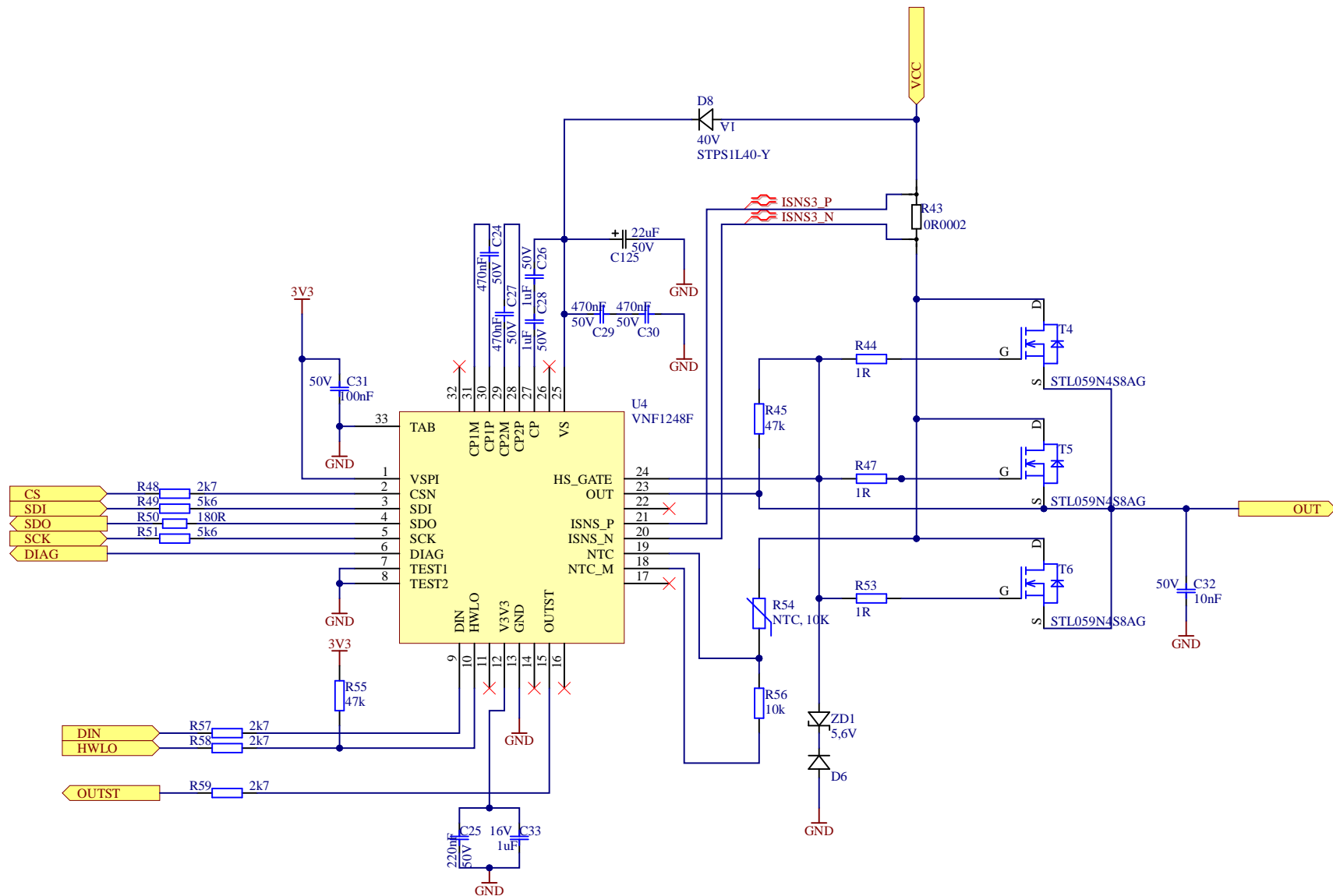
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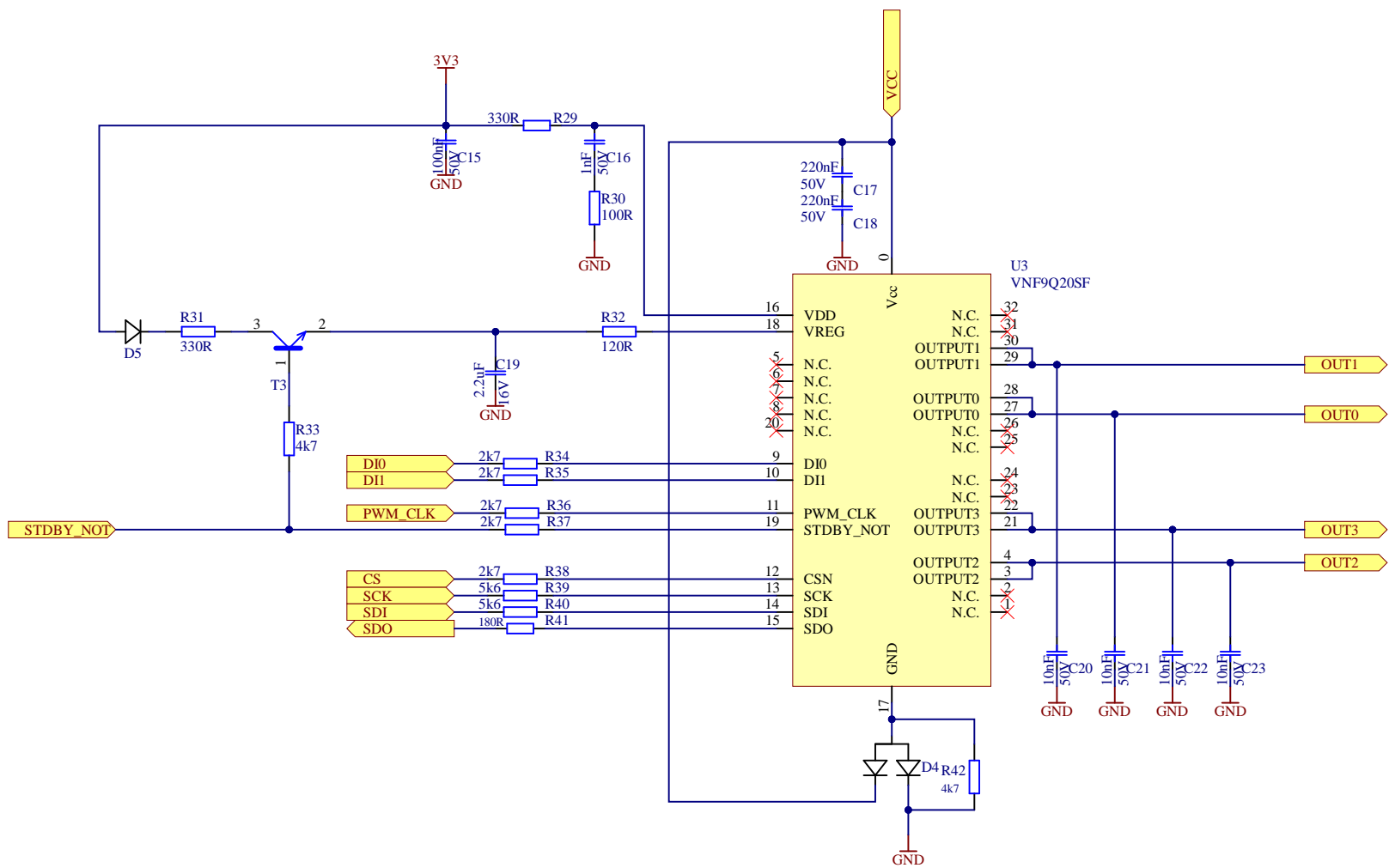
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Size: A4	Author: EMEA - ADG - M&A	Revision: 2.3	STMicroelectronics Pobrezni 3 186 00 Prague 8 Czech Republic life.augmented
Date: 27/06/25	Time: 11:08:42	Sheet 3 of 14	
Reference: *	Id: *		
File: VNF1248+8F8.SchDoc			Created by: *




STMicroelectronics assumes no responsibility for the consequences of the use of this information

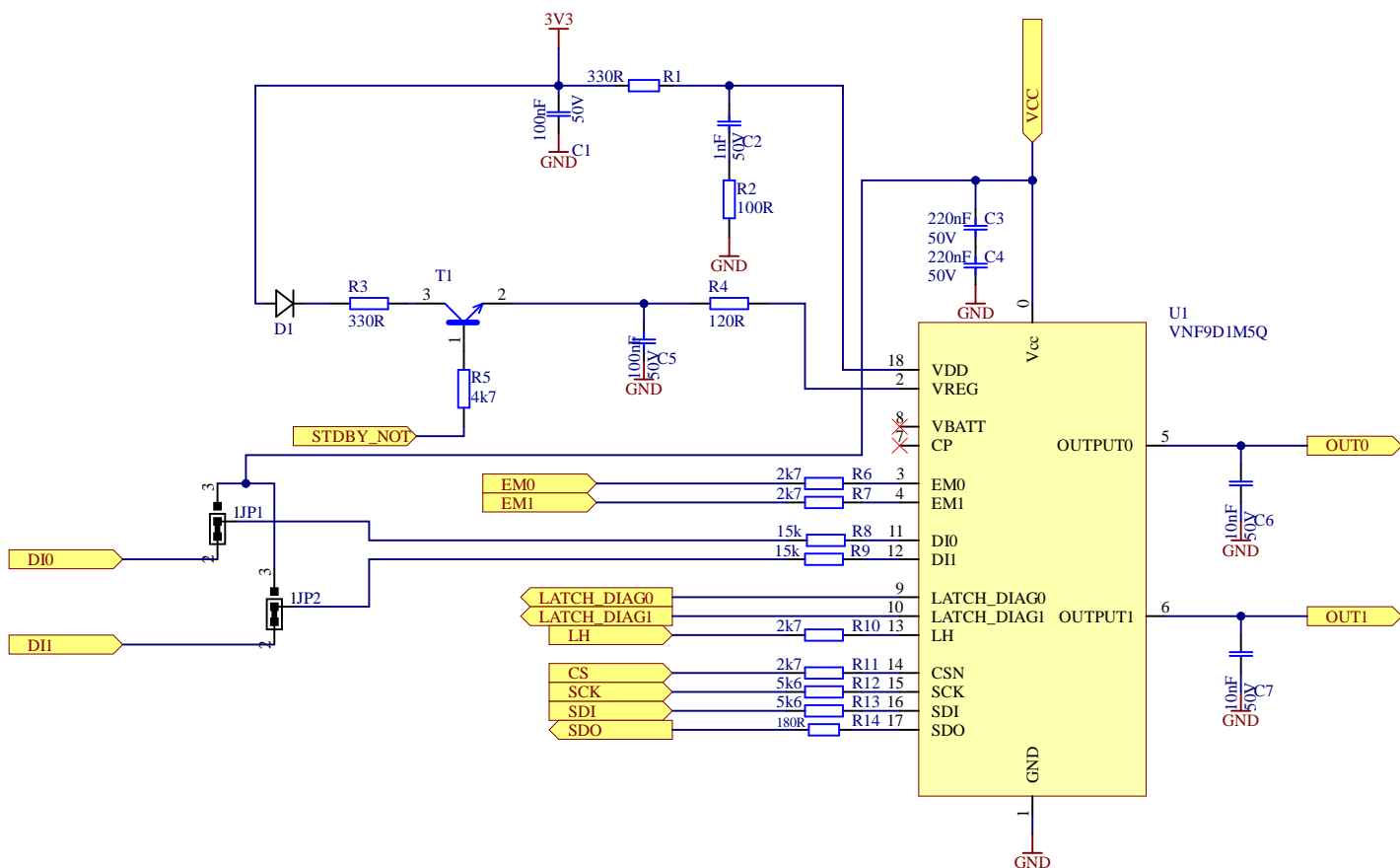
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Size: A4	Author: EMEA - ADG - M&A	Revision: 2.3	STMicroelectronics Pobrezni 3 186 00 Prague 8 Czech Republic	 life.augmented
Date: 27/06/25	Time: 11:08:42	Sheet 5 of 14		
Reference: *		Id: *		
File: VNF1248+3F8.SchDoc		Created by: *		





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Title: ST PDU - VNF9Q20SF				
Size: A4	Author: EMEA - ADG - M&A	Revision: 2.3	STMicroelectronics Pobrezni 3 186 00 Prague 8 Czech Republic	 life.augmented
Date: 27/06/25	Time: 11:08:42	Sheet 6 of 14		
Reference: *		Id: *		
File: VNF9Q20SF.SchDoc		Created by: *		



STMicroelectronics assumes no responsibility for the consequences of the use of this information

Title: ST PDU - VNF9D1M5Q				
Size: A4	Author: EMEA - ADG - M&A	Revision: 2.3	STMicroelectronics Pobrezni 3 186 00 Prague 8 Czech Republic	
Date: 27/06/25	Time: 11:08:43	Sheet 7 of 14		
Reference: *			Id: *	
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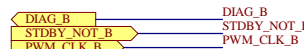
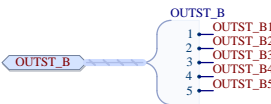
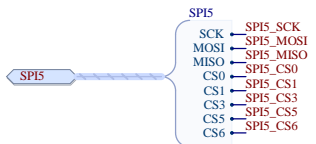
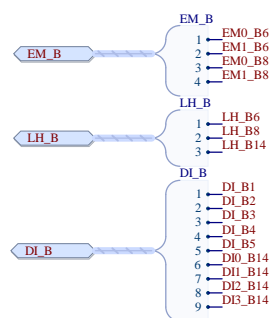
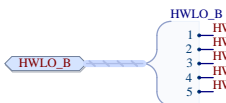
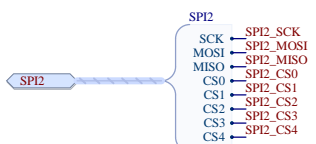
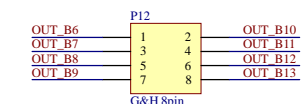
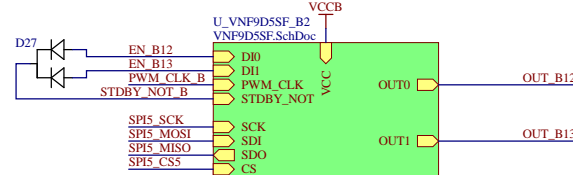
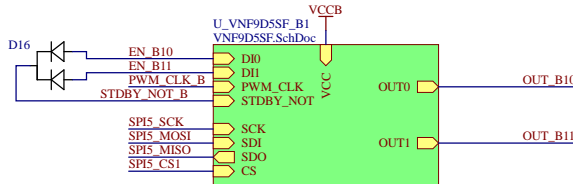
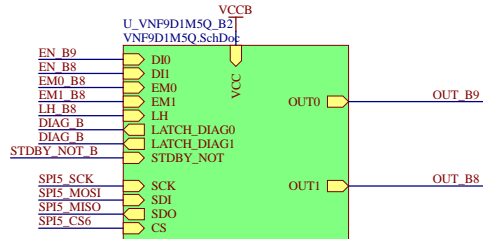
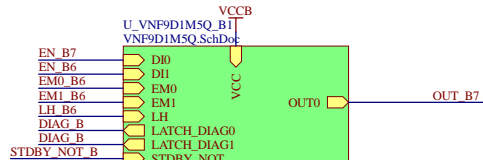
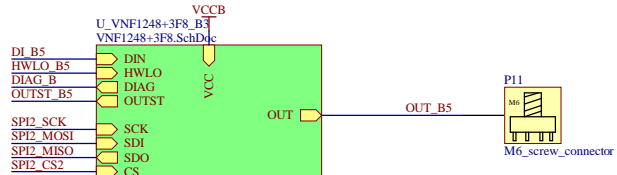
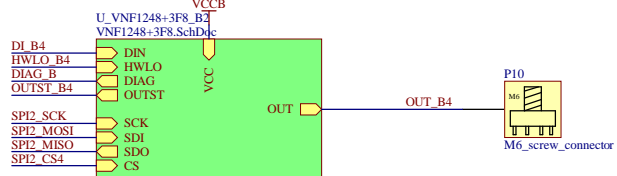
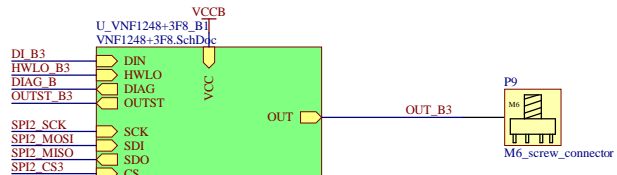
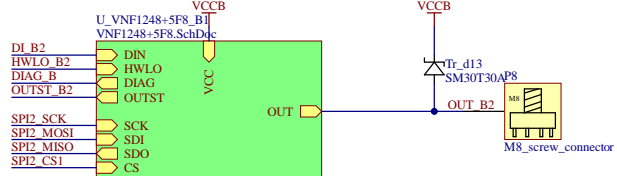
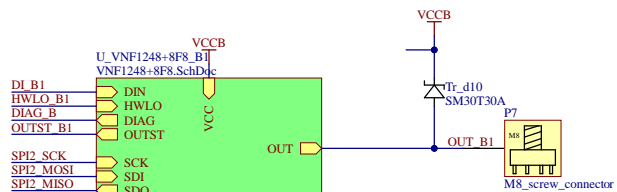
D

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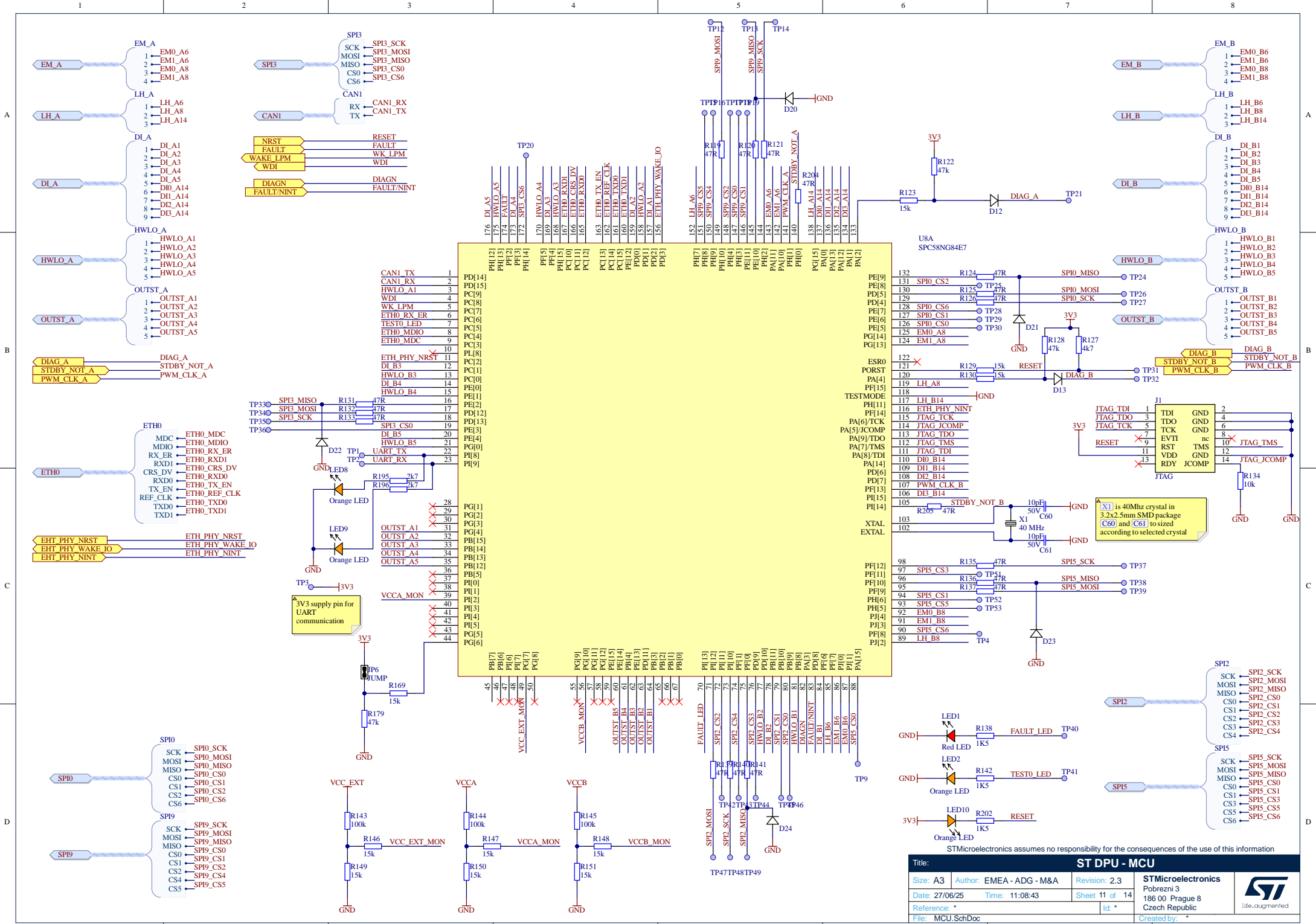


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Title: ST PDU - Group B					
Size: A3		Author: EMEA - ADG - M&A		Revision: 2.3	
Date: 27/06/25		Time: 11:08:43		Sheet 9 of 14	
Reference: *				Id: *	
File: GroupB.SchDoc				Created by: *	

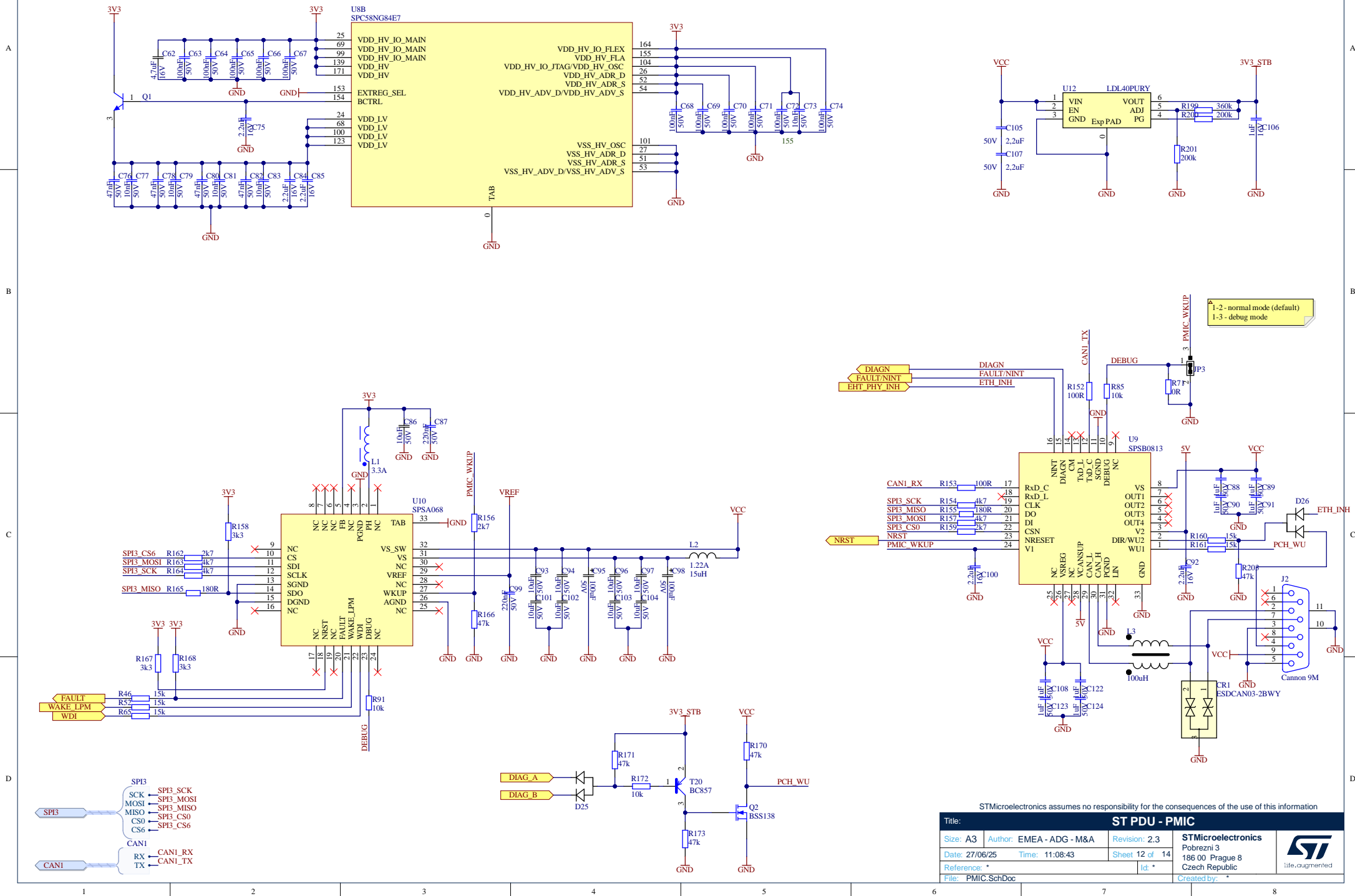
STMicroelectronics
Pobrezni 3
186 00 Prague 8
Czech Republic

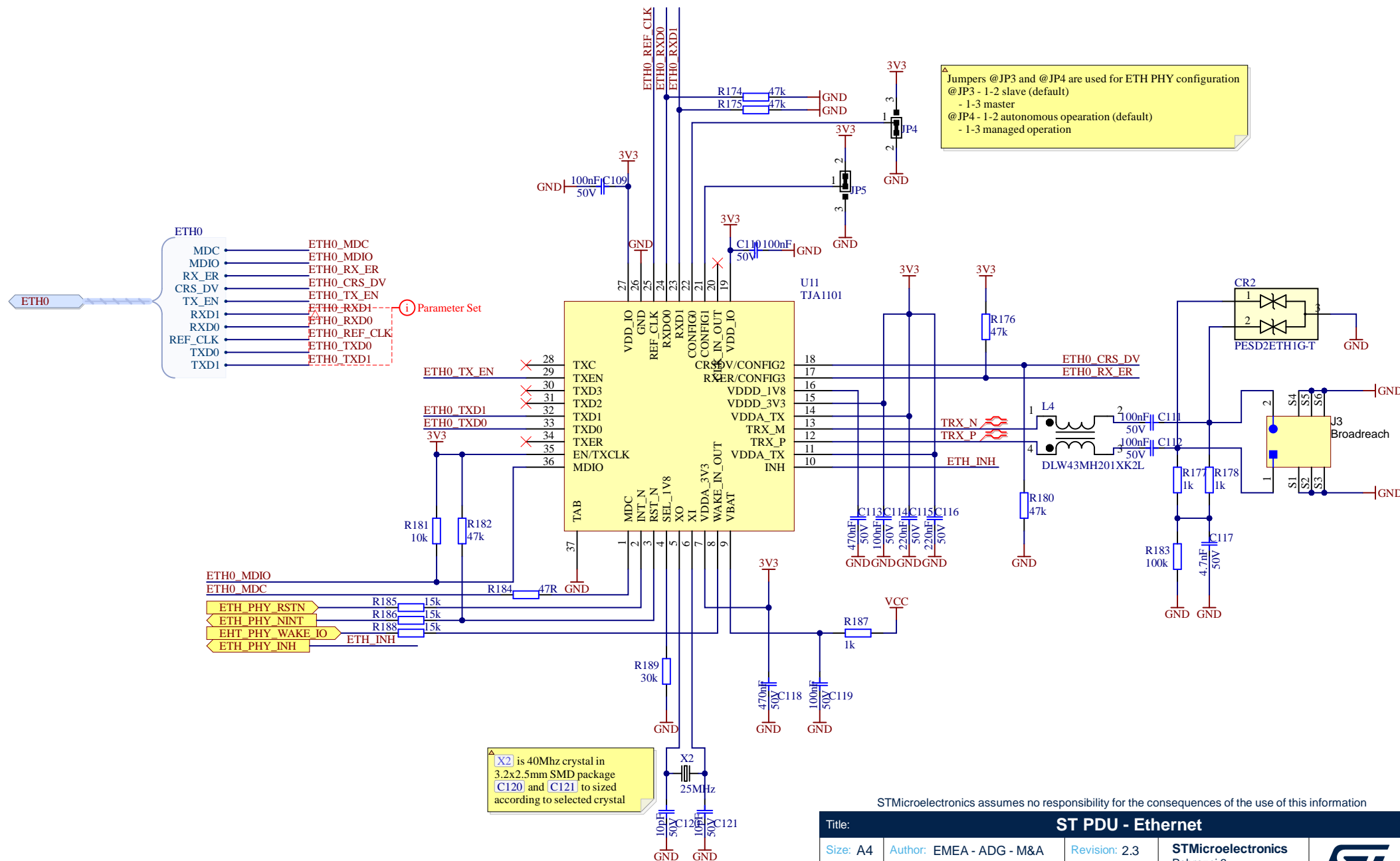

life.augmented




Title: ST DPU - MCU			
Size: A3	Author: EMEA - ADG - M&A	Revision: 2.3	STMicroelectronics
Date: 27/06/25	Time: 11:08:43	Sheet 11 of 14	Pobrezni 3
Reference: *		Id: *	186 00 Prague 8
File: MCU.SchDoc		Created by: *	Czech Republic





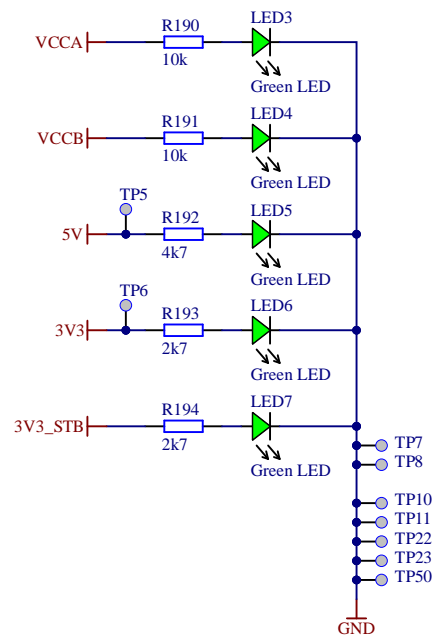
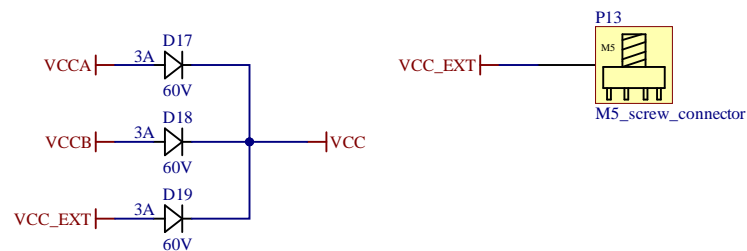
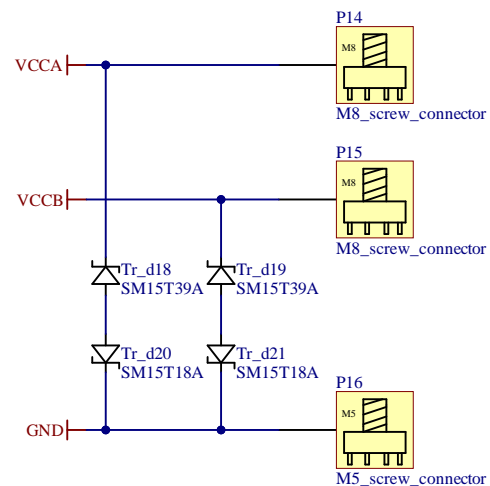


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Title: ST PDU - Ethernet				
Size: A4	Author: EMEA - ADG - M&A	Revision: 2.3	STMicroelectronics Pobrezni 3 186 00 Prague 8 Czech Republic	 life.augmented
Date: 27/06/25	Time: 11:08:43	Sheet 13 of 14		
Reference: *	Id: *			
File: Ethernet.SchDoc		Created by: *		



life.augmented



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Title: ST PDU - Power supply connection				
Size: A4	Author: EMEA - ADG - M&A	Revision: 2.3	STMicroelectronics Pobrezni 3 186 00 Prague 8 Czech Republic	
Date: 27/06/25	Time: 11:08:44	Sheet 14 of 14		
Reference: *			Id: *	
File: Connectors.SchDoc			Created by: *	



