
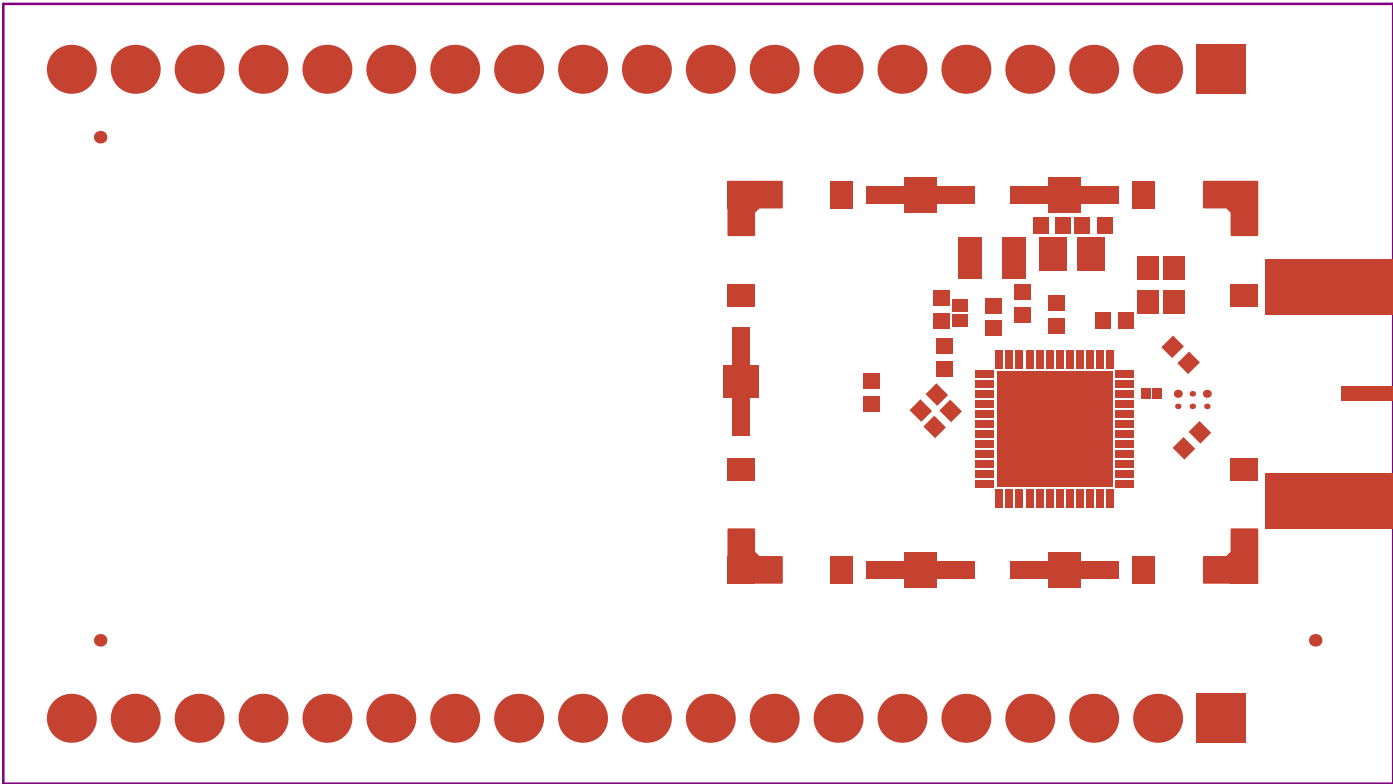

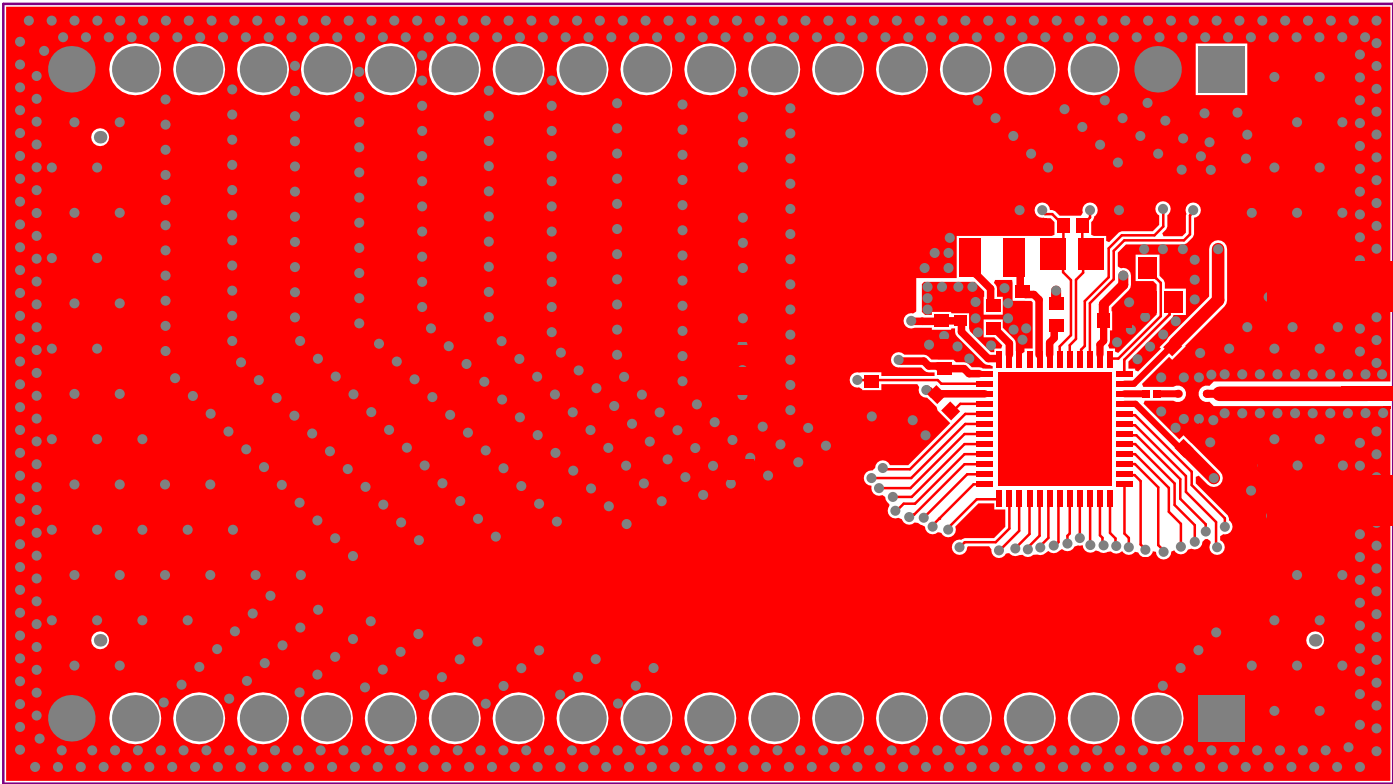



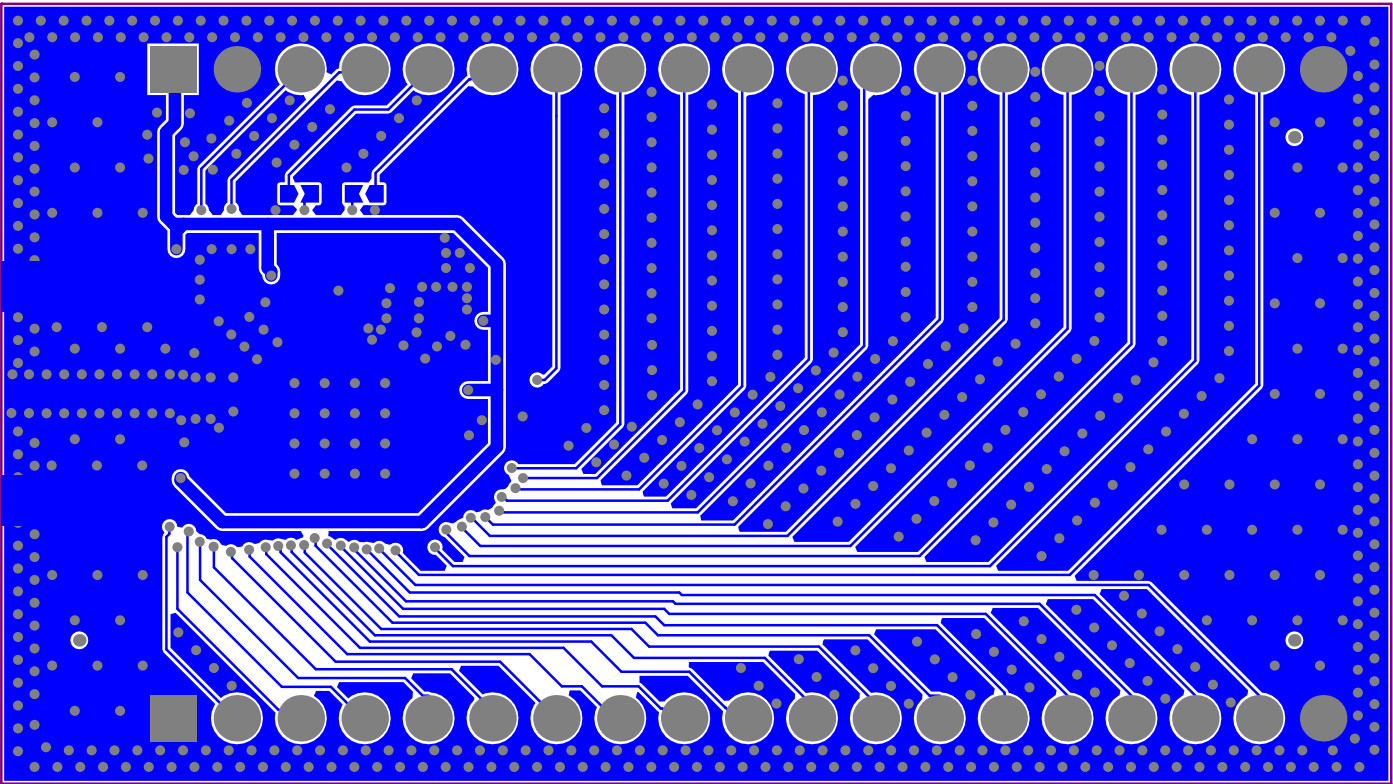
Project: STM32WB07_QFN48_2L_IPD		
Layer: Top Overlay	Gerber: .GTO	
Variant: [No Variations]	Ref: MB2172	
Date: 7 July 2024	Rev: A	




Project: STM32WB07_QFN48_2L_IPD		
Layer: Top Solder	Gerber: .GTS	
Variant: [No Variations]	Ref: MB2172	
Date: 7 July 2024	Rev: A	




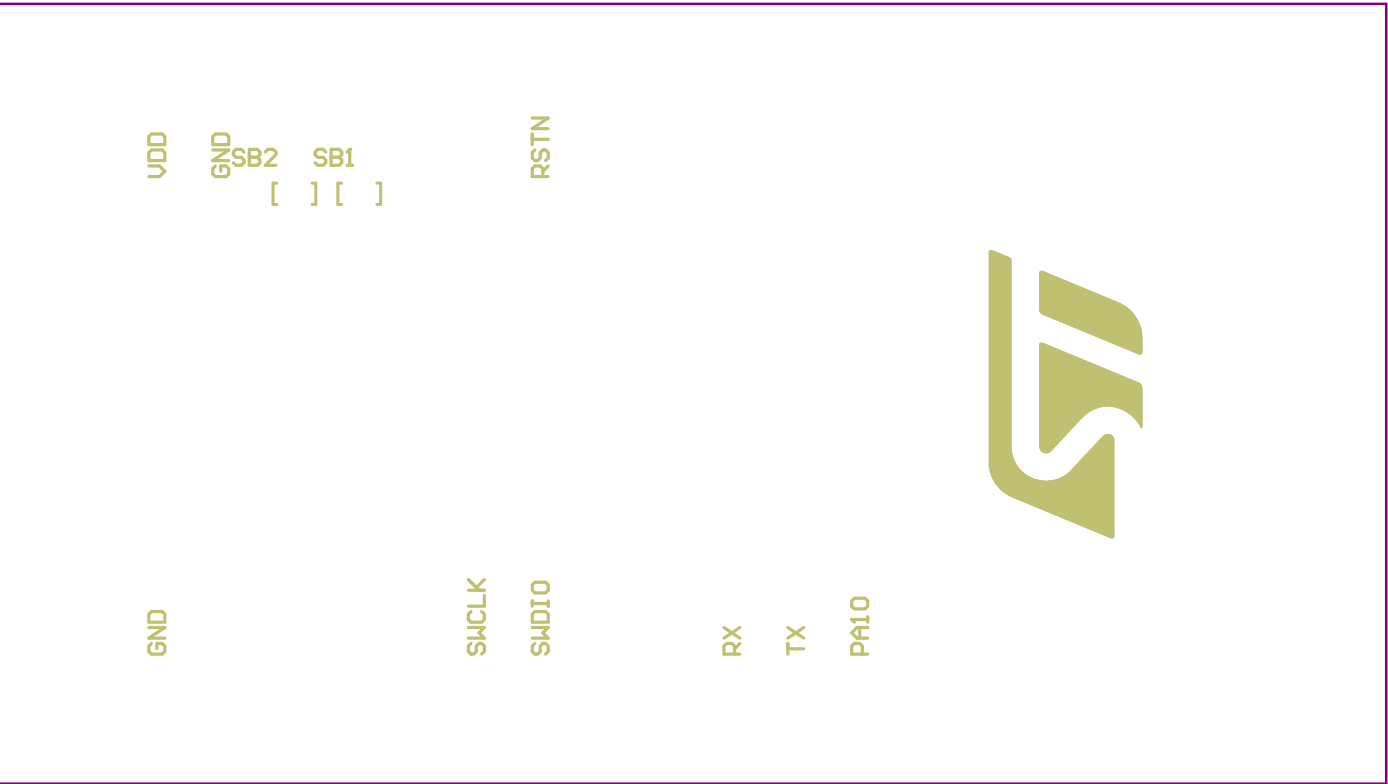
Project: STM32WB07_QFN48_2L_IPD		
Layer: Top Layer	Gerber: .GTL	
Variant: [No Variations]	Ref: MB2172	
Date: 7 July 2024	Rev: A	




Project: STM32WB07_QFN48_2L_IPD		
Layer: Bottom Layer	Gerber: .GBL	
Variant: [No Variations]	Ref: MB2172	
Date: 7 July 2024	Rev: A	



Project: STM32WB07_QFN48_2L_IPD		
Layer: Bottom Solder	Gerber: .GBS	
Variant: [No Variations]	Ref: MB2172	
Date: 7 July 2024	Rev: A	



Project: STM32WB07_QFN48_2L_IPD		
Layer: Bottom Overlay	Gerber: .GBO	
Variant: [No Variations]	Ref: MB2172	
Date: 7 July 2024	Rev: A	

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad	Pad Shape	Template
▽	844	0.200mm (7.87mil)	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	v40h20m0mx0
□	38	1.000mm (39.37mil)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	(Mixed)
	882 Total							

PCB SPECIFICATIONS :

A. MATERIAL :

FR-4☒TG-170☐TG-150☐TG-140

B. MATERIAL FAMILY :

N/A

C. SOLDERMASK COLOR :

☒GREEN☐BLUE☐RED☐BLACK

D. SILKSCREEN COLOR :

☒WHITE☐YELLOW☐BLACK

E. SURFACE FINISH :

☒ENIG☐IMMERSION SILVER☐IMMERSION TIN
☐HASL☐HASL (PB-FREE)☐GOLDEN FINGER

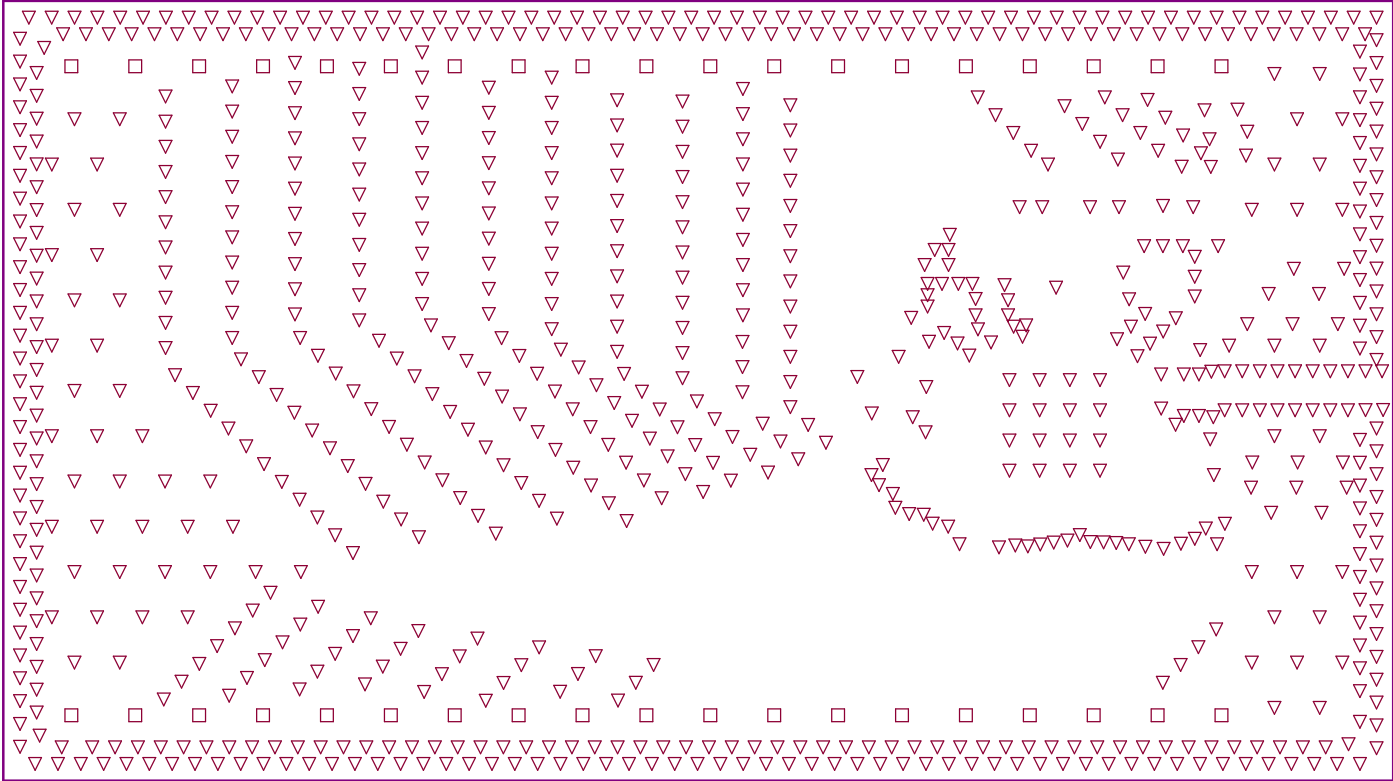
☒IMPEDANCE CONTROL :☐NO☒YES (SEE IMPEDANCE TABLE FOR DETAIL INFORMATION)

G. THROUGH VIA :


PLUG THE VIAS WHICH ARE COVERED WITH SOLDERMASK ONE OR TWO SIDE.
PLUG MATERIAL : ☒SOLDERMASK☐NON-CONDUCTIVE EPOXY.

☒STACK-UP :

SEE LAYER STACK-UP SEQUENCE FOR OVERALL THICKNESS.



Layer	Name	Material	Thickness	Constant
	Top Overlay			
	Top Solder	Solder Resist	0.020mm	3.5
1	Top Layer		0.035mm	
	Dielectric 1	TU-768 (3 x 7628)	0.533mm	4.6
2	Bottom Layer		0.035mm	
	Bottom Solder	Solder Resist	0.020mm	3.5
	Bottom Overlay			

Project: STM32WB07_QFN48_2L_IPD		
Layer: Drill Drawing	Gerber: .DRL	
Variant: [No Variations]	Ref: MB2172	
Date: 7 July 2024	Rev: A	

IMPEDANCE TABLE					
LAYER	TRACE (mm)	SPACING (mm)	IMPEDANCE (Single ended)	IMPEDANCE (Differential)	TOL.
TOP	0.58	0.2	50 ohm	NA	+/- 10%
TOP	0.28	0.135	57 ohm	NA	+/- 10%