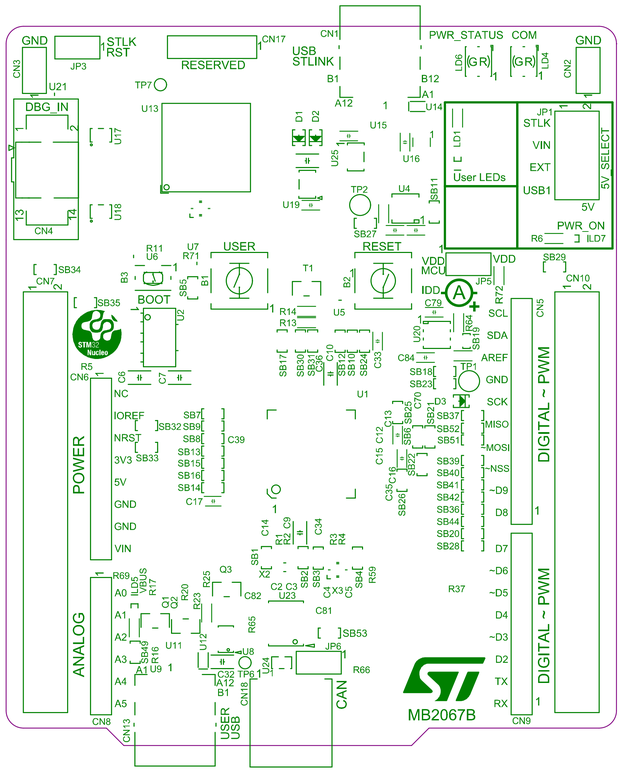
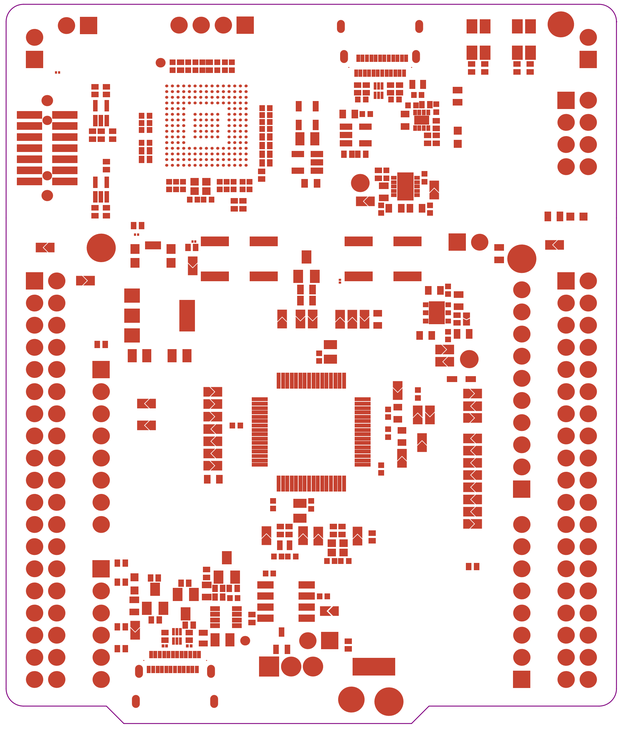
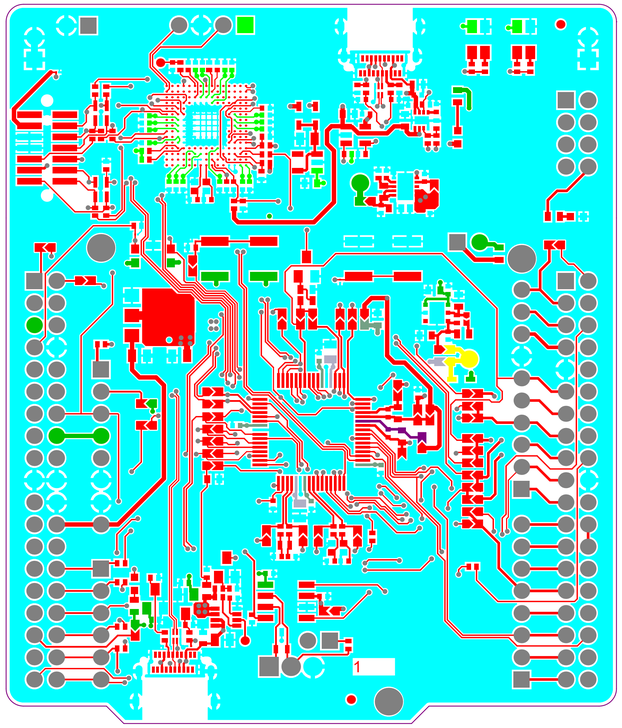


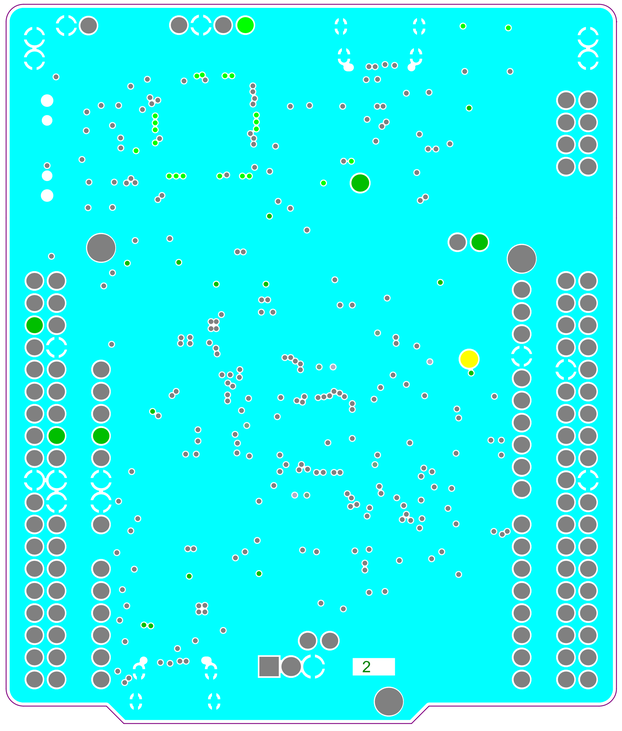
Project: NUCLEO-64 STM32G5	
Layer: Top Overlay	Gerber: .GTO
Variant: [No Variations]	MB2067
Date: 09-JAN-2026	Rev: B



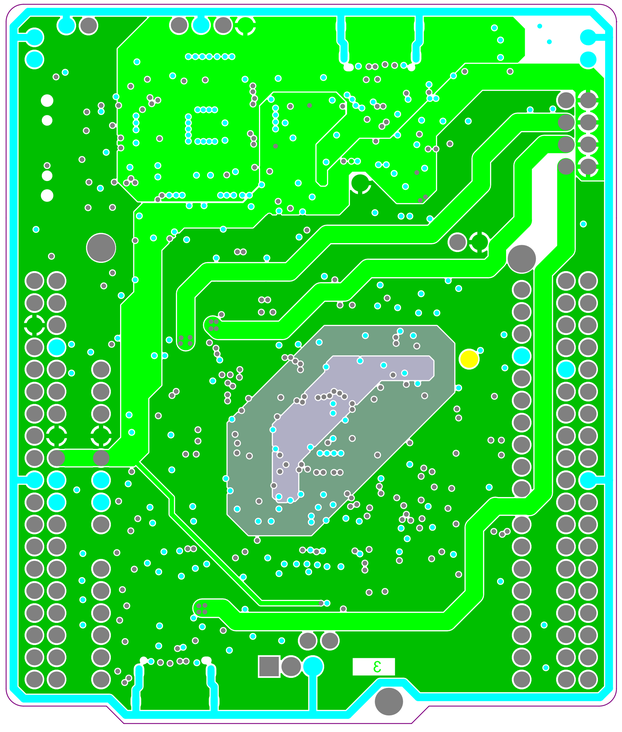
Project: NUCLEO-64 STM32G5	
Layer: Top Solder	Gerber: .GTS
Variant: [No Variations]	MB2067
Date: 09-JAN-2026	Rev: B



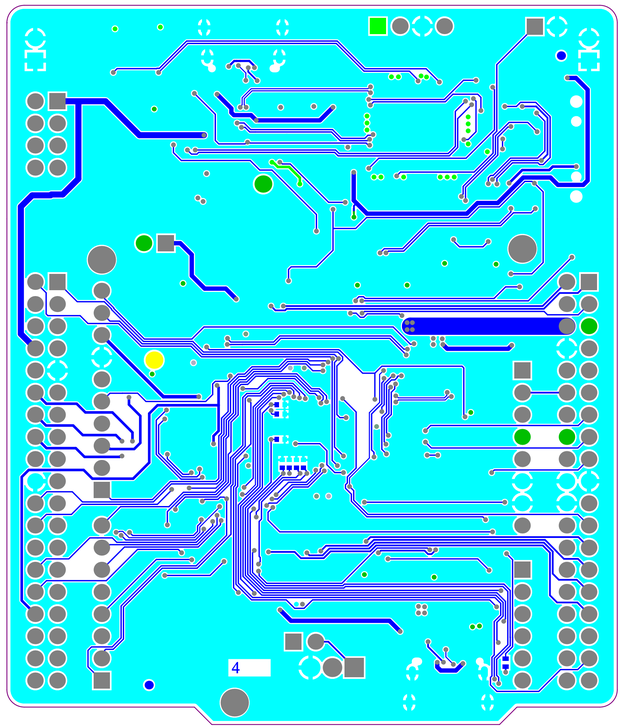
Project: NUCLEO-64 STM32G5	
Layer: Top Layer	Gerber: .GTL
Variant: [No Variations]	MB2067
Date: 09-JAN-2026	Rev: B



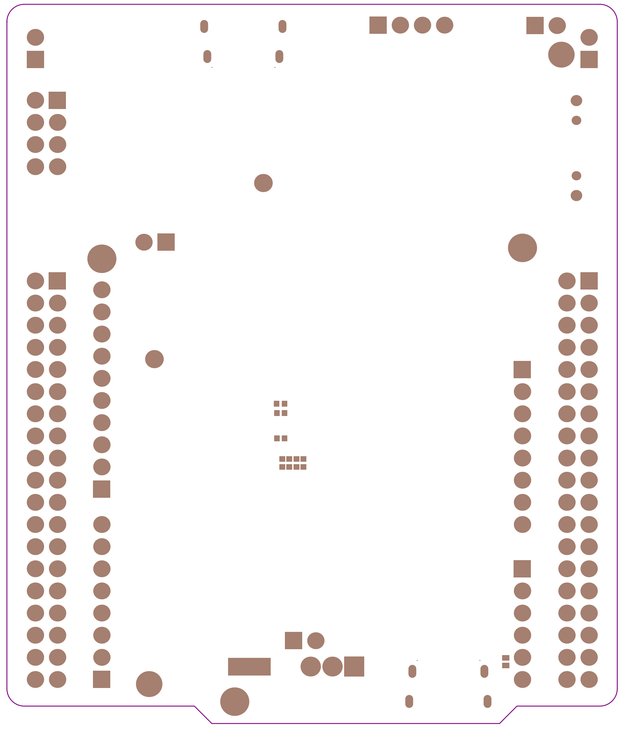
Project: NUCLEO-64 STM32G5	
Layer:	Gerber:
Variant: [No Variations]	MB2067
Date: 09-JAN-2026	Rev: B



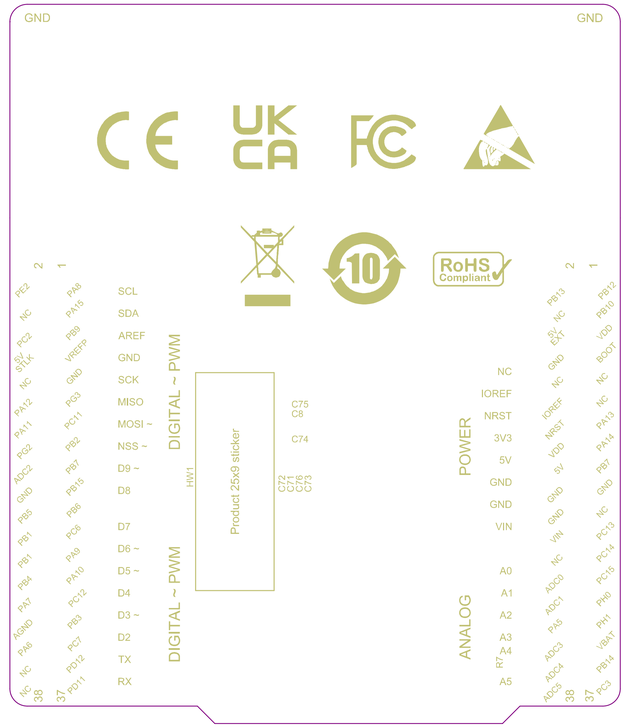
Project: NUCLEO-64 STM32G5	
Layer:	Gerber:
Variant: [No Variations]	MB2067
Date: 09-JAN-2026	Rev: B



Project: NUCLEO-64 STM32G5	
Layer: Bottom Layer	Gerber:.GBL
Variant: [No Variations]	MB2067
Date: 09-JAN-2026	Rev: B



Project: NUCLEO-64 STM32G5	
Layer: Bottom Solder	Gerber:.GBS
Variant: [No Variations]	MB2067
Date: 09-JAN-2026	Rev: B

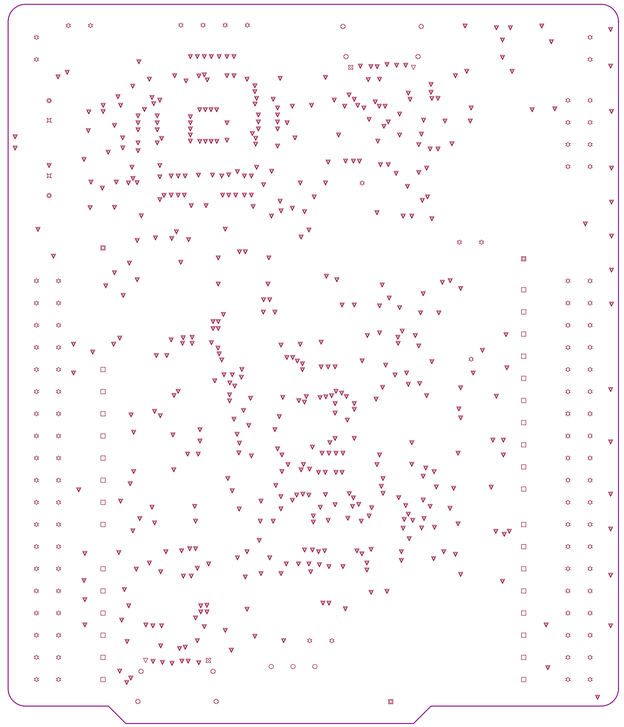


Project: NUCLEO-64 STM32G5	
Layer: Bottom Overlay	Gerber:.GBO
Variant: [No Variations]	MB2067
Date: 09-JAN-2026	Rev: B

PCB SPECIFICATIONS :

**Plating type : lead Gold

A.	MATERIAL :	FR-4	☐ TG-170	☒ TG-150	☐ TG-140
B.	MATERIAL FAMILY :	N/A			
C.	SOLDERMASK COLOR :	☐ GREEN	☒ WHITE	☐ RED	☐ BLACK
D.	SILKSCREEN COLOR :	☐ WHITE	☐ YELLOW	☐ BLACK	☒ Blue ink PANTONE 2955
E.	SURFACE FINISH :	☒ ENIG	☐ IMMERSION SILVER		☐ IMMERSION TIN
		☐ HASL	☐ HASL (PB-FREE)		☐ GOLDEN FINGER
F.	IMPEDANCE CONTROL :	☐ NO	☒ YES (SEE IMPEDANCE TABLE FOR DETAIL INFORMATION)		
G.	THROUGH VIA :	PLUG THE VIAS WHICH ARE COVERED WITH SOLDERMASK ONE OR TWO SIDE.			
		PLUG MATERIAL : ☒ SOLDERMASK ☐ NON-CONDUCTIVE EPOXY.			
H.	STACK-UP :	SEE LAYER STACK-UP SEQUENCE FOR OVERALL THICKNESS.			



PCB : TYPE 3
ASPECT-RATIO, AXE Z : 6:1 to 8:1 LEVEL "B"

MINIMUM PARAMETERS
DEFAULT TRACKS : 0.1mm GAPS : 0.1mm

Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	0,015mm	3.5	
1	Top Layer	CF-004	0,042mm		
	Dielectric 1	PP-IT-180A	0,106mm	4.2	
2	Layer 1	CF-003	0,035mm		
	Dielectric 2	FR4	1,248mm	4.2	
3	Layer 2	CF-003	0,035mm		
	Dielectric 3	PP-IT-180A	0,106mm	4.2	
4	Bottom Layer	CF-004	0,042mm		
	Bottom Solder	Solder Resist	0,015mm	3.5	
	Bottom Overlay				

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad	Pad Shape	Template	Description	Hole Tolerance (+)	Hole Tolerance (-)	Hole Length	Routed Path Length
▽	2	0.65mm (25.59mil)	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c0hn65m10				-	-
⊠	2	0.65mm (25.59mil)	NPTH	Slot	Top Layer - Bottom Layer	Pad	Rounded	c0hn65_95m10				0.95mm (37.40mil)	0.30mm (11.81mil)
⊠	2	0.97mm (38.19mil)	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c0hn97m107				-	-
◎	2	1.19mm (46.85mil)	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c0hn119m129				-	-
○	3	1.40mm (55.12mil)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)				-	-
▣	3	3.20mm (125.98mil)	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c320hn320				-	-
⌖	8	0.50mm (19.69mil)	PTH	Slot	Top Layer - Bottom Layer	Pad	Rounded	r140_80h50_110r40m150_90				1.10mm (43.31mil)	0.60mm (23.62mil)
□	32	1.10mm (43.31mil)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	(Mixed)				-	-
✡	100	1.00mm (39.37mil)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	(Mixed)				-	-
▽	531	0.30mm (11.81mil)	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	v55h30m0mx0				-	-
	685 Total												

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

Project: NUCLEO-64 STM32G5	
Layer: Drill Drawing	Gerber: .DRL
Variant: [No Variations]	MB2067
Date: 09-JAN-2026	Rev: B