

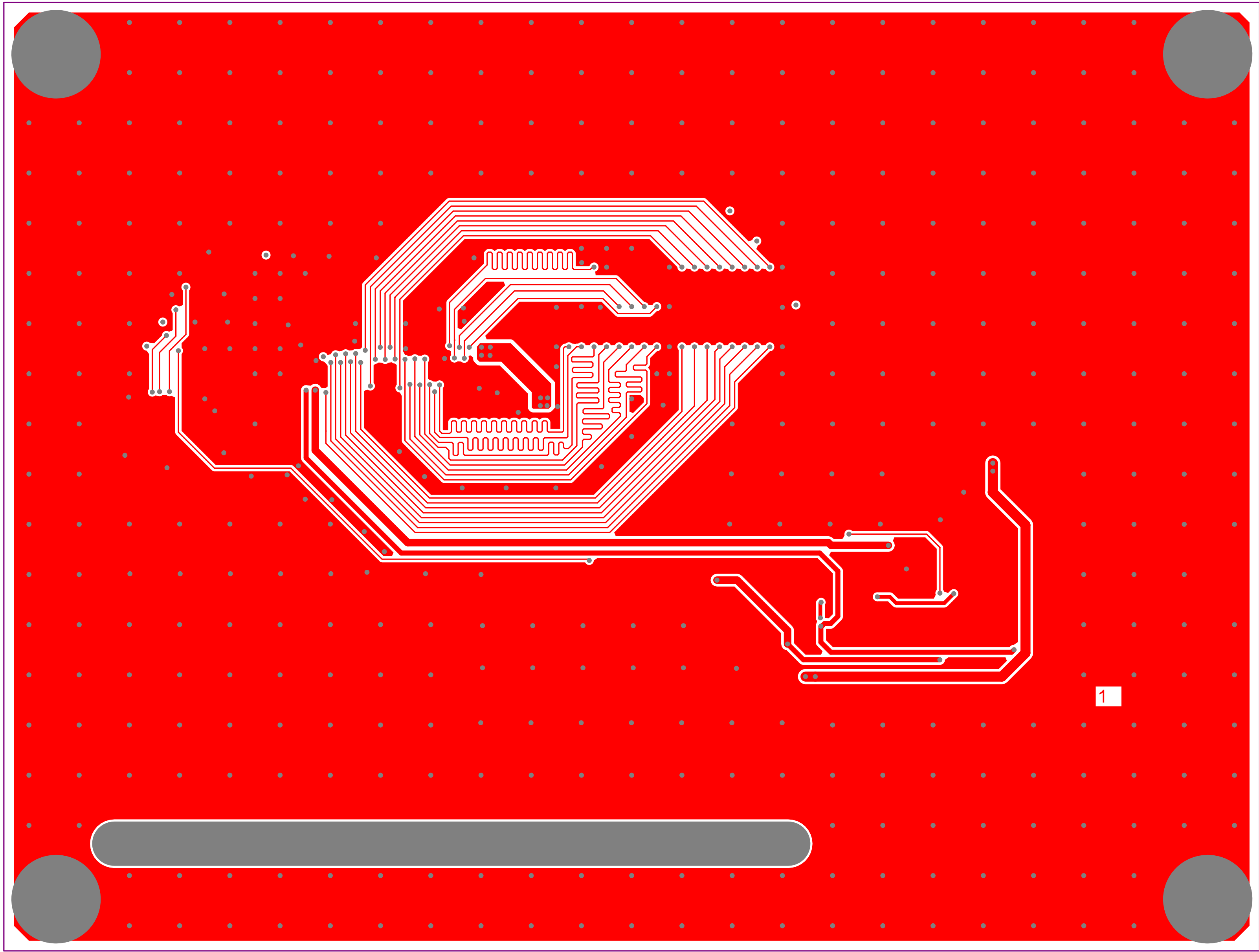



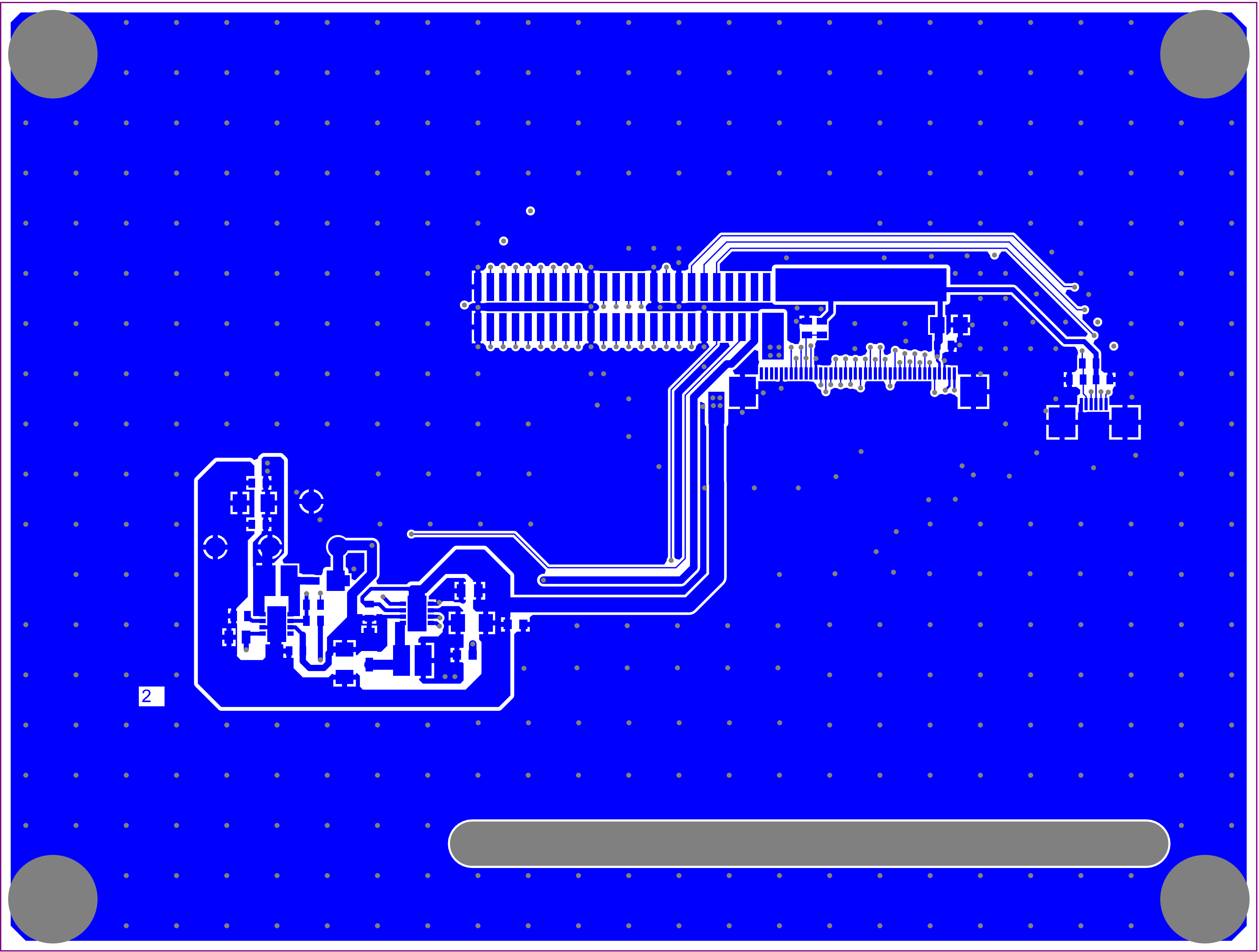
Project: 5 inch LCD daughter board		
Layer: Top Overlay	Gerber: .GTO	
Variant: [No Variations]	Ref: MB1860	
Date: 18-NOV-21	Rev: A	



Project: 5 inch LCD daughter board		
Layer: Top Solder	Gerber: .GTS	
Variant: [No Variations]	Ref: MB1860	
Date: 18-NOV-21	Rev: A	



Project: 5 inch LCD daughter board		
Layer: Top Layer	Gerber: .GTL	
Variant: [No Variations]	Ref: MB1860	
Date: 18-NOV-21	Rev: A	



Project: 5 inch LCD daughter board

Layer: Bottom Layer

Gerber:.GBL

Variant: [No Variations]


Ref: MB1860

Date: 18-NOV-21

Rev: A

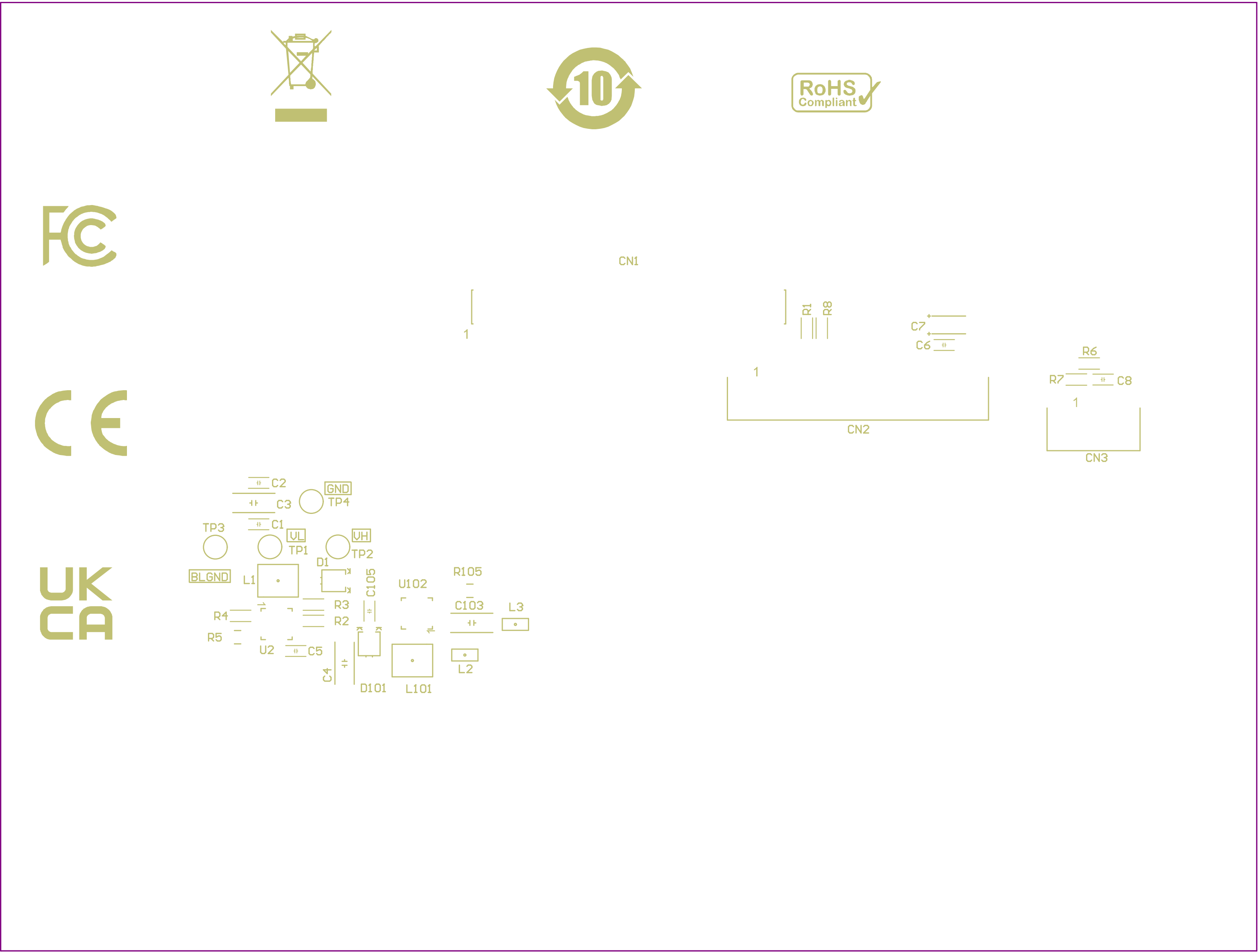


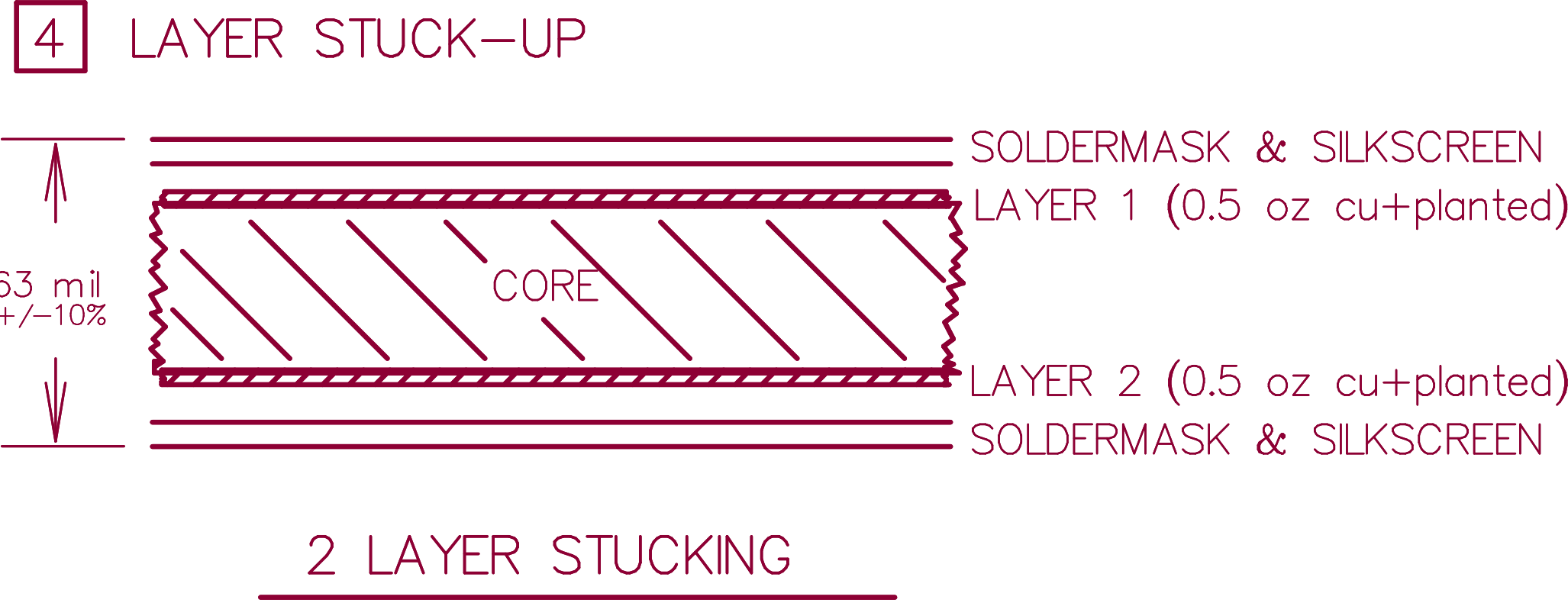
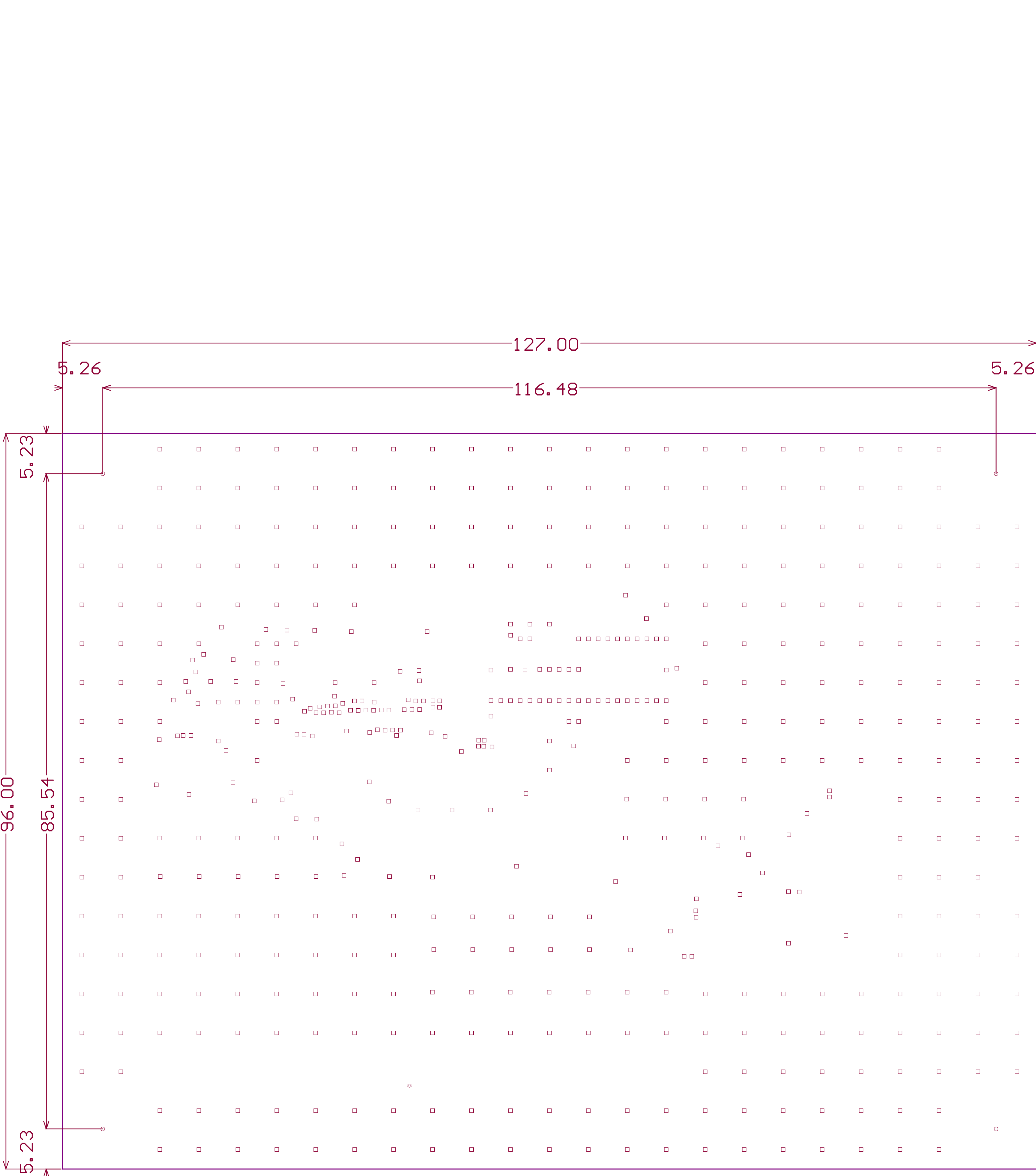


Project: 5 inch LCD daughter board		
Layer: Bottom Solder	Gerber:.GBS	
Variant: [No Variations]	Ref: MB1860	
Date: 18-NOV-21	Rev: A	

5 inch LCD daughter board

Project: 5 inch LCD daughter board		
Layer: Bottom Overlay	Gerber: .GBO	
Variant: [No Variations]	Ref: MB1860	
Date: 18-NOV-21	Rev: A	





PCB SPECIFICATIONS :

A. MATERIAL : FR-4 ☐ TG-170 ☒ TG-150 ☐ TG-140

B. MATERIAL FAMILY : N/A

C. SOLDERMASK COLOR : ☐ GREEN ☐ WHITE ☐ RED ☐ BLACK ☒ Blue ink PANTONE 2955

D. SILKSCREEN COLOR : ☒ WHITE ☐ YELLOW ☐ BLACK ☐ Blue ink PANTONE 2955

E. SURFACE FINISH : ☒ ENIG ☐ IMMERSION SILVER ☐ IMMERSION TIN

☐ HASL ☐ HASL (PB-FREE) ☐ GOLDEN FINGER

F. IMPEDANCE CONTROL : ☒ NO ☐ YES (SEE IMPEDANCE TABLE FOR DETAIL INFORMATION)

G. THROUGH VIA : PLUG THE VIAS WHICH ARE COVERED WITH SOLDERMASK ONE OR TWO SIDE.
PLUG MATERIAL : ☒ SOLDERMASK ☐ NON-CONDUCTIVE EPOXY.

H. STACK-UP : SEE LAYER STACK-UP SEQUENCE FOR OVERALL THICKNESS.

PCB : TYPE 3


ASPECT-RATIO, AXE Z :
6:1 to 8:1
LEVEL "B"

MINIMUM PARAMETERS

DEFAULT
TRACKS : 0.127mm
GAPS : 0.1274mm

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad	Pad Shape	Template	Description	Hole Tolerance (+)	Hole Tolerance (-)
☆	1	177.17mil (4.500mm)	NPTH	Slot	Top Layer - Bottom Layer	Pad	Rounded	r7264_450hn450_7264r100			
○	4	177.17mil (4.500mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c900h450m905			
□	518	10.00mil (0.254mm)	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	v51h25m0mx0(Tol8-8)		3.00mil (0.076mm)	3.00mil (0.076mm)
	523 Total										

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

Project: 5 inch LCD daughter board		
Layer: Drill Drawing	Gerber: .DRL	
Variant: [No Variations]	Ref: MB1860	
Date: 18-NOV-21	Rev: A	