
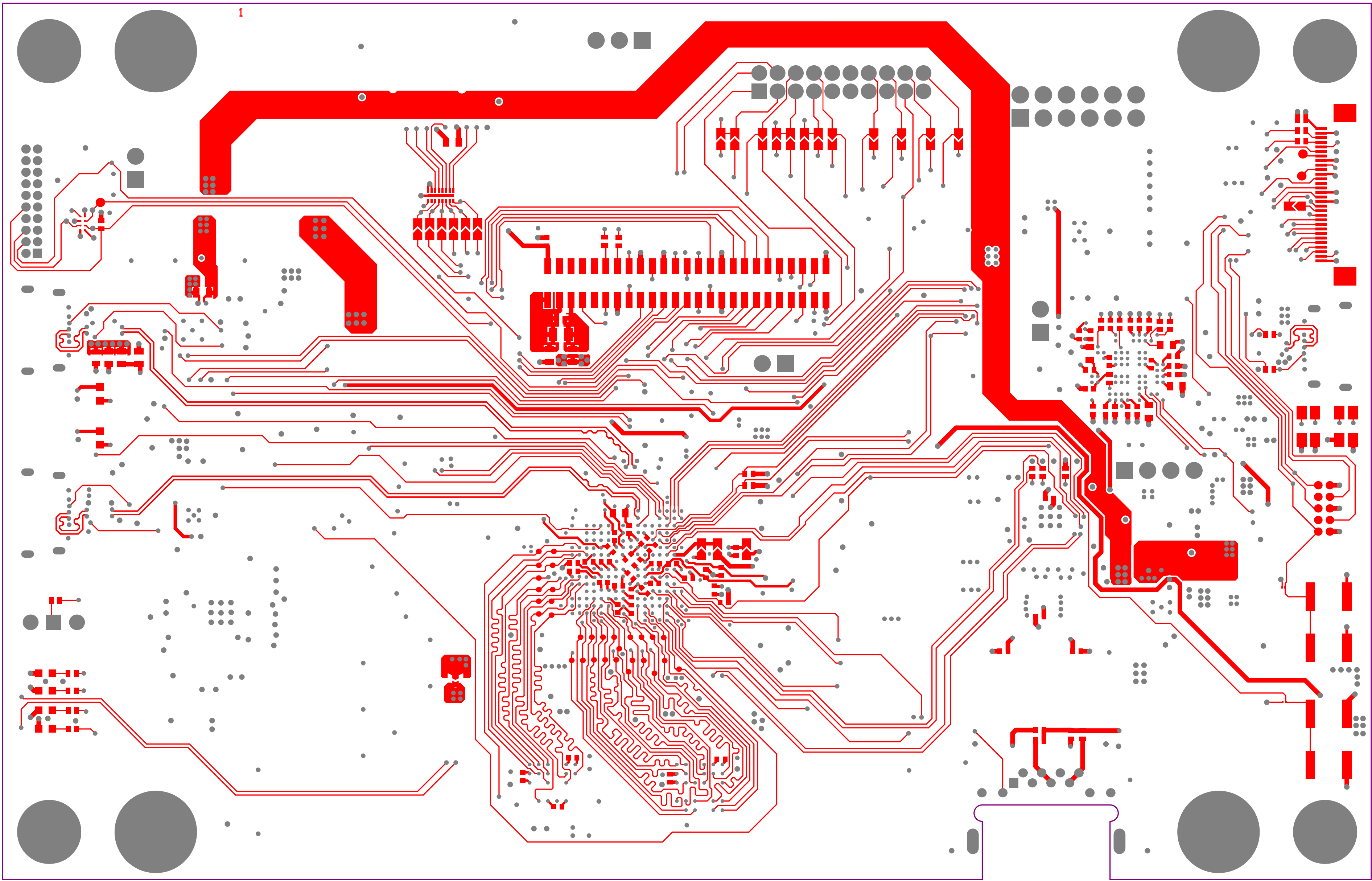

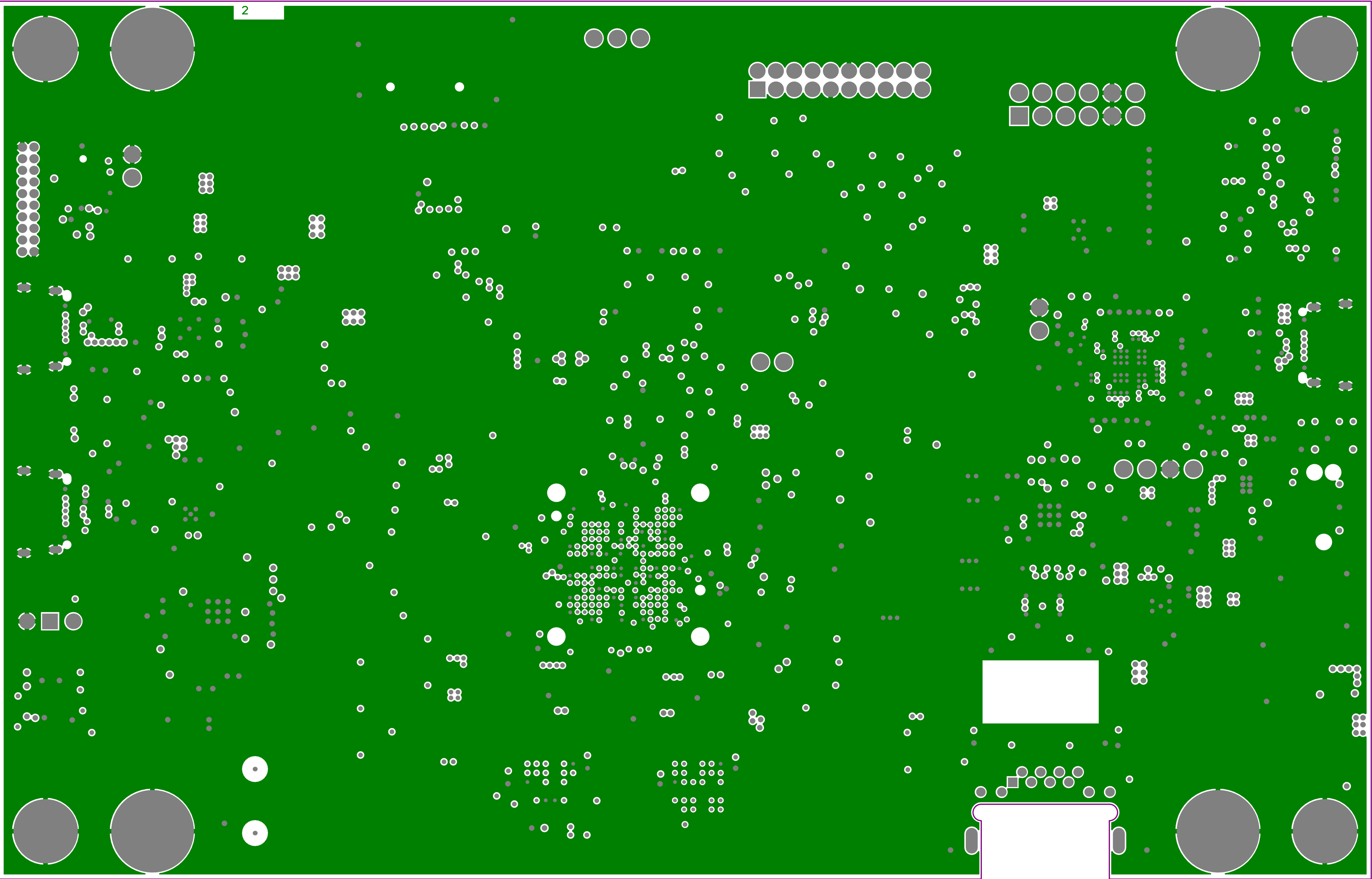



Project: STM32H7S78-DK		
Layer: <span>Top Solder</span>	Gerber: <span>.GTS</span>	
Variant: [No Variations]	Ref: MB1736	
Date: 18-OCT-23	Rev: D	

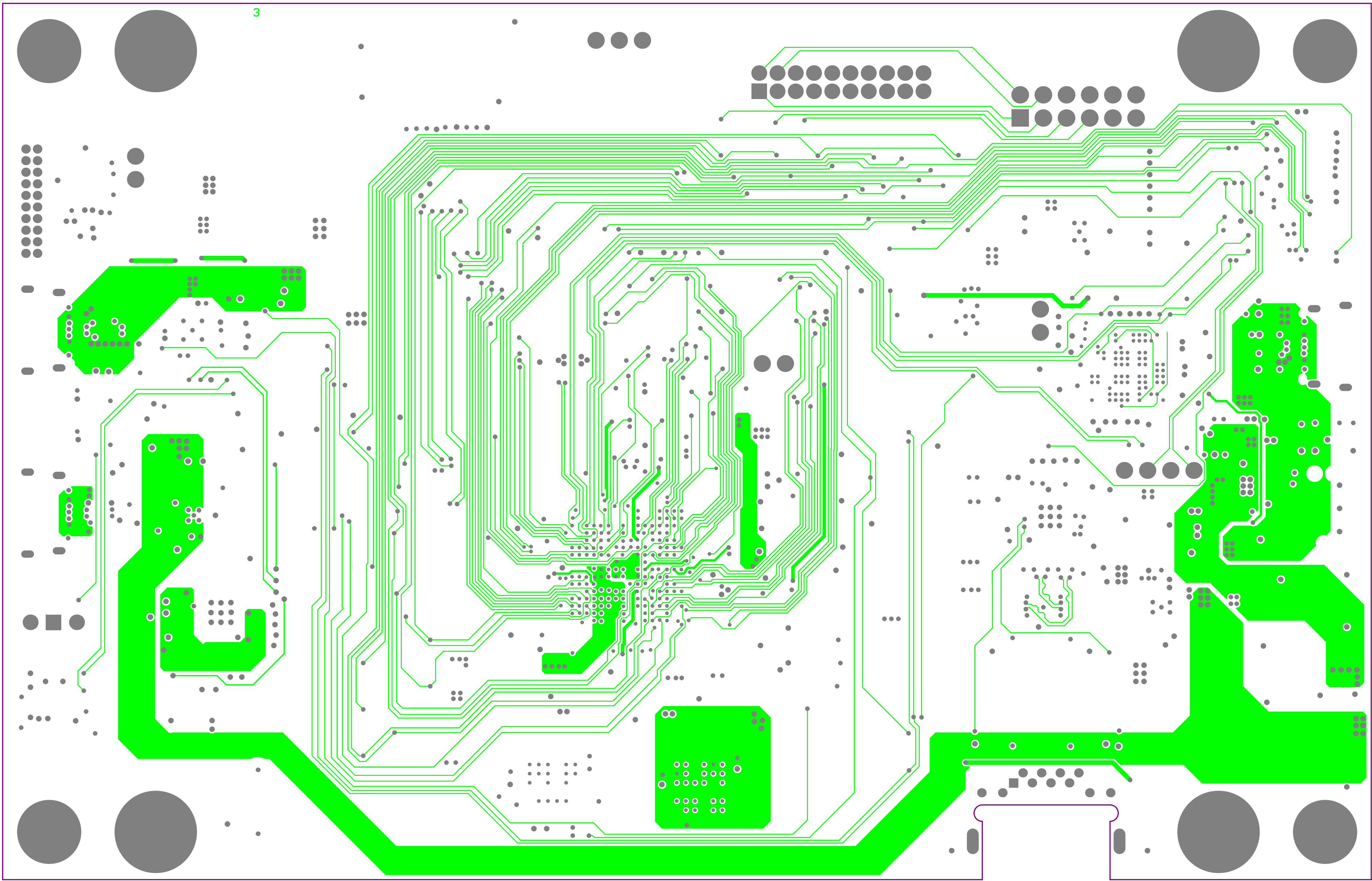



Project: STM32H7S78-DK		
Layer: <b>Top Layer</b>	Gerber: <b>.GTL</b>	
Variant: [No Variations]	Ref: MB1736	
Date: 18-OCT-23	Rev: D	

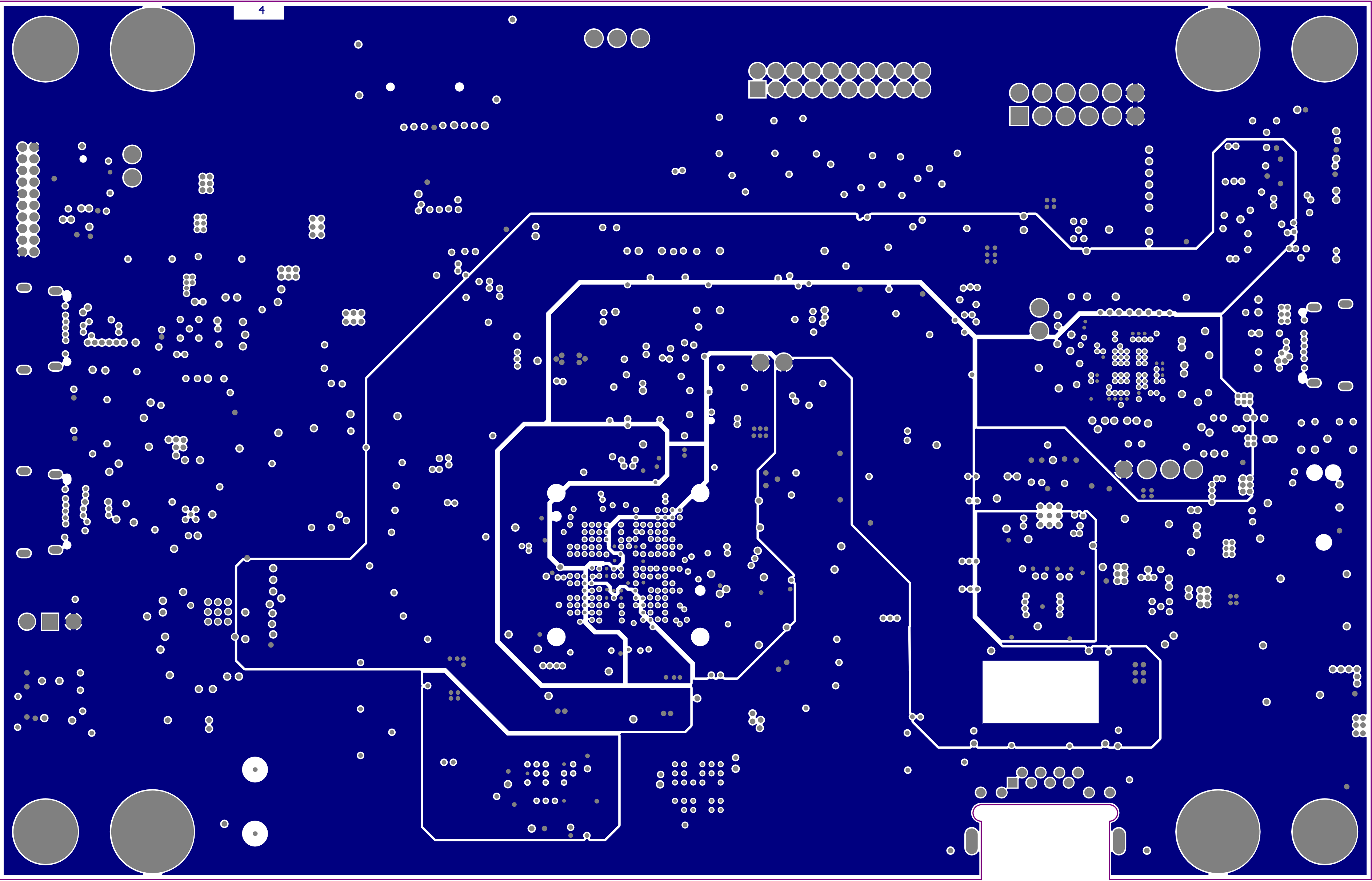



Project: STM32H7S78-DK		
Layer: L2_GND	Gerber: .G1	
Variant: [No Variations]	Ref: MB1736	
Date: 18-OCT-23	Rev: D	

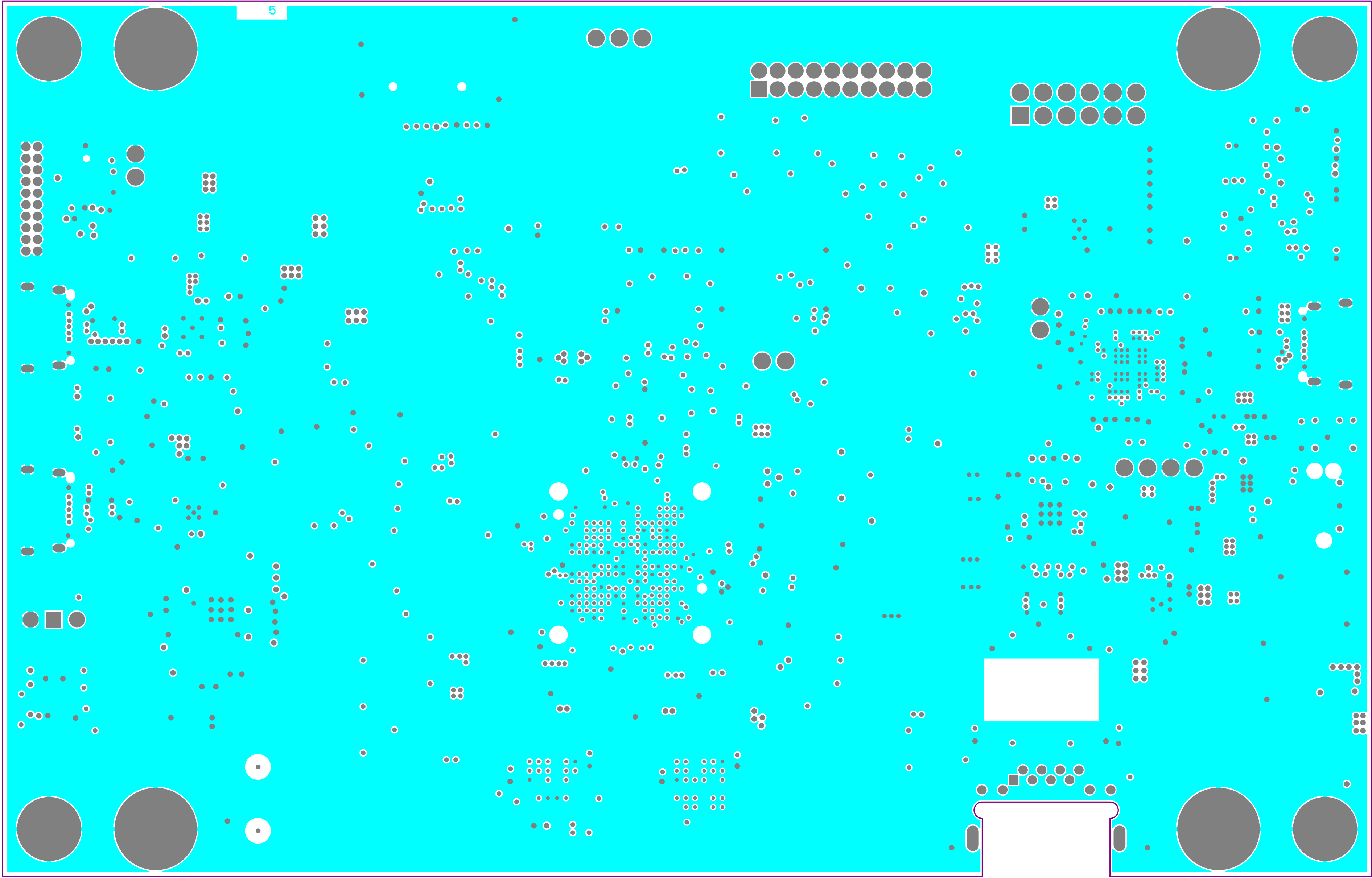





Project: STM32H7S78-DK		
Layer: L3_SIG	Gerber: .G2	
Variant: [No Variations]	Ref: MB1736	
Date: 18-OCT-23	Rev: D	

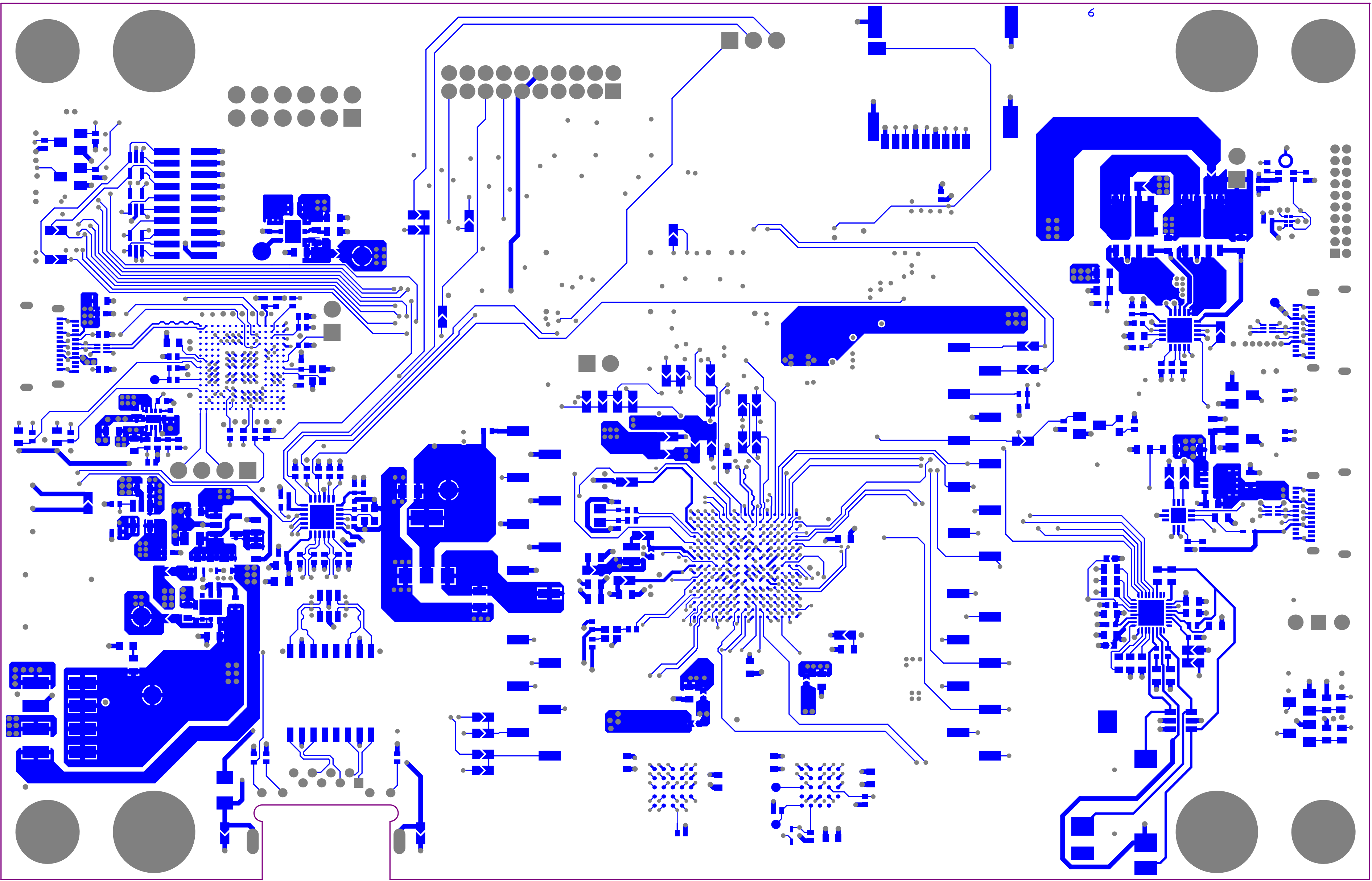


Project: STM32H7S78-DK		
Layer: L4_VCC	Gerber: .G3	
Variant: [No Variations]	Ref: MB1736	
Date: 18-OCT-23	Rev: D	



Project: STM32H7S78-DK		
Layer: L5_GND	Gerber: .G4	
Variant: [No Variations]	Ref: MB1736	
Date: 18-OCT-23	Rev: D	





Project: STM32H7S78-DK

Layer: [Bottom Layer](#)

Gerber: [.GBL](#)

Variant: [No Variations]

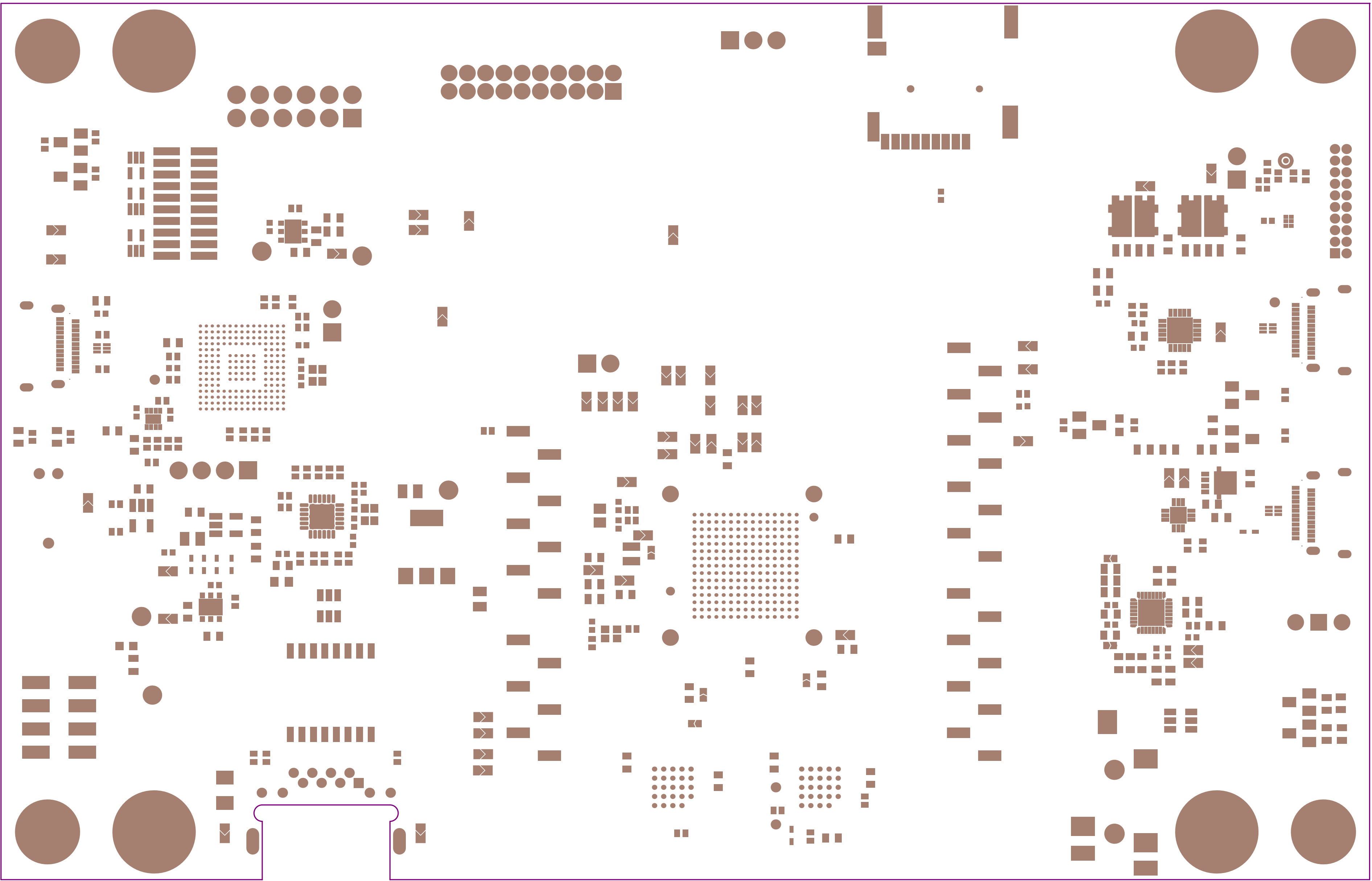
Ref: MB1736

Date: 18-OCT-23

Rev: D







Project: STM32H7S78-DK

Layer: Bottom Solder

Gerber:.GBS

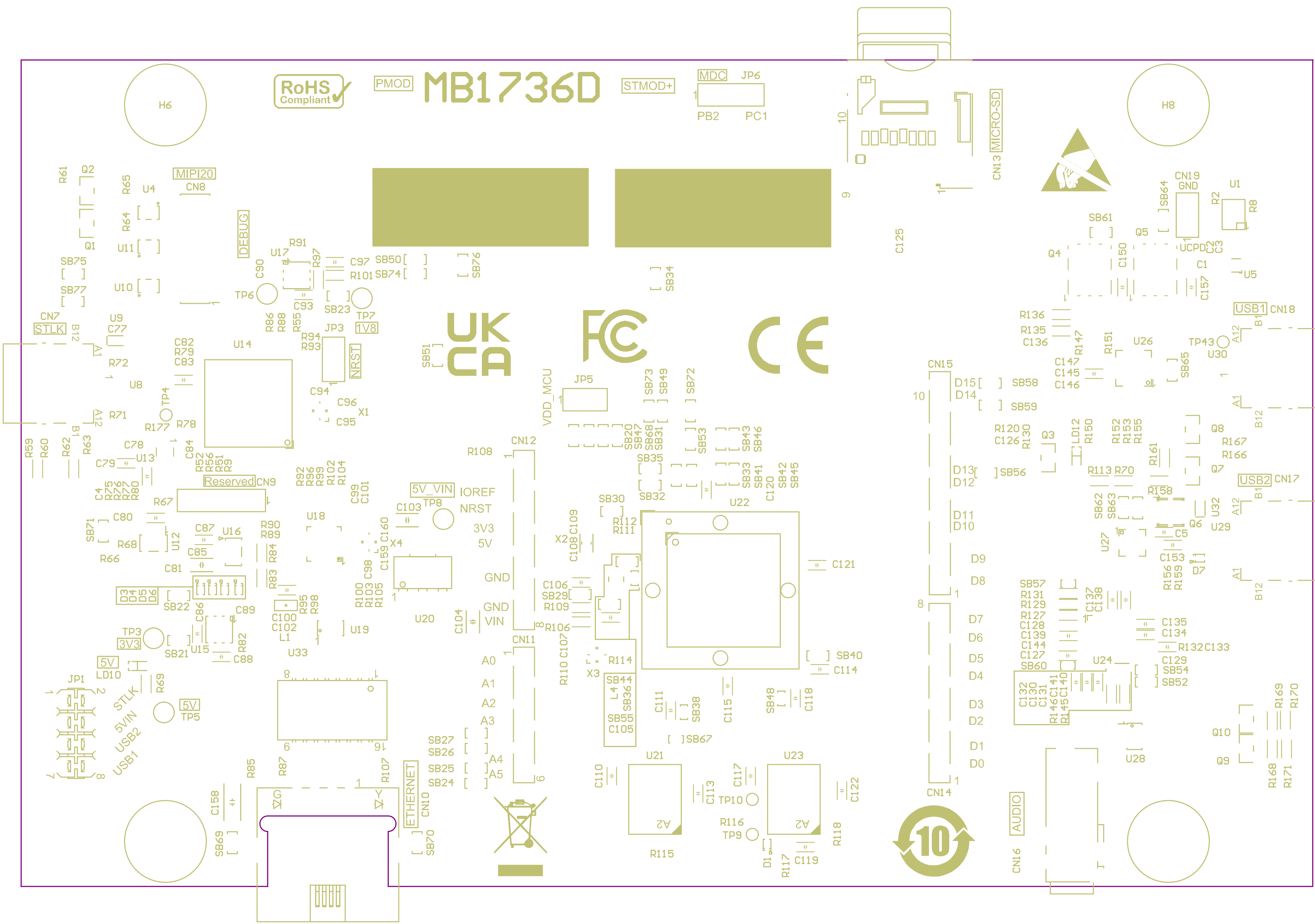
Variant: [No Variations]

Ref: MB1736

Date: 18-OCT-23

Rev: D





Project: STM32H7S78-DK

Layer: Bottom Overlay

Gerber:.GBO

Variant: [No Variations]

Ref: MB1736

Date: 18-OCT-23

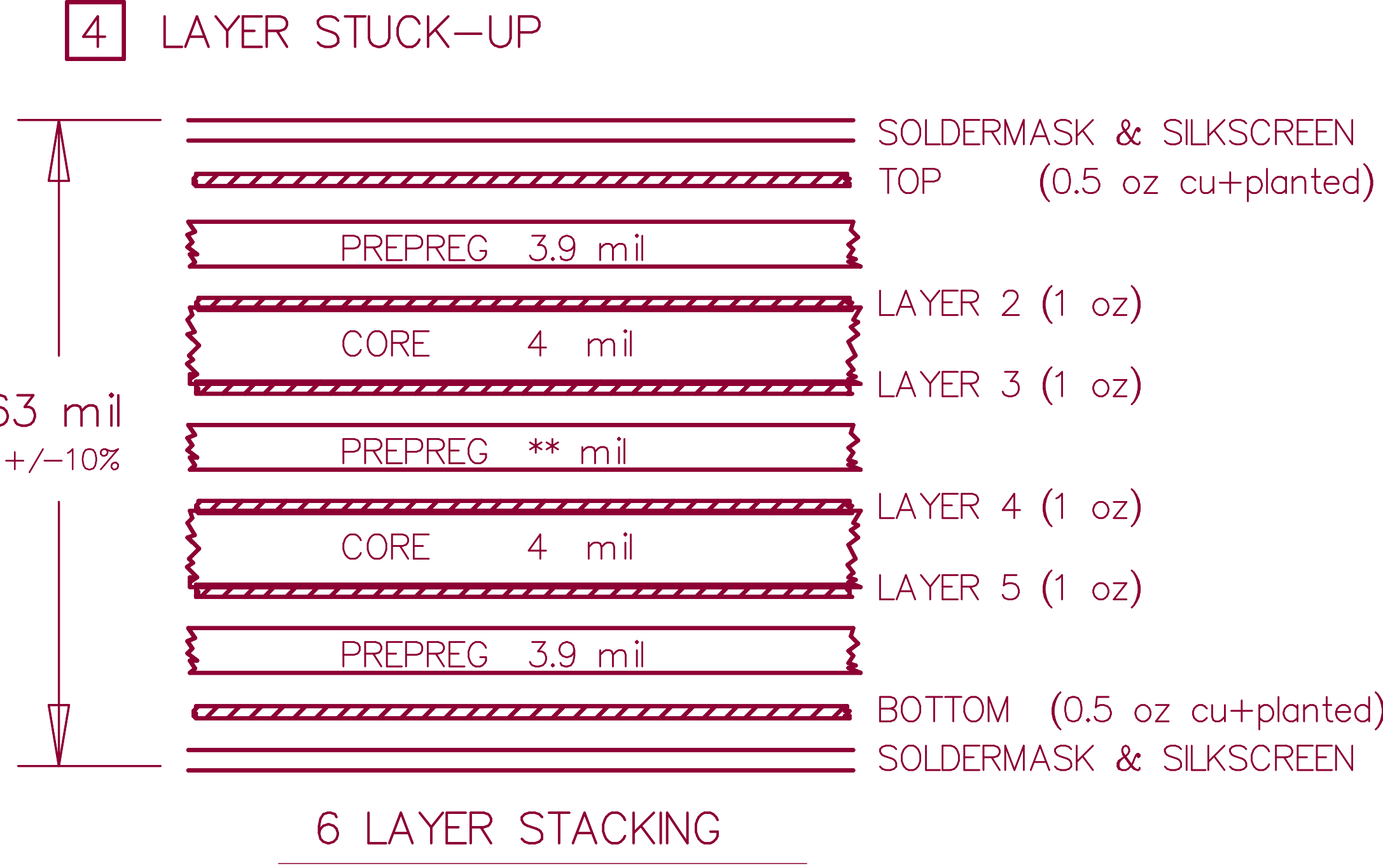
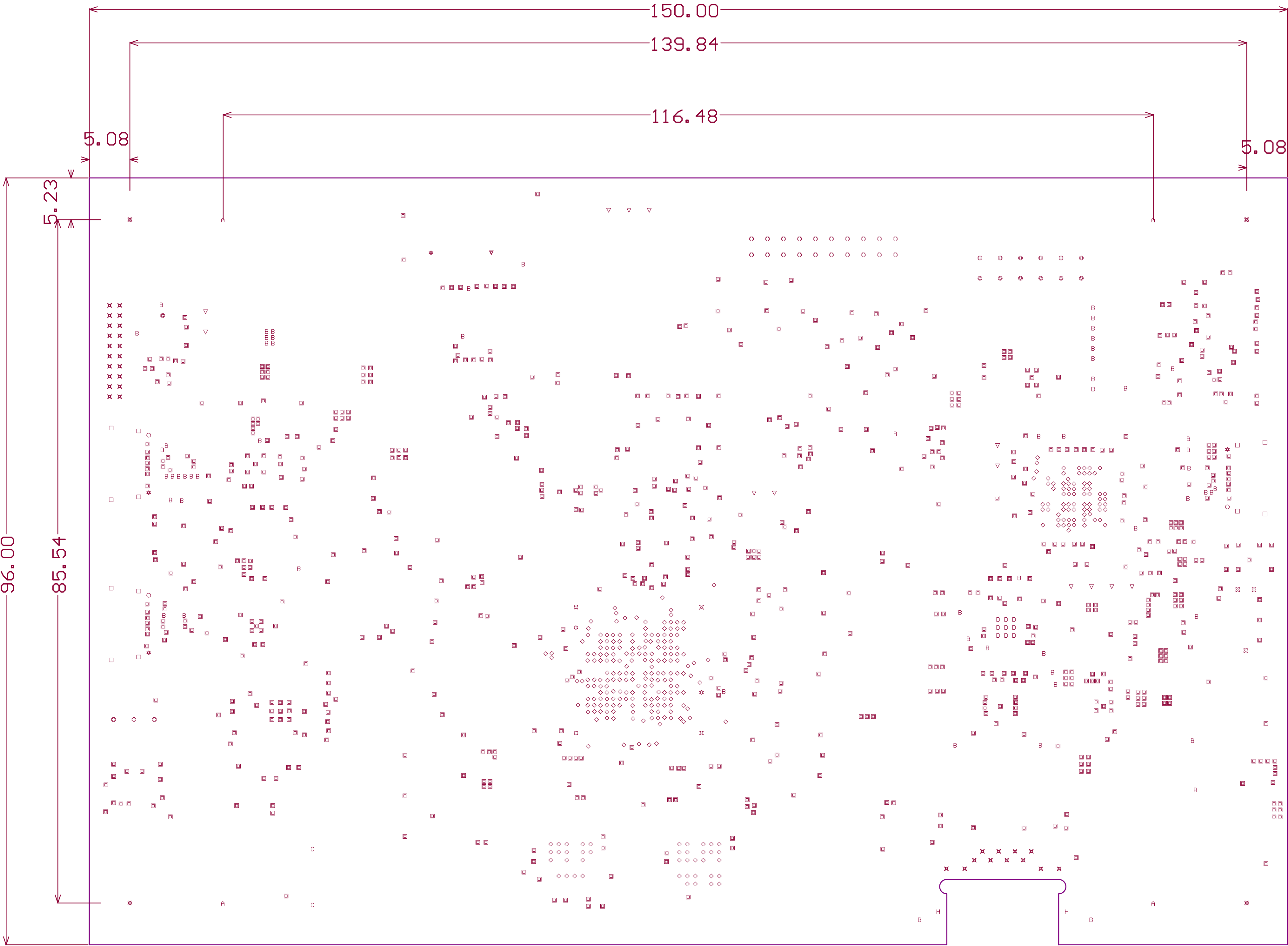
Rev: D





5 IMPEDANCE TABLE

LAYER	TRACE (mil)	SPACING (mil)	IMPEDANCE (Single end)	IMPEDANCE (Differential)	TOLERANCE
1,6 3	5.0 4.0	N/A	55 OHM	N/A	+/-10%
1,6 3	6.1 5.1	8.9 9.9	N/A	90 OHM	+/-10%
			N/A	90 OHM	+/-10%



PCB SPECIFICATIONS :

A. MATERIAL : FR-4 ☐ TG-170 ☒ TG-150 ☐ TG-140

B. MATERIAL FAMILY : N/A

C. SOLDERMASK COLOR : ☐ GREEN ☐ WHITE ☐ RED ☐ BLACK ☒ Blue ink PANTONE 2955

D. SILKSCREEN COLOR : ☒ WHITE ☐ YELLOW ☐ BLACK ☐ Blue ink PANTONE 2955

E. SURFACE FINISH : ☒ ENIG ☐ IMMERSION SILVER ☐ IMMERSION TIN

☐ HASL ☐ HASL (PB-FREE) ☐ GOLDEN FINGER

☒ IMPEDANCE CONTROL : ☒ NO ☐ YES (SEE IMPEDANCE TABLE FOR DETAIL INFORMATION)

G. THROUGH VIA : PLUG THE VIAS WHICH ARE COVERED WITH SOLDERMASK ONE OR TWO SIDE.  
PLUG MATERIAL : ☒ SOLDERMASK ☐ NON-CONDUCTIVE EPOXY.

☒ STACK-UP : SEE LAYER STACK-UP SEQUENCE FOR OVERALL THICKNESS.

PCB : TYPE 3

ASPECT-RATIO, AXE Z :

6:1 to 8:1


MINIMUM PARAMETERS

DEFAULT

TRACKS : 0.127mm

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad	Pad Shape	Template	Description	Hole Tolerance (+)	Hole Tolerance (-)
⊕	1	19.69mil <0.500mm	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c0hn50m55			
▼	1	27.56mil <0.700mm	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c0hn70m75			
H	2	27.56mil <0.700mm	PTH	Slot	Top Layer - Bottom Layer	Pad	Rounded	r 280_130h70_220r100m285_135			
☆	2	33.47mil <0.850mm	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c0hn85m90<To15-5>		1.97mil <0.050mm>	1.97mil <0.050mm>
C	2	78.74mil <2.000mm	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c50hn200m210			
O	3	25.59mil <0.650mm	NPTH	Slot	Top Layer - Bottom Layer	Pad	Rounded	<Mixed>			
⊗	3	43.31mil <1.100mm	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c0hn110m115			
★	4	25.59mil <0.650mm	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	<Mixed>			
✕	4	67.32mil <1.710mm	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c0hn171m176<To11-1>		0.39mil <0.010mm>	0.39mil <0.010mm>
⊗	4	137.79mil <3.500mm	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c700h350m705			
A	4	177.17mil <4.500mm	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c900h450m905			
D	9	11.81mil <0.300mm	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	v60h30			
□	12	19.69mil <0.500mm	PTH	Slot	Top Layer - Bottom Layer	Pad	Rounded	<Mixed>			
⊙	12	43.31mil <1.100mm	PTH	Round	Top Layer - Bottom Layer	Pad	<Mixed>	<Mixed>			
▼	13	39.37mil <1.000mm	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	<Mixed>			
⊕	23	35.43mil <0.900mm	PTH	Round	Top Layer - Bottom Layer	Pad	<Mixed>	<Mixed>			
✕	32	27.56mil <0.700mm	PTH	Round	Top Layer - Bottom Layer	Pad	<Mixed>	<Mixed>			
B	61	12.00mil <0.305mm	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	v61h30m0mx0			
◇	311	8.00mil <0.203mm	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	v41h20m0mx0			
■	819	10.00mil <0.254mm	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	<Mixed>			
	1322 Total										

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.  
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

Project: STM32H7S78-DK		
Layer: Drill Drawing	Gerber: .DRL	
Variant: [No Variations]	Ref: MB1736	
Date: 18-OCT-23	Rev: D	