

A

B

C

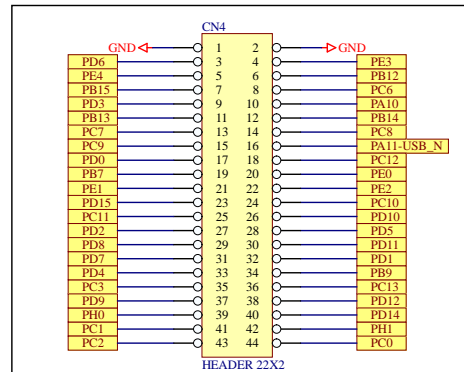
D

A

B

C

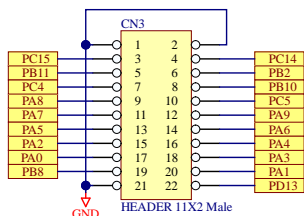
D



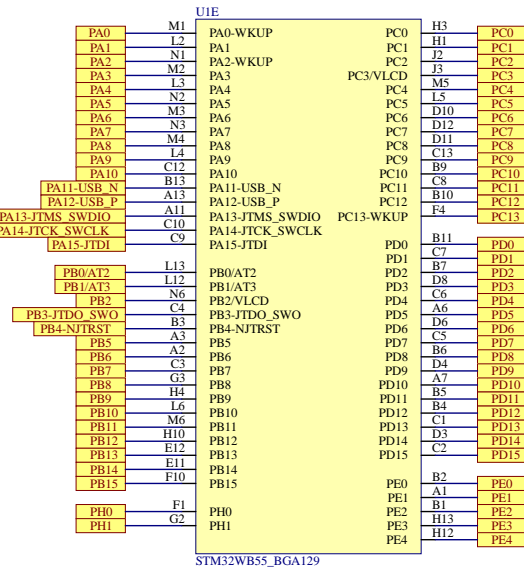
ADC testpoints:

ADC_CLOCK (RF_DTB0) -> PC14
 ADC_T[0] (RF_DTB9) -> PA7
 ADC_T[1] (RF_DTB8) -> PA6
 ADC_T[2] (RF_DTB1) -> PC15
 ADC_T[3] (RF_DTB2) -> PA0
 ADC_T[4] (RF_DTB3) -> PA1
 ADC_T[5] (RF_DTB4) -> PA2
 ADC_T[6] (RF_DTB6) -> PA3
 ADC_T[7] (RF_DTB6) -> PA4
 ADC_T[8] (RF_DTB7) -> PA5

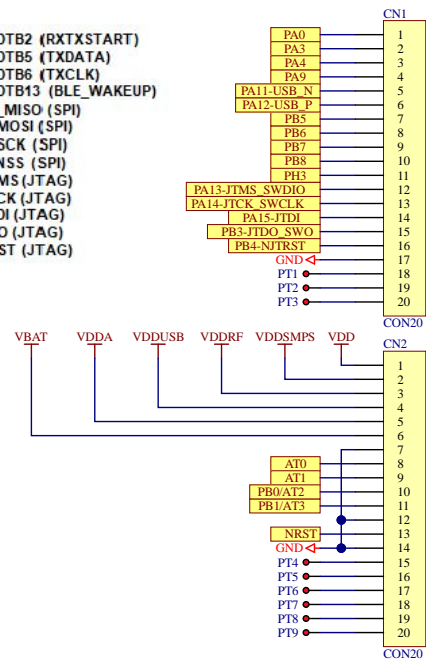
ADC_Q[4] (RF_DTB10) -> PB2
 ADC_Q[5] (RF_DTB11) -> PB7
 ADC_Q[6] (RF_DTB12) -> PA8
 ADC_Q[7] (RF_DTB13) -> PA9
 ADC_Q[8] (RF_DTB14) -> PA10
 ADC_Q[1] (RF_DTB15) -> PA11
 ADC_Q[1] (RF_DTB16) -> PB8
 ADC_Q[0] (RF_DTB17) -> PB11
 ADC_Q[0] (RF_DTB18) -> PB10



Connecteurs sur la carte fille (TOP LAYER)



PA0 = RF_DTB2 (RXTXSTART)
 PA3 = RF_DTB5 (TXDATA)
 PA4 = RF_DTB6 (TXCLK)
 PA9 = RF_DTB13 (BLE_WAKEUP)
 PA12 = RF_MISO (SPI)
 PB5 = RF_MOSI (SPI)
 PB6 = RF_SCK (SPI)
 PH3 = RF_NSS (SPI)
 PA13 = JTMS (JTAG)
 PA14 = JTCK (JTAG)
 PA15 = JTDI (JTAG)
 PB3 = JTDO (JTAG)
 PB4 = NJRST (JTAG)



Connecteurs vers la carte mère (BOTTOM LAYER)

