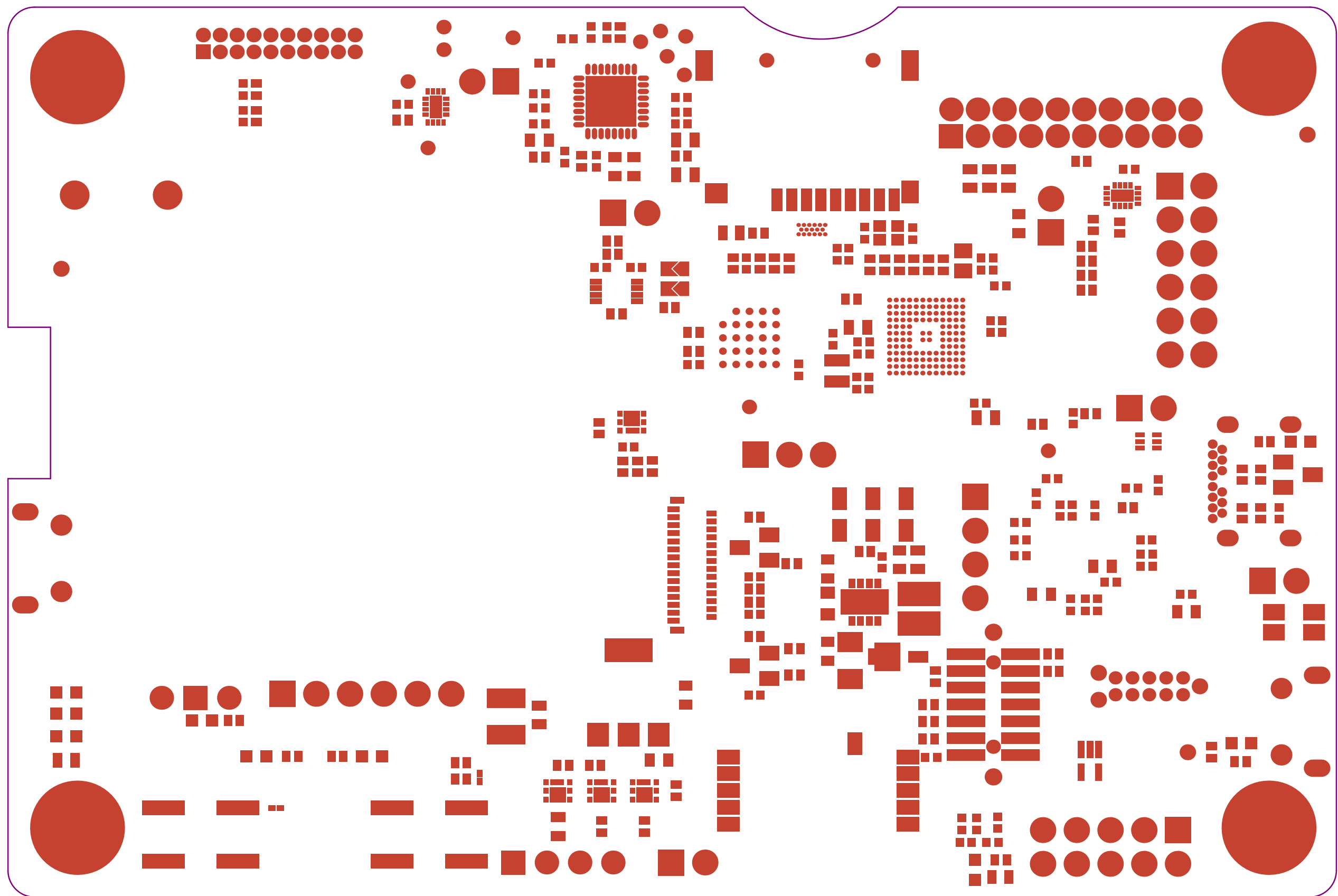


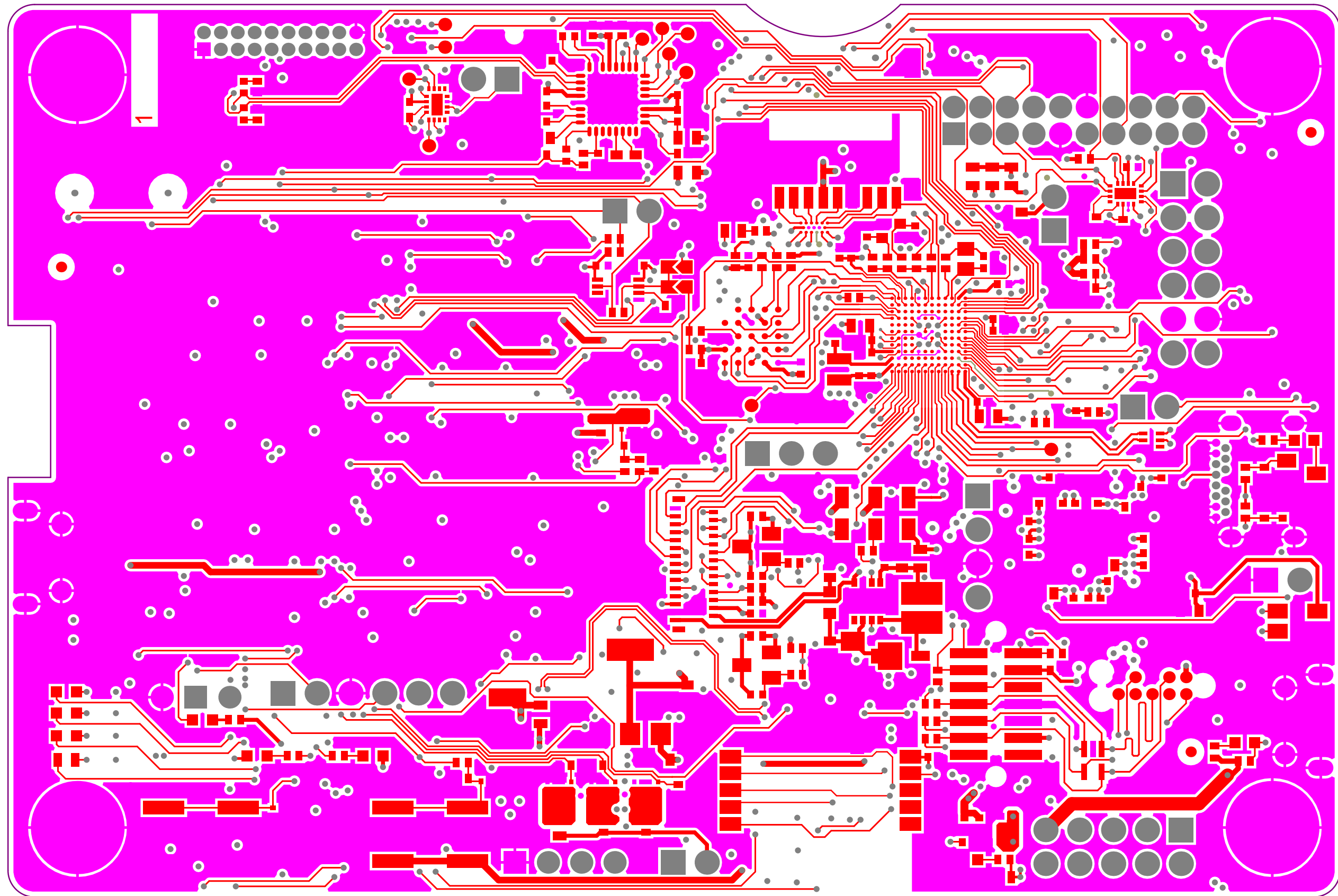
Top Overlay

.GTO



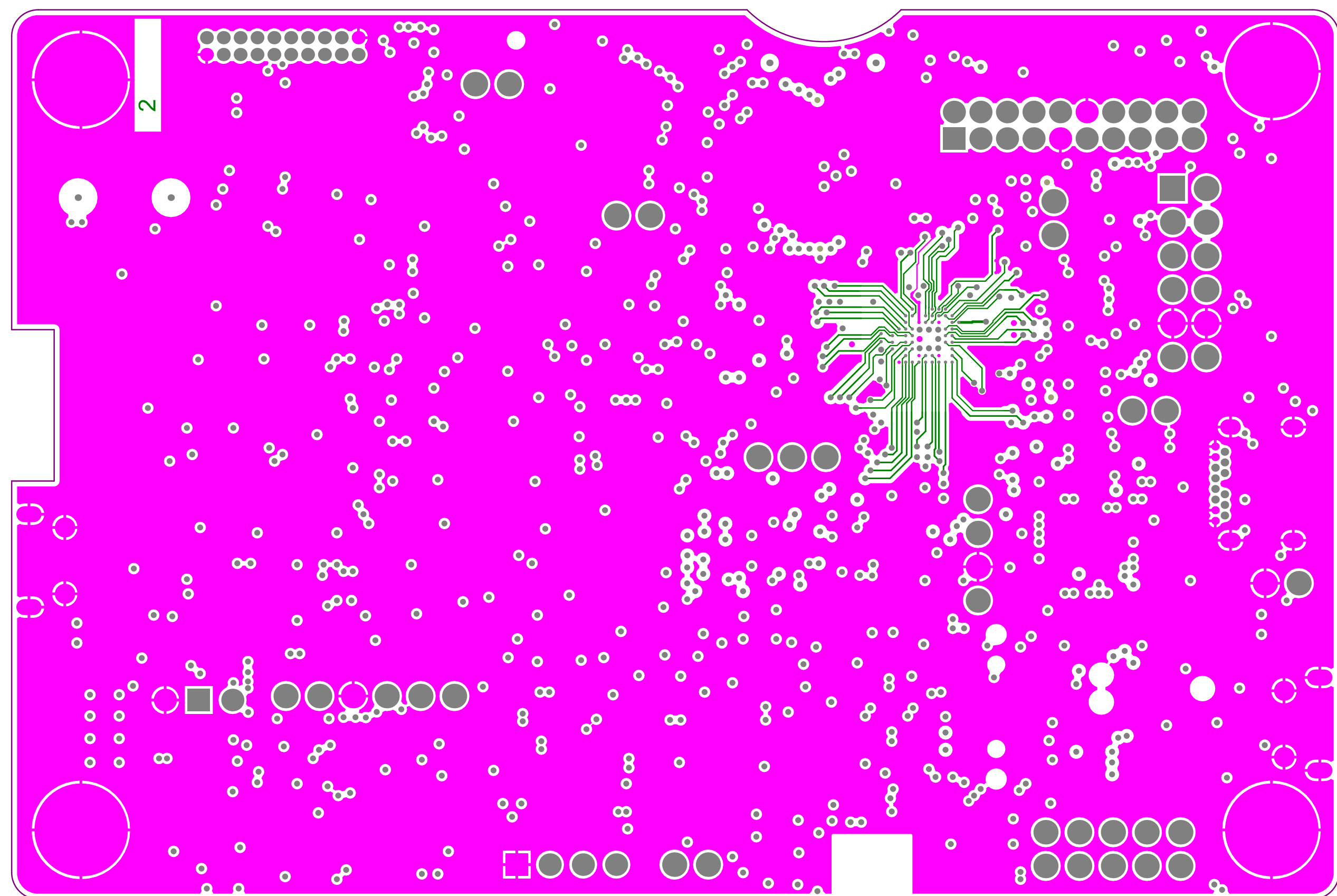
Top Solder

.GTS



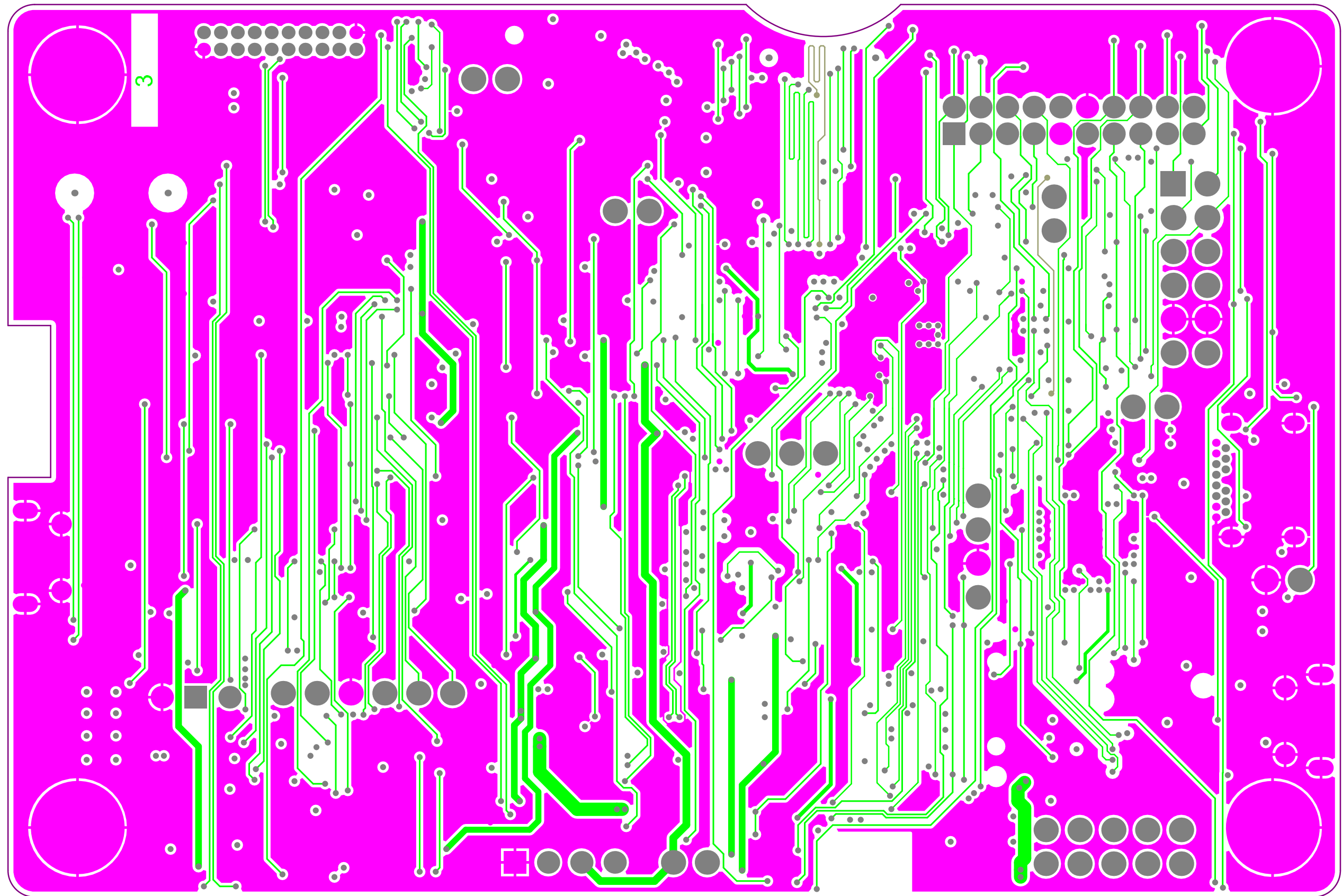
Top Layer

.GTL



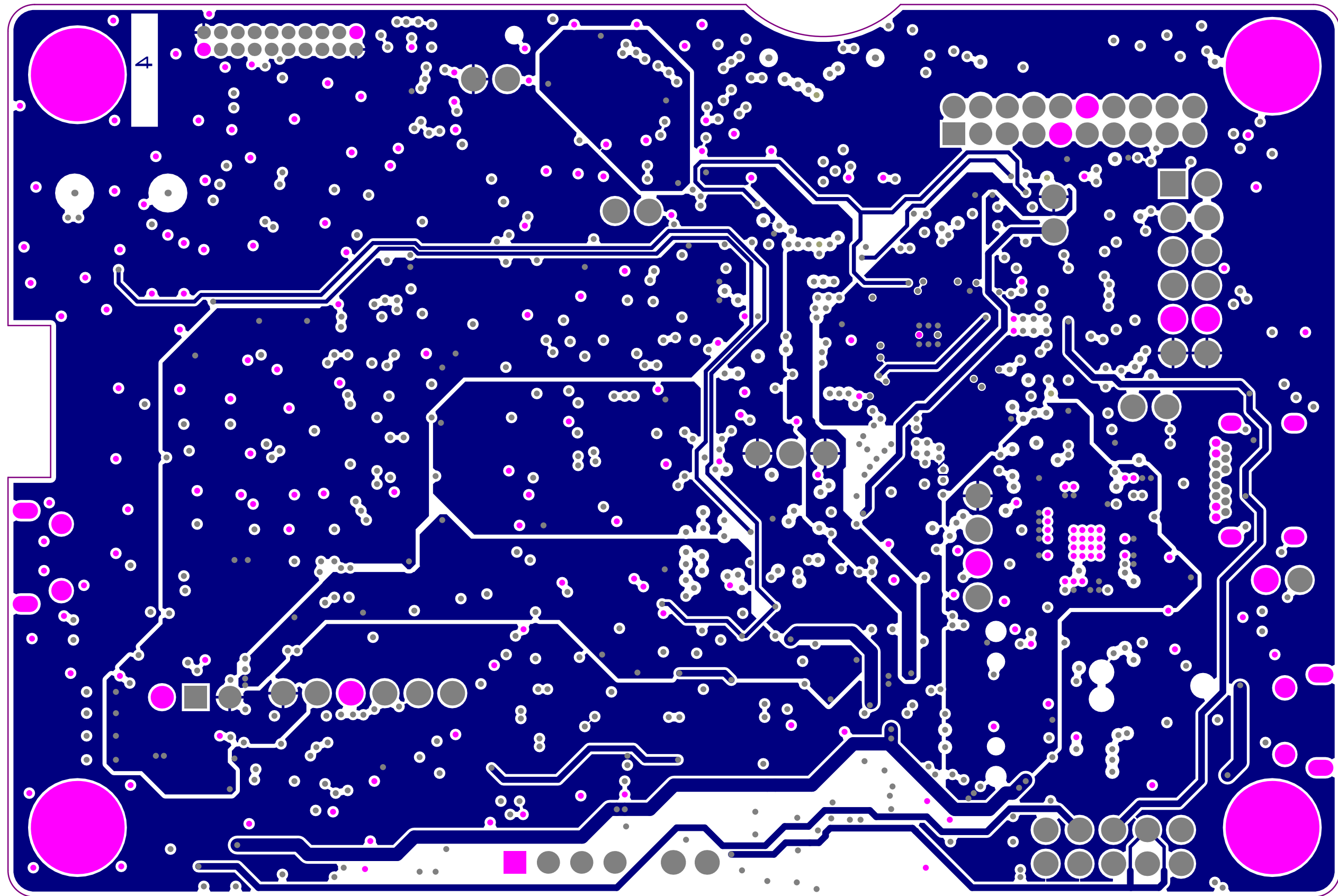
Signal Layer 1

.G1



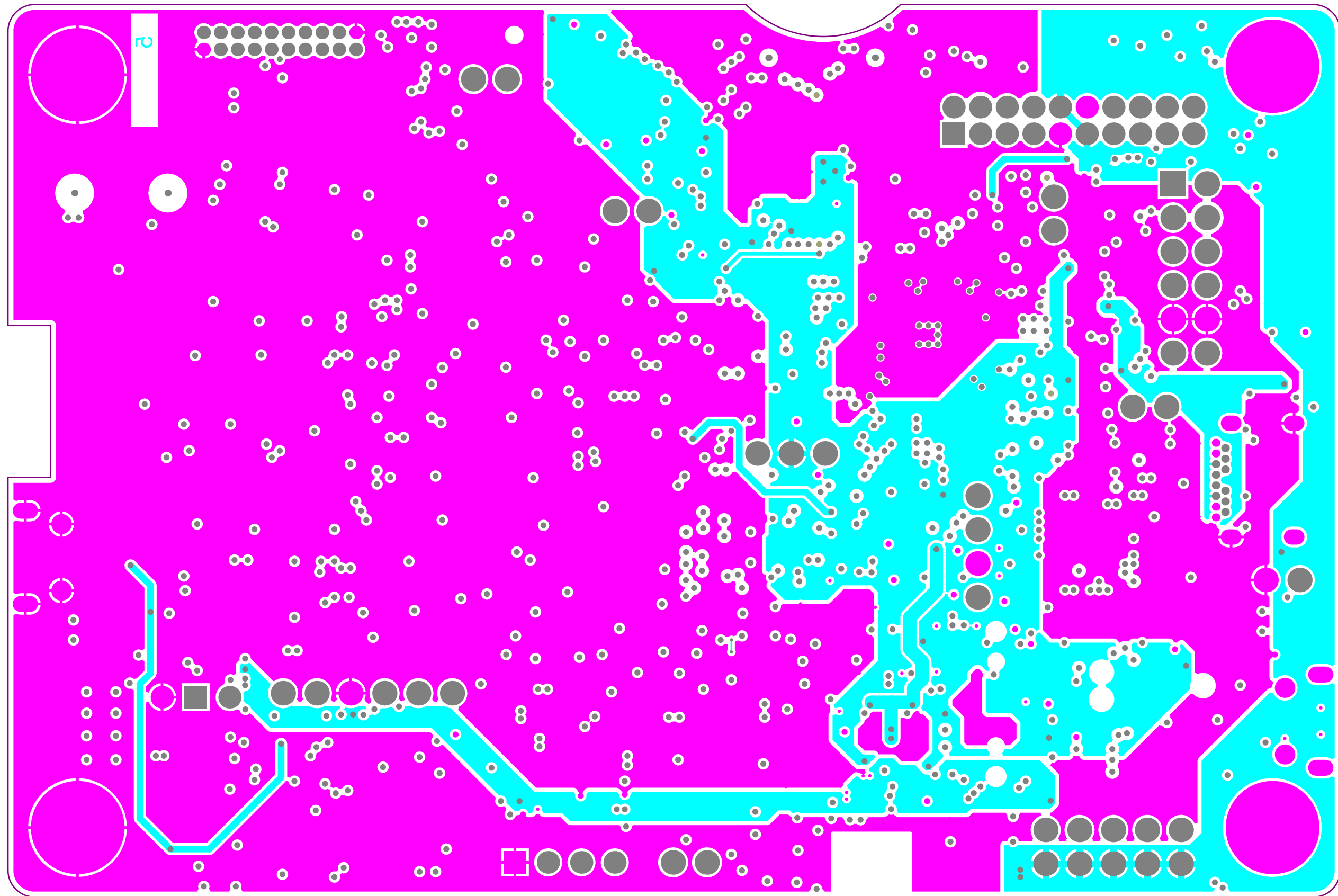
Signal Layer 2

.G2



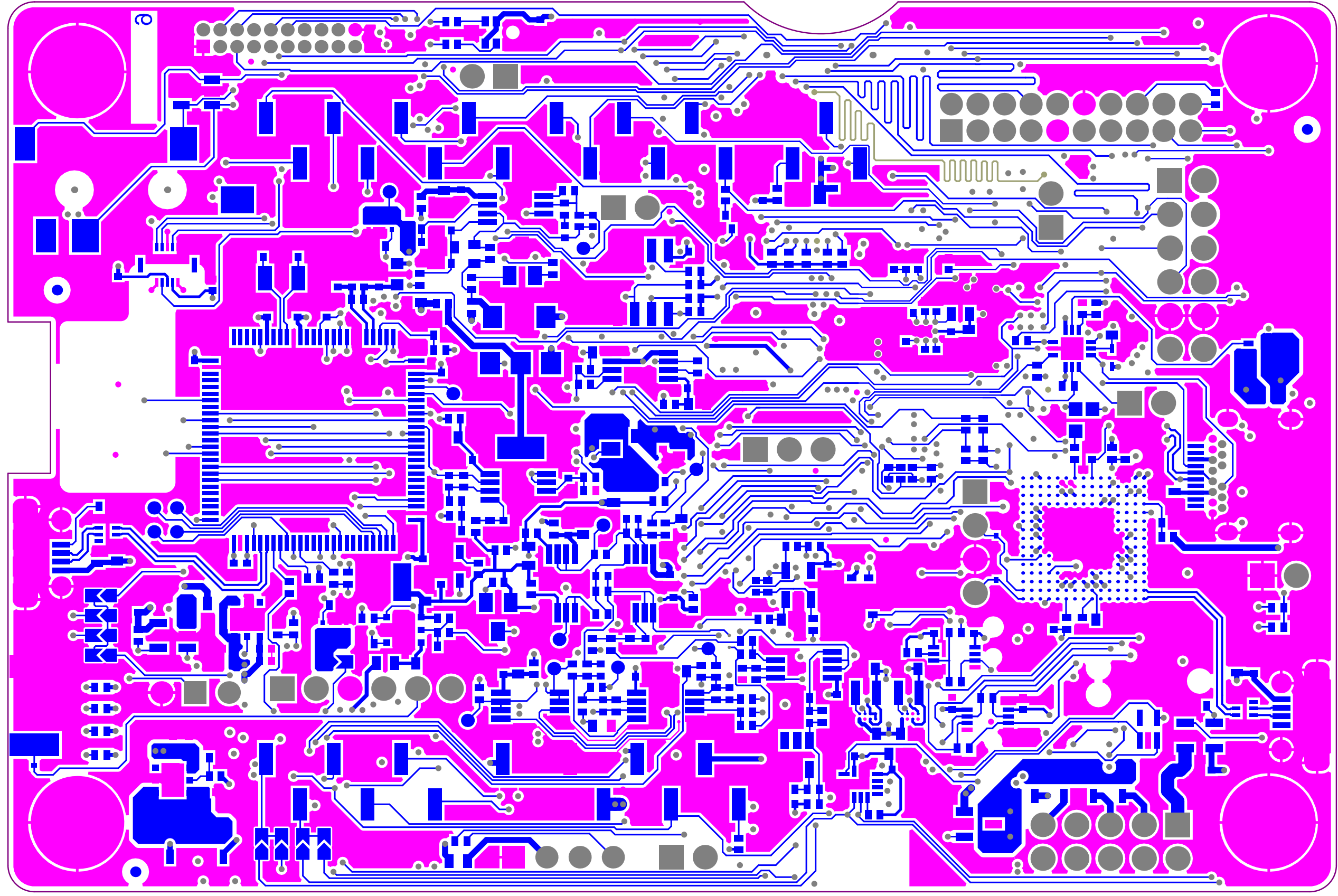
Signal Layer 3

.G3



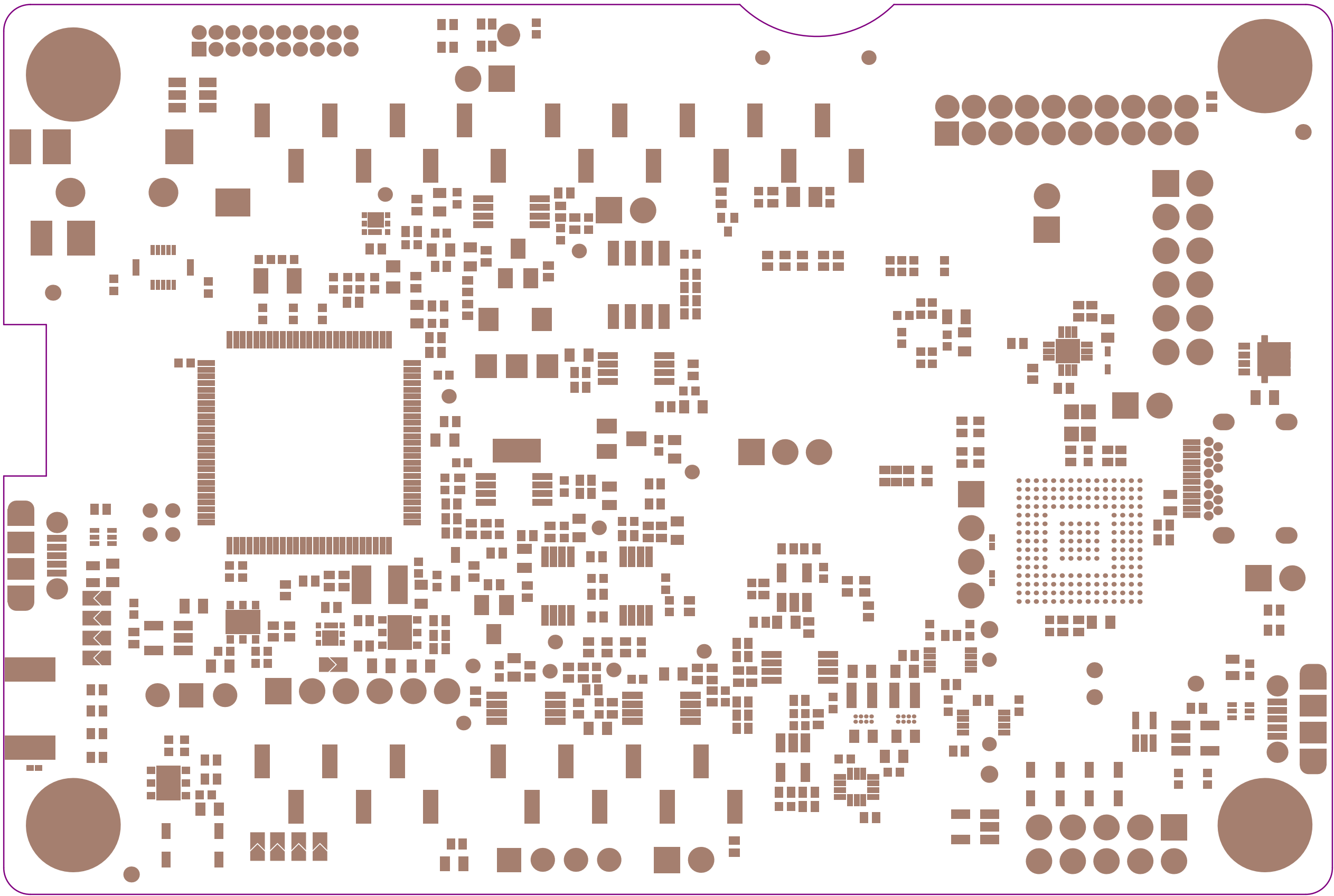
Signal Layer 4

.G4



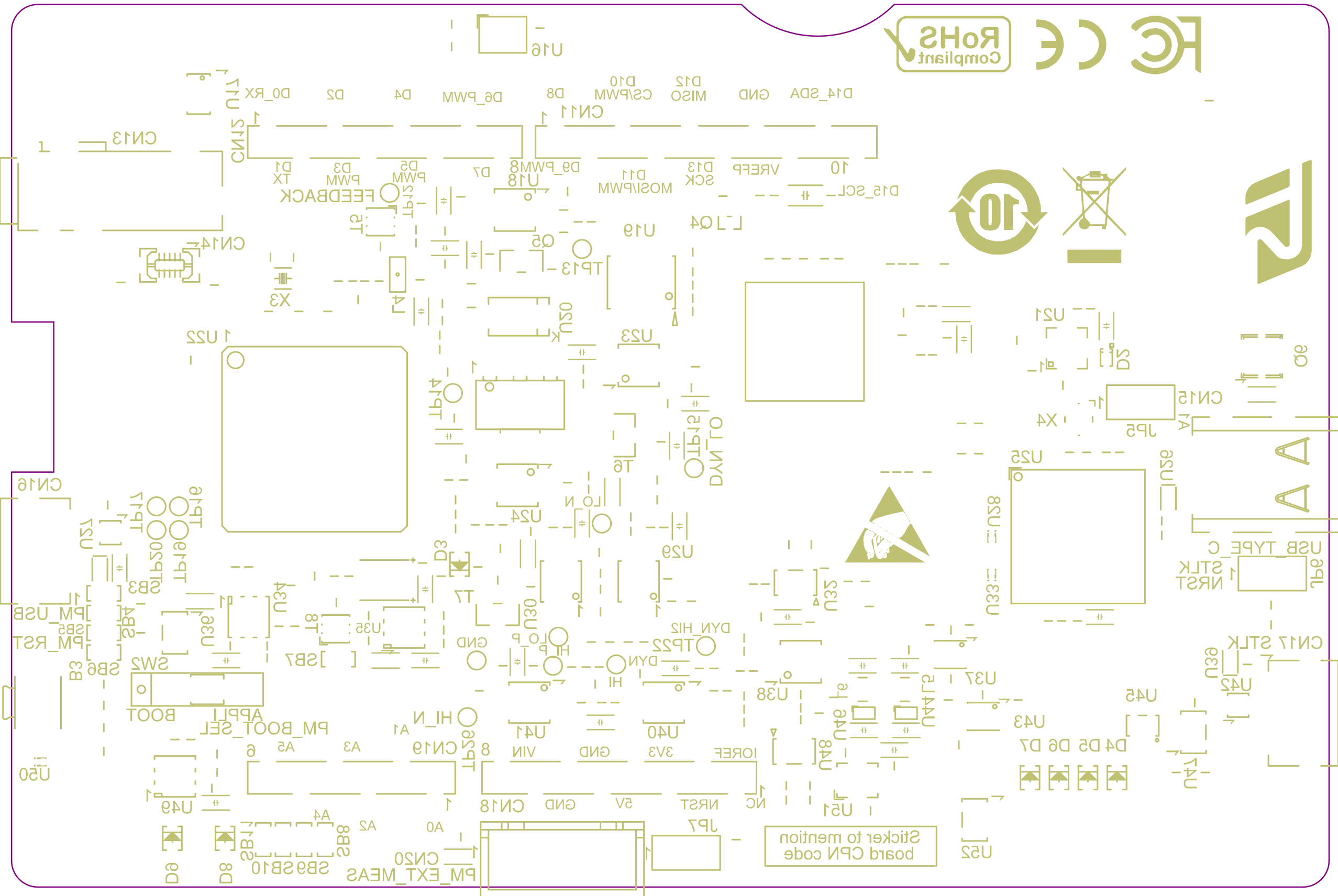
Bottom Layer

.GBL



.GB2

Bottom Solder



.GBO

Bottom Overlay

THE COMPONENTS WITH PLATED THROUGH HOLE (PTH) MAY BE WELDED (CABLED) IN PIN-IN-PASTE MODE (IF NECESSARY)

PCB SPECIFICATIONS :

A. MATERIAL :

B. MATERIAL FAMILY :

C. SOLDERMASK COLOR :

D. SILKSCREEN COLOR :

E. SURFACE FINISH :

F

IMPEDANCE CONTROL :

G. THROUGH VIA :

H

STACK-UP :

FR-4

N/A

☒BLUE

☐WHITE

☐RED

☐BLACK

☒WHITE

☐YELLOW

☐BLACK

☐Blue ink PANTONE 2955

☒ENIG

☐IMMERSION SILVER

☐IMMERSION TIN

☐HASL

☐HASL (PB-FREE)

☐GOLDEN FINGER

☐NO

☒YES (SEE IMPEDANCE TABLE FOR DETAIL INFORMATION)

PLUG THE VIAS WHICH ARE COVERED WITH SOLDERMASK ONE OR TWO SIDE.
PLUG MATERIAL : ☒SOLDERMASK ☐NON-CONDUCTIVE EPOXY.
SEE LAYER STACK-UP SEQUENCE FOR OVERALL THICKNESS.

PCB : TYPE 3

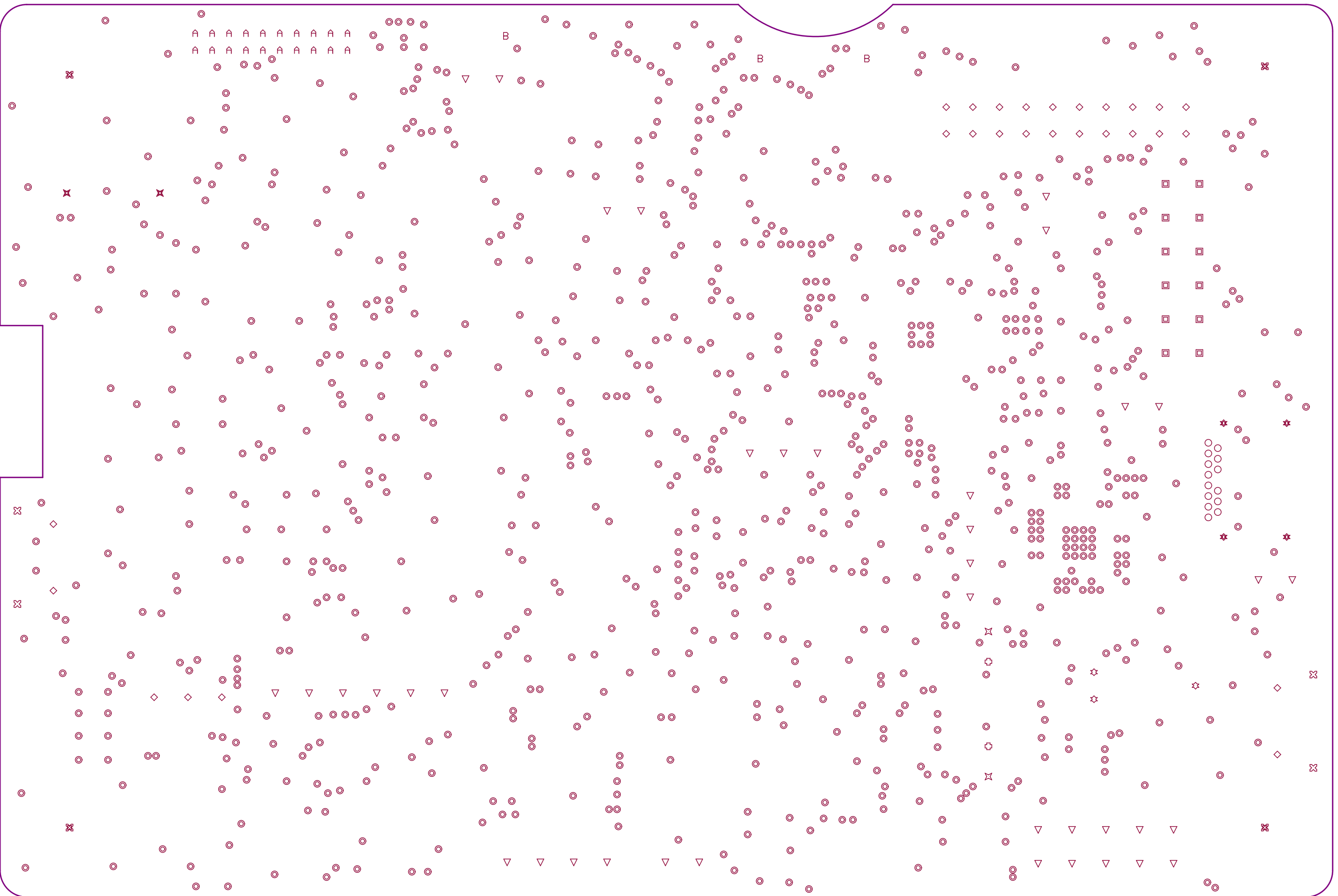
ASPECT-RATIO, AXE Z :
6:1 to 8:1
LEVEL "B"

MINIMUM PARAMETERS

DEFAULT
TRACKS : 0.12mm
GAPS : 0.1mm

MINIMUM PARAMETERS

MCU
TRACKS : 0.09mm
GAPS : 0.08mm



Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0,015mm	3,5	
3	Top Layer	Copper	0,042mm		
4	Dielectric 1	FR-4	0,099mm	4,2	
5	Signal Layer 1	Copper	0,035mm		
6	Dielectric 3		0,102mm	4,2	
7	Signal Layer 2	Copper	0,035mm		
8	Dielectric 4		0,946mm	4,2	
9	Signal Layer 3	Copper	0,035mm		
10	Dielectric 5		0,102mm	4,2	
11	Signal Layer 4	Copper	0,035mm		
12	Dielectric 2		0,099mm	4,2	
13	Bottom Layer	Copper	0,042mm		
14	Bottom Solder	Solder Resist	0,015mm	3,5	
15	Bottom Overlay				

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Pad Shape	Template	Hole Length	Routed Path Length
⊙	926	0,200mm	PTH	Round	Top Layer - Bottom Layer	Rounded	v45h20m0mx0	-	-
○	14	0,400mm	PTH	Round	Top Layer - Bottom Layer	Rounded	c60h40m65(Tol3-3)	-	-
✱	4	0,450mm	PTH	Slot	Top Layer - Bottom Layer	Rounded	r155_115h45_85r100m160_120(Tol5-5)	0,850mm	0,400mm
⊗	4	0,600mm	PTH	Slot	Top Layer - Bottom Layer	Rounded	r190_120h60_130r100m195_125	1,300mm	0,700mm
⋈	20	0,700mm	PTH	Round	Top Layer - Bottom Layer	Rounded	(Mixed)	-	-
◇	27	0,900mm	PTH	Round	Top Layer - Bottom Layer	(Mixed)	(Mixed)	-	-
⊕	2	0,970mm	NPTH	Round	Top Layer - Bottom Layer	Rounded	c0hn97m102	-	-
⋈	3	1,000mm	NPTH	Round	Top Layer - Bottom Layer	Rounded	(Mixed)	-	-
▽	39	1,000mm	PTH	Round	Top Layer - Bottom Layer	(Mixed)	(Mixed)	-	-
✱	3	1,100mm	NPTH	Round	Top Layer - Bottom Layer	Rounded	c0hn110m115	-	-
⊕	12	1,100mm	PTH	Round	Top Layer - Bottom Layer	(Mixed)	(Mixed)	-	-
⋈	2	1,190mm	NPTH	Round	Top Layer - Bottom Layer	Rounded	c0hn119m124	-	-
✱	2	2,000mm	NPTH	Round	Top Layer - Bottom Layer	Rounded	c50hn200m210	-	-
⊗	4	3,500mm	PTH	Round	Top Layer - Bottom Layer	Rounded	c700h350m705	-	-
	1062 Total								

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout