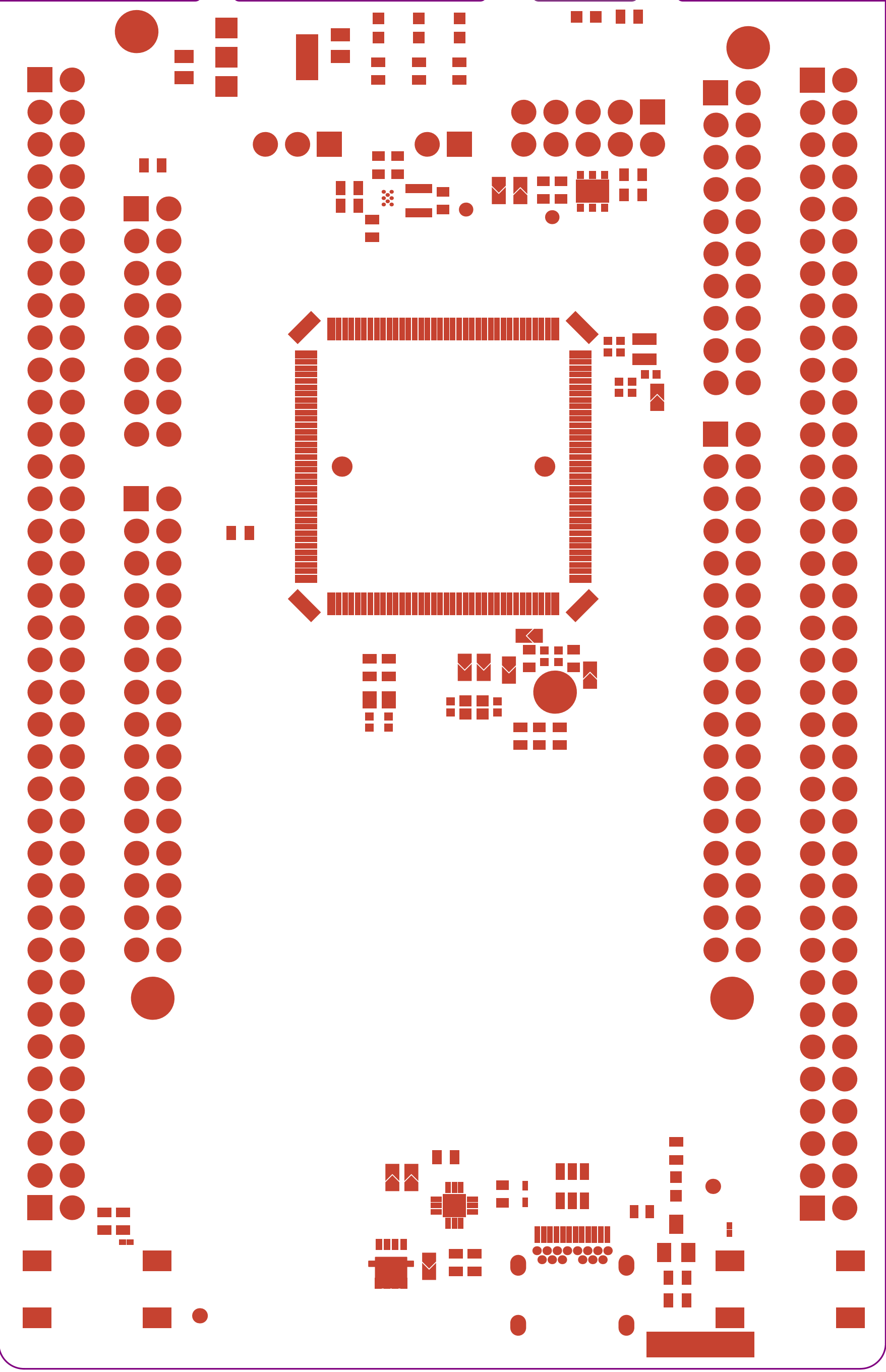
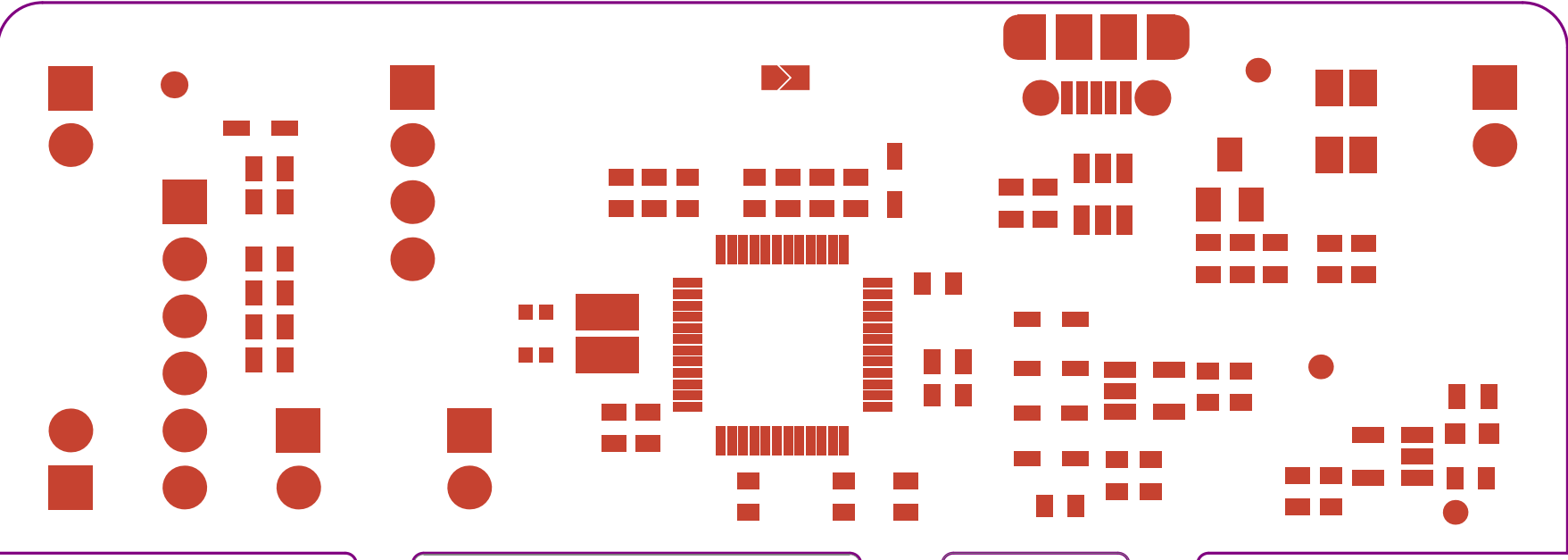
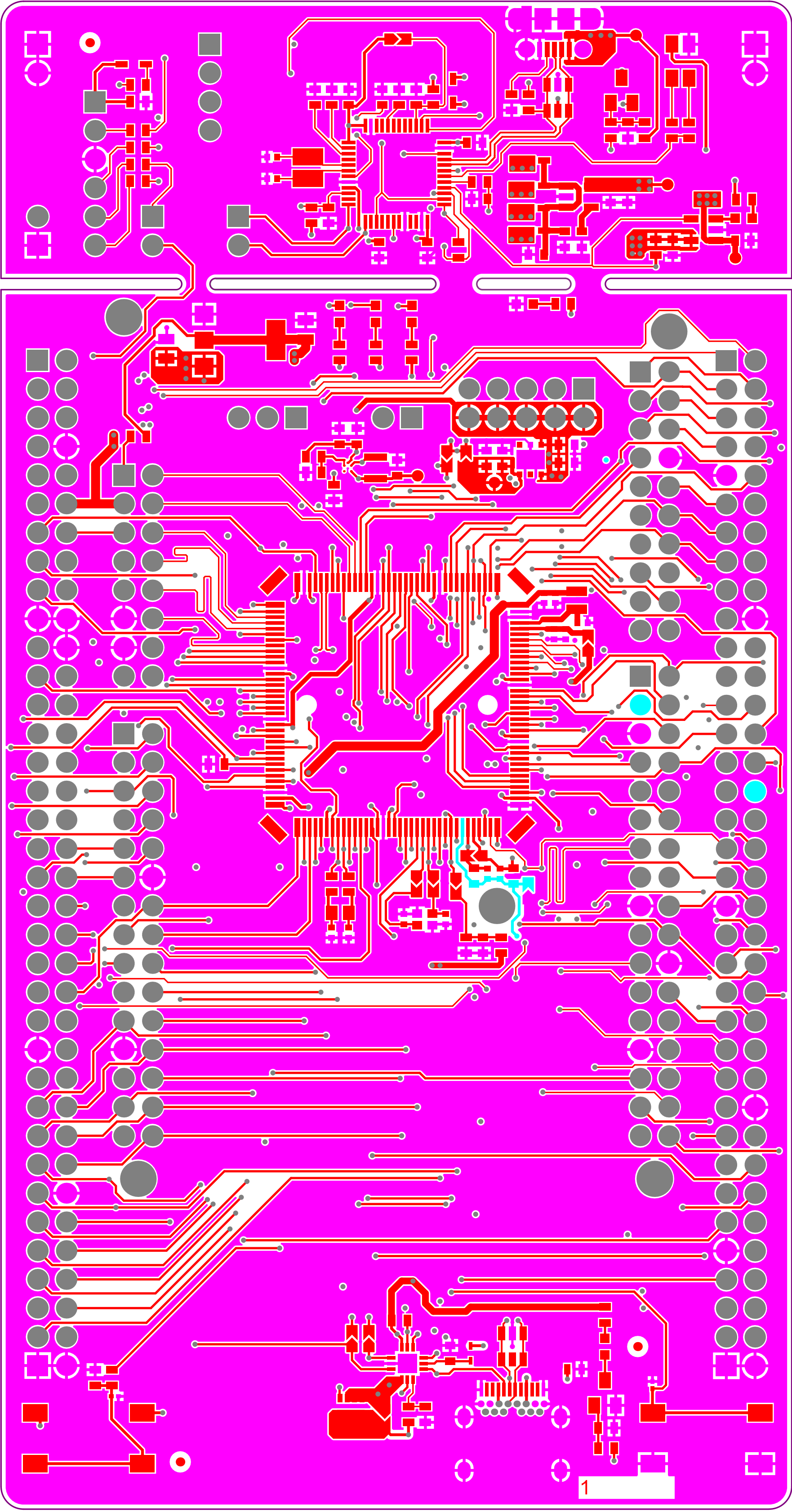
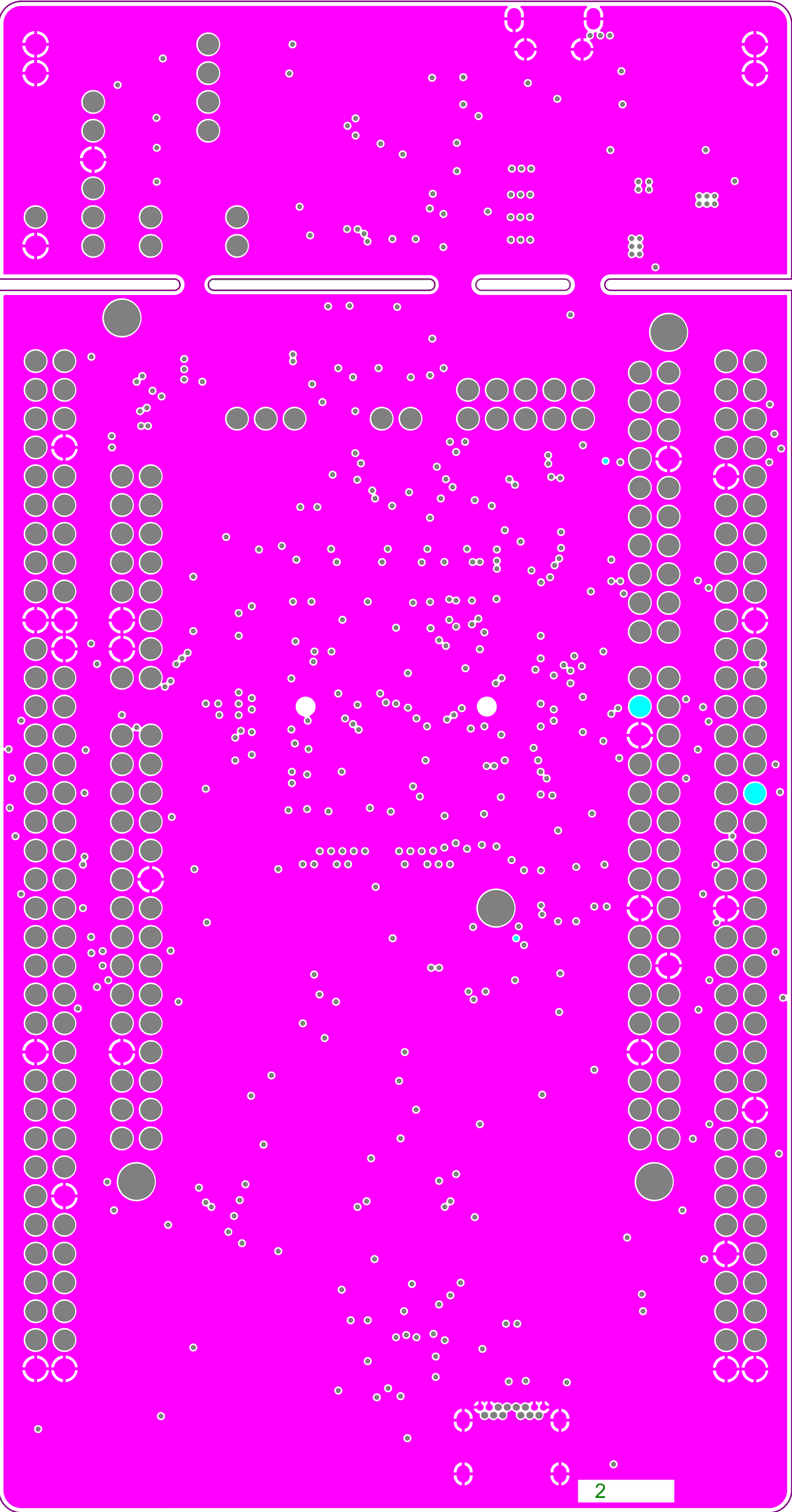


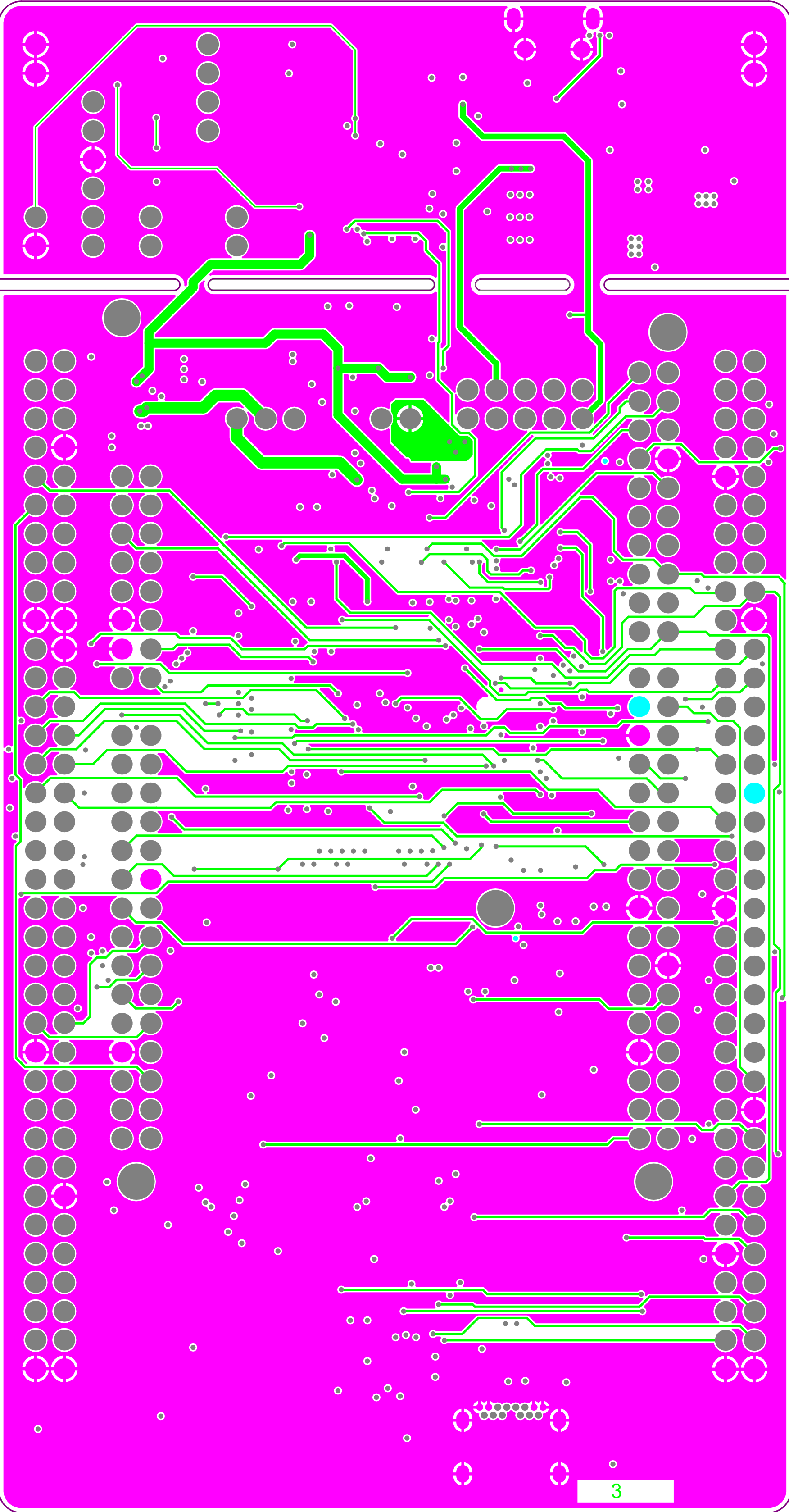


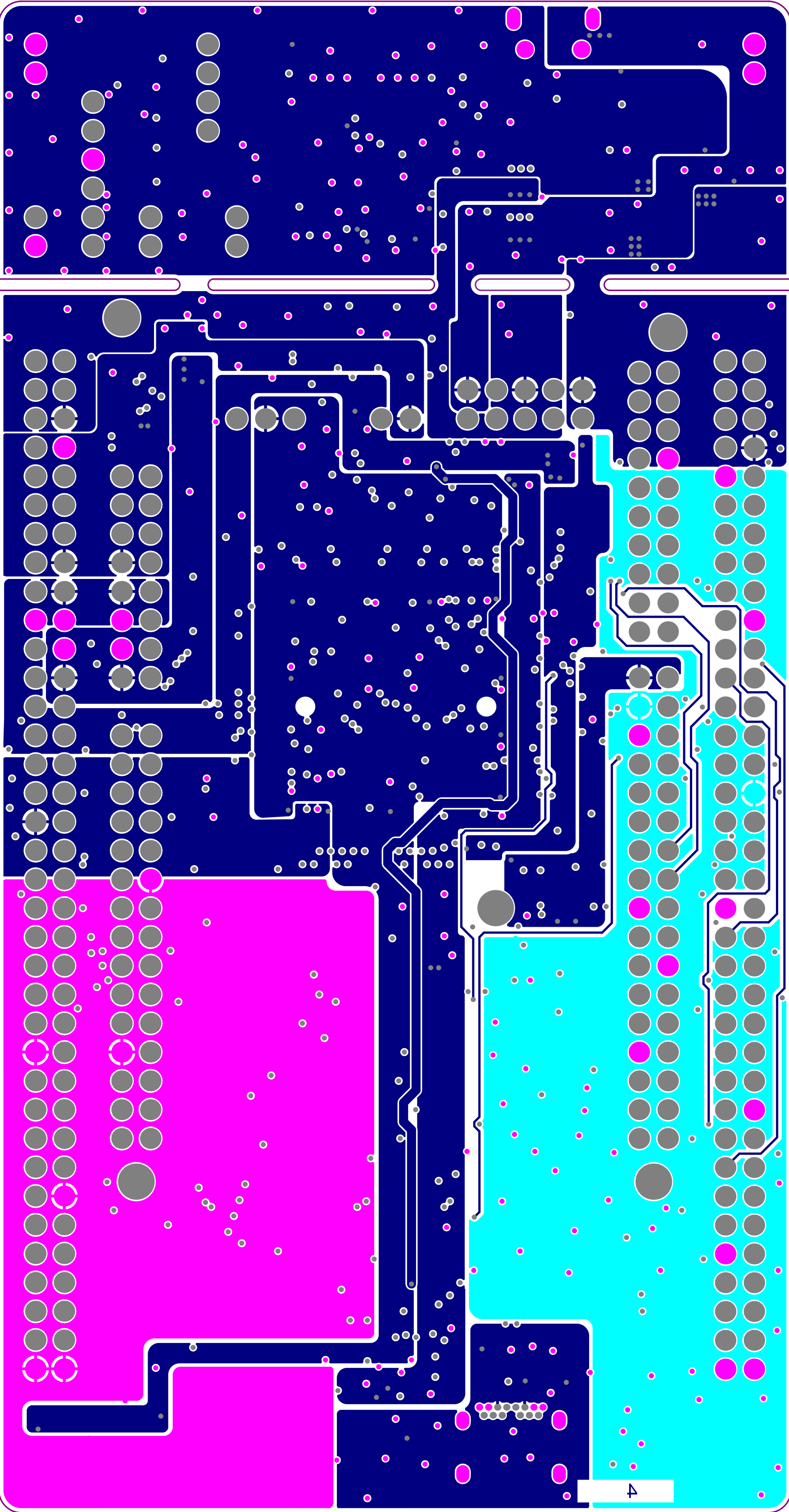
.GTO

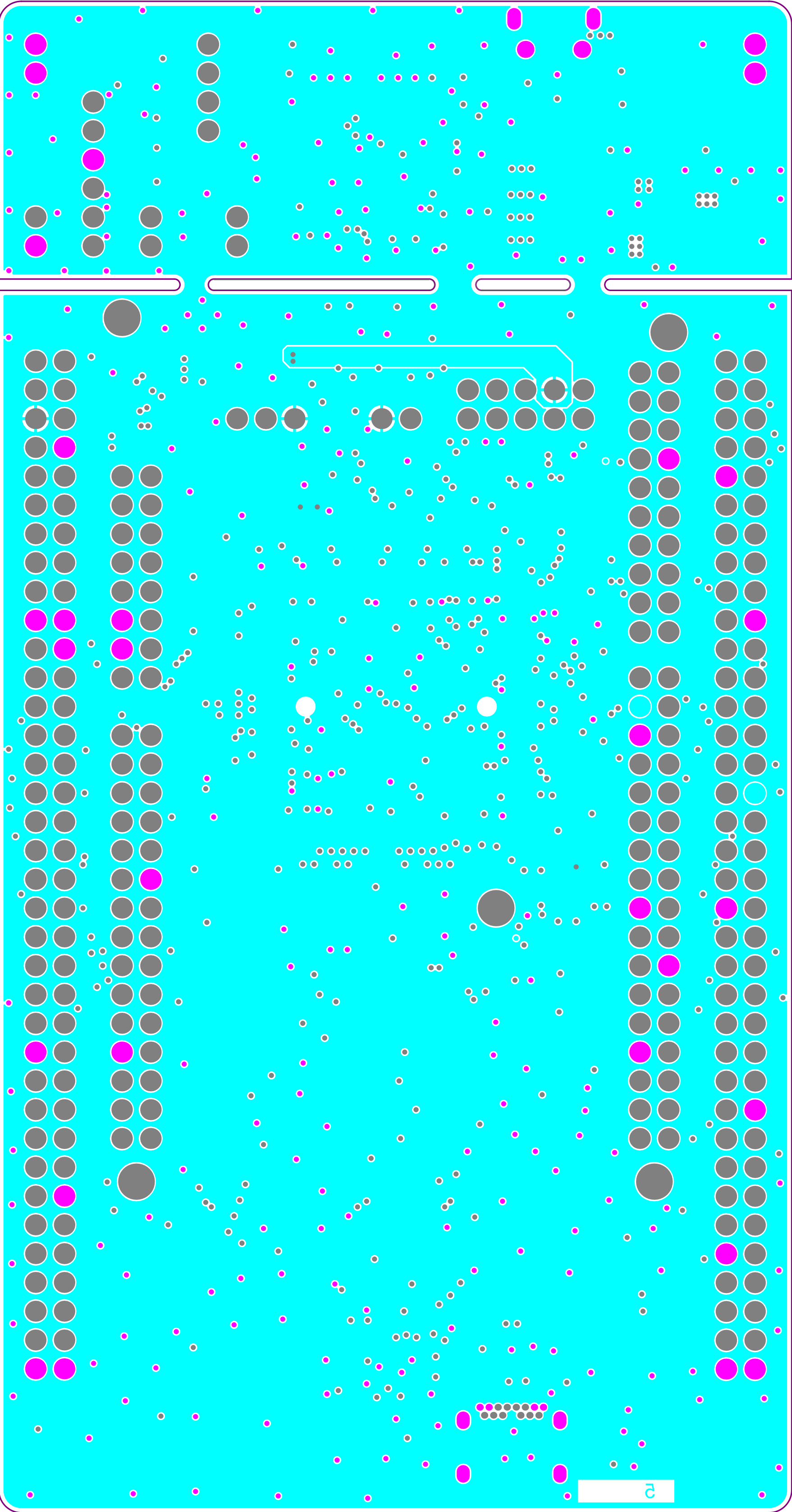


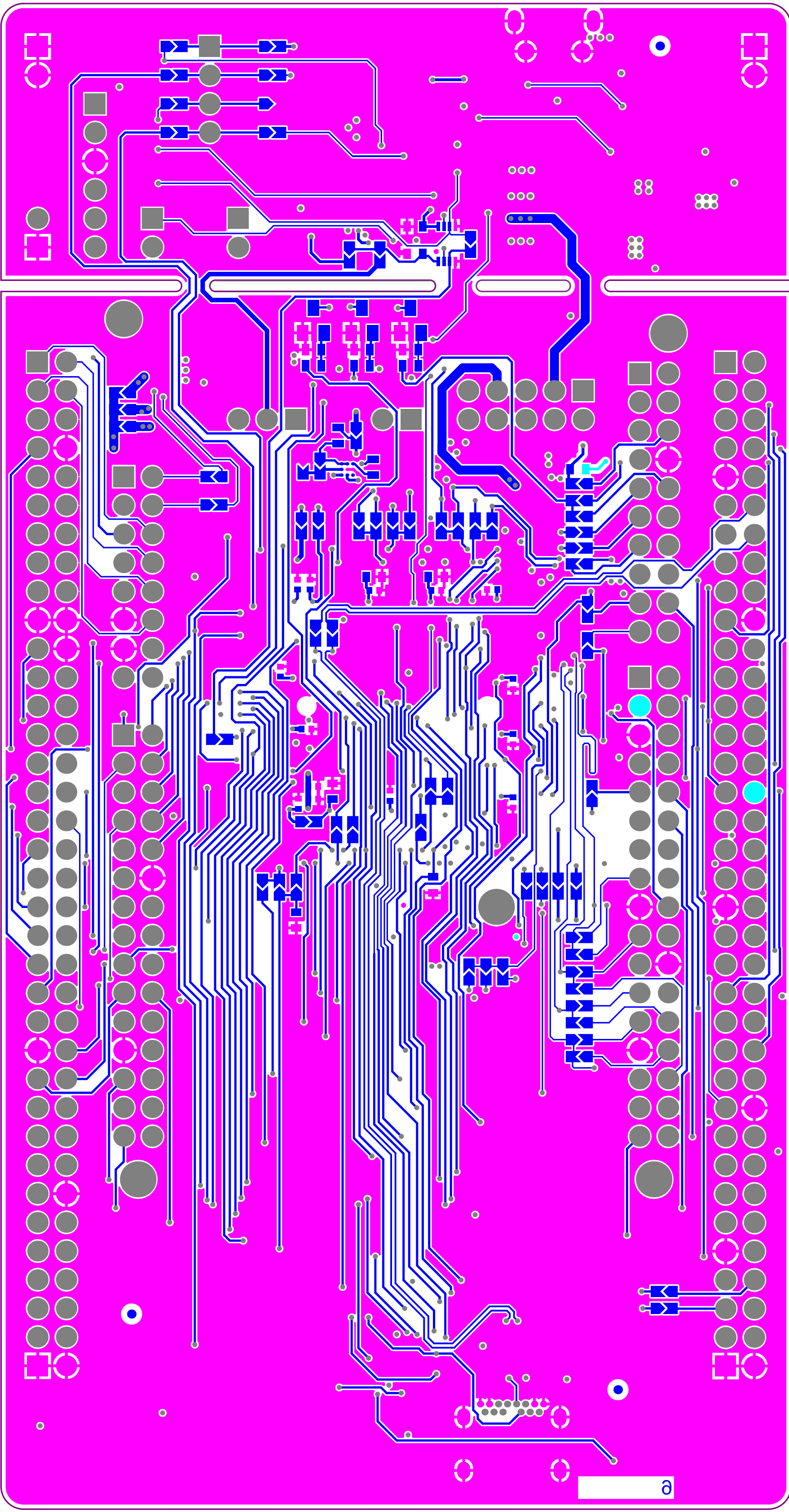


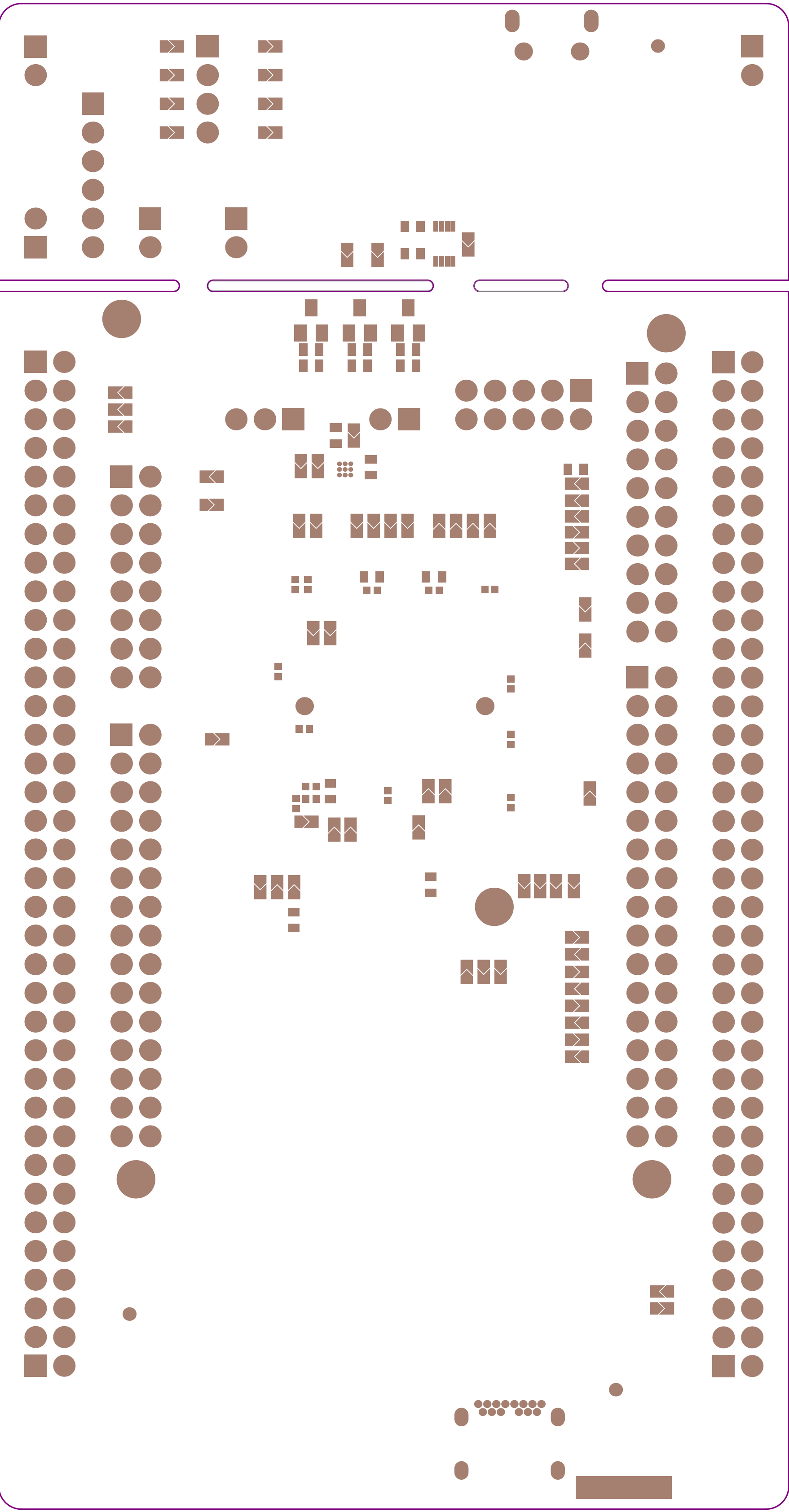












PCB SPECIFICATIONS :

- A. MATERIAL :

B. MATERIAL FAMILY :

C. SOLDERMASK COLOR :

D. SILKSCREEN COLOR :

E. SURFACE FINISH :

F. IMPEDANCE CONTROL :

G. THROUGH VIA :

H. STACK-UP :
- FR-4

N/A

☐ GREEN

☐ WHITE

☒ ENIG

☐ HASL

☐ NO
- ☐ TG-170

☒ TG-150

☐ TG-140
- ☒ WHITE

☐ RED

☐ BLACK

☒ Blue ink PANTONE 2955
- ☐ IMMERSION SILVER

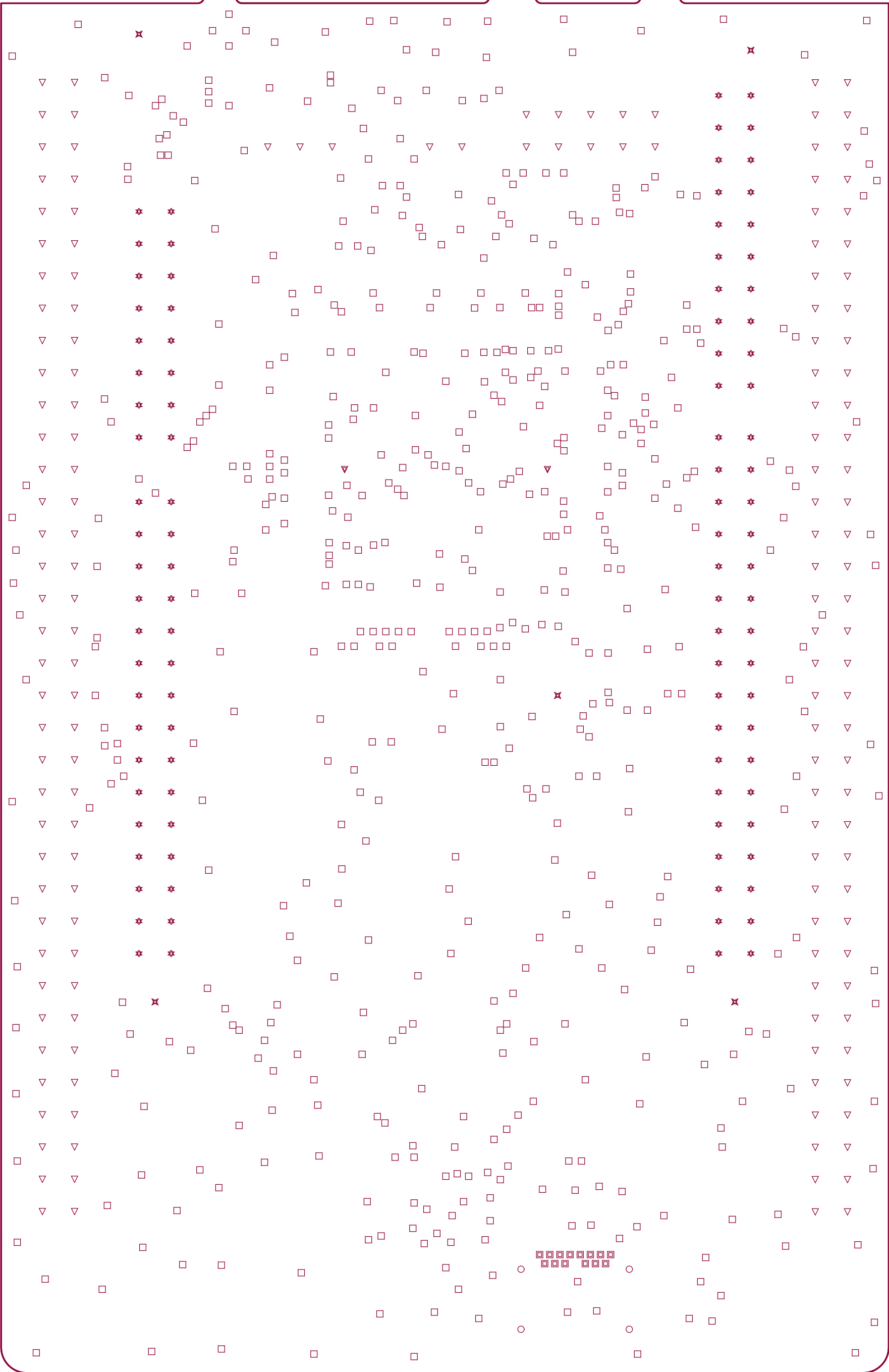
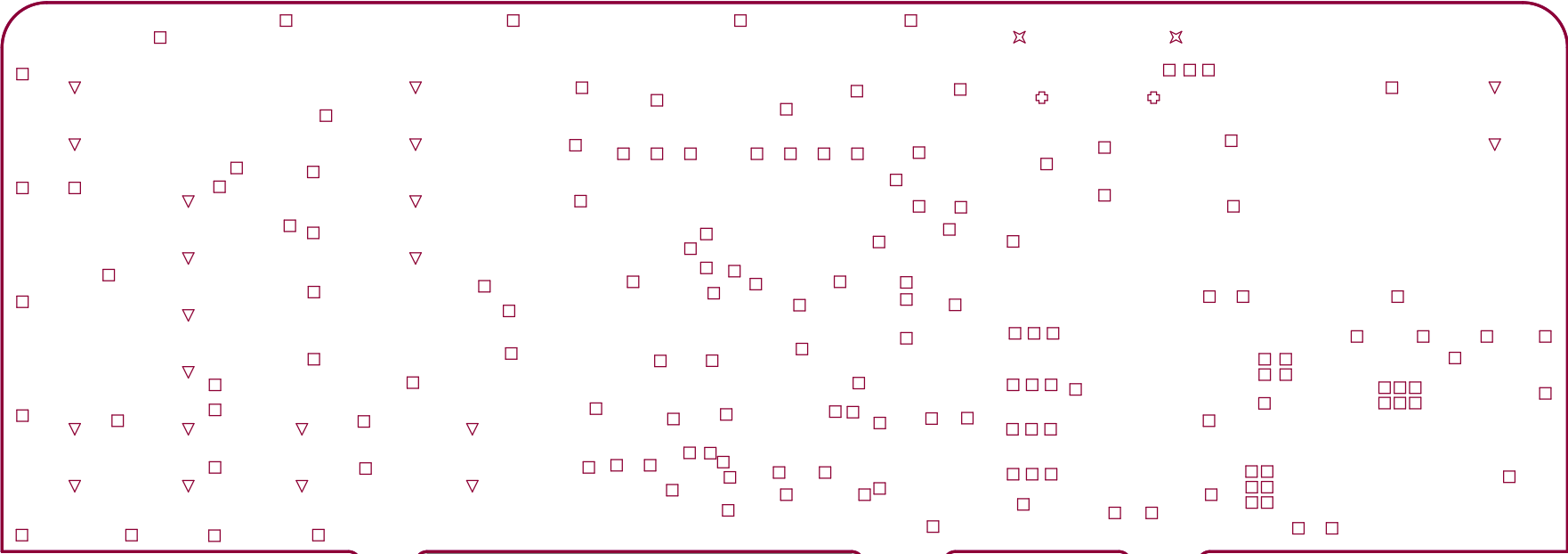
☐ IMMERSION TIN

☐ GOLDEN FINGER
- ☒ YES (SEE IMPEDANCE TABLE FOR DETAIL INFORMATION)

☐ NON-CONDUCTIVE EPOXY.

SEE LAYER STACK-UP SEQUENCE FOR OVERALL THICKNESS.

MB1361C



Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0,015mm	3,5	
3	Top Layer	Copper	0,042mm		
4	Dielectric 1	FR-4	0,100mm	4,2	
5	Signal Layer 1	Copper	0,035mm		
6	Dielectric 2		0,102mm	4,2	
7	Signal Layer 2	Copper	0,035mm		
8	Dielectric 3		0,950mm	4,2	
9	Signal Layer 3	Copper	0,035mm		
10	Dielectric 4		0,102mm	4,2	
11	Signal Layer 4	Copper	0,035mm		
12	Dielectric 5		0,100mm	4,2	
13	Bottom Layer	Copper	0,042mm		
14	Bottom Solder	Solder Resist	0,015mm	3,5	
15	Bottom Overlay				

MINIMUM PARAMETERS

DEFAULT
TRACKS : 0.127 mm
GAPS : 0.127 mm

IMPEDANCE
TRACKS : 0.102 mm

Symbol	Count	Hole Size	Plated	Drill Layer Pair	Via/Pad	Pad Shape	Hole Length	Routed Path Length
✂	2	0,600mm (23,62mil)	PTH	Top Layer - Bottom Layer	Pad	Rounded	1,300mm (51,18mil)	0,700mm (27,56mil)
⊕	2	0,900mm (35,43mil)	PTH	Top Layer - Bottom Layer	Pad	Rounded	-	-
▽	2	1,500mm (59,06mil)	NPTH	Top Layer - Bottom Layer	Pad	Rounded	-	-
○	4	0,450mm (17,72mil)	PTH	Top Layer - Bottom Layer	Pad	Rounded	0,850mm (33,47mil)	0,400mm (15,75mil)
✂	5	3,200mm (125,98mil)	NPTH	Top Layer - Bottom Layer	Pad	Rounded	-	-
□	14	0,400mm (15,75mil)	PTH	Top Layer - Bottom Layer	Pad	Rounded	-	-
✂	100	1,100mm (43,31mil)	PTH	Top Layer - Bottom Layer	Pad	Rounded	-	-
▽	179	1,000mm (39,37mil)	PTH	Top Layer - Bottom Layer	Pad	Rounded	-	-
□	705	0,200mm (7,87mil)	PTH	Top Layer - Bottom Layer	Via	Rounded	-	-
	1013 Total							

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout