



Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0,010mm	3,5	
3	Top Layer	Copper	0,042mm		
4	Dielectric 1	FR4	0,093mm	4,2	
5	Layer 2	Copper	0,035mm		
6	Dielectric 2	FR4	0,100mm	4,2	
7	Layer 3	Copper	0,035mm		
8	Dielectric 3	FR4	0,986mm	4,2	
9	Layer 4	Copper	0,035mm		
10	Dielectric 4	FR4	0,100mm	4,2	
11	Layer 5	Copper	0,035mm		
12	Dielectric 5	FR4	0,093mm	4,2	
13	Bottom Layer	Copper	0,042mm		
14	Bottom Solder	Solder Resist	0,010mm	3,5	
15	Bottom Overlay				

Symbol	Count	Hole Size	Plated	Hole Type	Via/Pad	Hole Length	Routed Path Length
I	1	0,800mm <31,50mil>	NPTH	Round	Pad	-	-
▣	1	1,100mm <43,31mil>	NPTH	Round	Pad	-	-
✱	1	2,100mm <82,68mil>	NPTH	Round	Pad	-	-
◇	2	0,600mm <23,62mil>	PTH	Slot	Pad	1,300mm <51,18mil>	0,700mm <27,56mil>
⊕	2	0,650mm <25,59mil>	PTH	Slot	Pad	0,850mm <33,47mil>	0,200mm <7,88mil>
⊗	2	0,850mm <33,47mil>	NPTH	Slot	Pad	2,425mm <95,47mil>	1,575mm <62,01mil>
F	2	1,000mm <39,37mil>	PTH	Slot	Pad	1,600mm <62,99mil>	0,600mm <23,62mil>
□	2	1,500mm <59,06mil>	NPTH	Round	Pad	-	-
○	2	1,700mm <66,93mil>	PTH	Round	Pad	-	-
⊗	2	2,150mm <84,65mil>	NPTH	Round	Pad	-	-
◇	2	3,200mm <125,98mil>	NPTH	Round	Pad	-	-
▽	4	2,000mm <78,74mil>	NPTH	Round	Pad	-	-
⊗	4	3,200mm <125,98mil>	PTH	Round	Pad	-	-
▣	4	3,300mm <129,92mil>	PTH	Round	Pad	-	-
B	5	0,810mm <31,89mil>	PTH	Round	Pad	-	-
⌢	8	1,050mm <41,34mil>	NPTH	Round	Pad	-	-
⊕	8	2,330mm <91,73mil>	PTH	Round	Pad	-	-
⊙	9	3,500mm <137,80mil>	PTH	Round	Pad	-	-
□	10	0,900mm <35,43mil>	PTH	Round	Pad	-	-
⊙	12	0,200mm <7,87mil>	PTH	Round	Via	-	-
⊕	16	0,920mm <36,22mil>	PTH	Round	Pad	-	-
✱	20	0,700mm <27,56mil>	PTH	Round	Pad	-	-
H	20	1,200mm <47,24mil>	PTH	Round	Pad	-	-
✱	31	0,150mm <5,91mil>	PTH	Round	Via	-	-
⊗	34	1,100mm <43,31mil>	PTH	Round	Pad	-	-
▽	85	1,000mm <39,37mil>	PTH	Round	Pad	-	-
▽	1666	0,300mm <11,81mil>	PTH	Round	Via	-	-
	1955 Total						

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout