

FABRICATION NOTE:

- 1:FABRICATE USING LATEST REVISION OF IPC-6012 CLASS 2 OR ABOVE
- 2:FABRICATE USING MASTER ARTWORK EVALMasterGaN1-R1 FOR CIRCUIT PATTERN
NO DEVIATION FROM MASTER ARTWORK ARE PERMITTED WITHOUT WRITTEN
APPROVAL FROM AN AUTHORIZED STM REPRESENTATIVE
- 3:VENDOR MUST BE UL QUALIFIED AND BOARDS MUST BE IDENTIFIED WITH
APPROPRIATE VENDOR UL IDENTIFICATION MARK, LOT OR JOB NUMBER AND
PCB MATERIAL FLAMMABILITY RATING (94V-0) MINIMUM.
- 4:MATERIAL: FR4 RoHS COMPLIANT MINIMUM 130 C
SEE "DETAIL 'A'" FOR BOARD THICKNESS, COPPER WEIGHT AND LAYER CONSTRUCTION
- 5:SOLDER MASK BOTH SIDE OF BOARD WITH LIQUID PHOTO IMAGEABLE PER
IPC-SM-840 (LATEST LEVEL), CLASS H. COLOR: GREEN
- 6:FINISH: HAL LF RoHS COMPLIANT
- 7:FOR SMT COMPONENTS WITH PIN PITCH OF 0.019 INCH OR MORE NO GANG RELIEF
OF SOLDER MASK IS ALLOWED
- 8:SILKSCREEN TO BE WHITE, NON CONDUCTIVE, EPOXY INK OR EQUIVALENT
- 9:THIS IS NOT A CONTROLLED IMPEDANCE BOARD
- 10:ON BOTTOM PCB MUST BE IDENTIFIED MANUFACTURER LOGO AND PCB TYPE
(MEANS OF MATERIAL TYPE INDICATED ON THE YELLOW CARD DOCUMENT)
- 11:TEARDROPPING OF VIAS AND PADS ALLOWED AT PAD TO TRACE INTERSECTION
TO INSURE A 2MIL ANULAR RING AT THE JUNCTION
- 12:REMOVE SILKSCREEN FROM SOLDERABLE SURFACE
- 13:ALL BOARD TO BE 100% ELECTRICALLY NETLIST TESTED FOR OPENS AND ON
SHORTS. APPLY TEST STAMP IN REFERENCED AREA
- 14:THIS BOARD SHALL BE FULLY COMPLIANT WITH UL796

Finished Hole Tolerance - All units are in Inches -

Finish Hole Diameter	Plated Through Finish Hole Diameter	Non Plated Through Finish Hole Diameter
0.008"-0.013"	+0.002/-FHS"	+/-0.002"
0.014"-0.063"	+/-0.003"	+/-0.002"
0.064"-0.156"	+/-0.004"	+/-0.003"
0.157"-0.250"	+/-0.007"	+/-0.004"
0.251"and up	+/-0.007"	+/-0.005"
0.125"	Non Plated Only	+0.003/-0.000"

DETAIL 'A'

LAYER BUILD UP
(reference only)

	Cu - 70u
	CORE - 254u
	Cu - 70u
	Prepreg - 127u
	Cu - 70u
	CORE - 254u
	Cu - 70u

(external Cu th are after plating)
estimated total thickness 950u


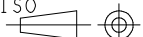
DRILL CHART: TOP to BOTTOM

ALL UNITS ARE IN MILS

FIGURE	FINISHED SIZE	PLATED	QTY
A	13.0	PLATED	203
B	31.5	PLATED	29
C	35.43	PLATED	31
D	55.0	PLATED	2
E	82.68	PLATED	2
F	122.05	NON-PLATED	4

TOTAL HOLES: 271

REVISION DATE

REVISION DATA		Description						Symbol	
		EVALMasterGaN1						MasterGaN1	
								Group	
I&PC - AMS Group AGRATE B. ITALY		FABRICATION DRAWING							
ALL RIGHTS STRICTLY RESERVED. REPRODUCTION OR ISSUE TO THIRD PARTIES IN ANY FORM WHATEVER IS NOT PERMITTED WITHOUT WRITTEN AUTHORIZATION FROM STMicroelectronics.		TOLERANCE UNLESS NOTED						SCALE:	REV. 1.0
		UNIT=MM							
		linear	0 <10	>10 <50	>50 <200	>200	ANG. LES.	Material	
Drawn M. RIGO	date 08-May-2019	midle	±0.1	±0.2	±0.3	±0.5	±1°	Treatment and surface finishing	
Approved	date	accurate	±0.05	±0.1	±0.15	±0.25	±30°		