

# Using X-CUBE-RC-CALIB software to calibrate STM32C0 Series internal RC oscillator

#### Introduction

The STM32C0 Series microcontrollers have internal RC oscillators that can be selected as the system clock source. These are known as the HSI48 (high-speed internal 48 MHz) and LSI (low-speed internal) oscillators.

The operating temperature has an impact on the accuracy of the RC oscillators. At 25 °C, the HSI48 oscillator has an accuracy of 0.25% typically, but in the temperature range of -40 to 125 °C, the accuracy decreases.

To compensate for the influence of temperature on internal RC oscillators accuracy, the STM32C0 Series microcontrollers have built-in features to allow users to calibrate the HSI48 oscillator and to measure the LSI (low-speed internal) oscillator frequency.

This application note focuses on how to calibrate internal RC oscillator HSI48. Three methods are presented:

- Method 1 consists in finding the frequency with the minimum error.
- Method 2 consists in finding the frequency within the allowed error.
- Method 3 consists in preparing the table of calibration values that is later used for fast calibration.

All three methods require an accurate reference signal.

The measurement of the LSI oscillator is performed by connecting the oscillator to a timer input capture.

This application note concerns the STM32C0 Series microcontrollers and the X-CUBE-RC-CALIB software.



# 1 General information

This document applies to Arm®-based devices.

Note: Arm is a registered trademark of Arm Limited (or its subsidiaries) in the US and/or elsewhere.

arm

AN5857 - Rev 1 page 2/23



# 2 STM32C0 Series system clock

The STM32C0 Series microcontrollers have various clock sources that can be used to drive the system clock:

- A 48-MHz high-speed internal (HSI48) RC oscillator clock
- A 4 to 48-MHz high-speed external (HSE) oscillator clock
- A 32-kHz low-speed internal (LSI) RC oscillator clock
- A 32.768 kHz low-speed external (LSE) oscillator clock

Typically, the high-speed internal (HSI48) RC oscillator has a frequency of 48 MHz and consumes 500 μA.

The internal RC oscillator (HSI48) has the advantage of providing a low-cost clock source (no external components required). It also has a faster startup time and a lower power consumption than the external oscillator. The HSI48 oscillator can be calibrated to improve its accuracy. But even with calibration, the internal RC oscillator frequency is less accurate than the frequency of an external crystal oscillator or a ceramic resonator (tens of ppm).

The 32 kHz low-speed internal (LSI) RC is designed to drive the independent watchdog and optionally the real time clock (RTC). The LSI oscillator cannot be calibrated, but can be measured to evaluate frequency deviations (due to temperature and voltage changes). The 32.768 kHz low-speed external crystal (LSE crystal) optionally drives the real time clock (RTC).

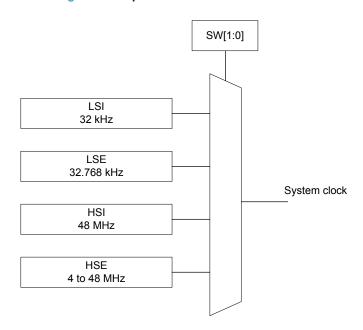


Figure 1. Simplified clock tree

7880/610

AN5857 - Rev 1 page 3/23



# 3 Internal RC oscillator calibration

The frequency of the internal RC oscillators may vary from one chip to another due to manufacturing process variations. For this reason, the HSI48 RC oscillator is factory-calibrated by STMicroelectronics to have a  $\pm 1\%$  accuracy from  $T_A = 0$  °C to  $T_A = 85$  °C. After reset, the factory calibration value is automatically loaded in the internal calibration bits.

The frequency of the internal RC oscillators can be fine-tuned to achieve a better accuracy with wider temperature and supply voltage ranges. The trimming bits are used for this purpose.

For the HSI48 oscillator, the calibration value is loaded in HSICAL[7:0] bits after reset. Seven trimming bits HSITRIM[6:0] are used for fine-tuning. The default trimming value is 64. An increase/decrease in this trimming value causes an increase/decrease in HSI48 frequency. The HSI48 oscillator is fine-tuned in steps of 0.3% (around 144 kHz).

The figure below shows the dependency of HSI frequency on calibration value. The HSI48 oscillator frequency increases with the calibration value (calibration value = default HSICAL[7:0] + HSITRIM[6:0]), except at multiple of 64. At these calibration values, the negative step can reach twenty times the positive step.

During factory calibration the effort is made to place value of HSICAL just between the two negative steps. This is done so the HSITRIM can be modified to smoothly manipulate the value of final frequency without reaching the negative step.

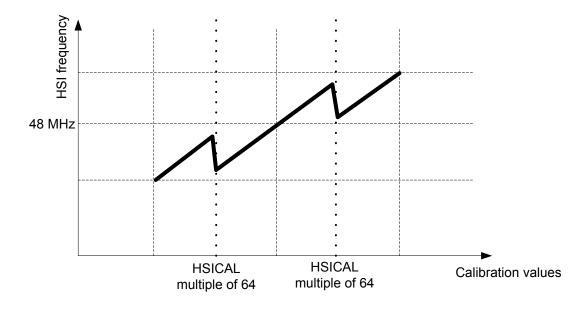


Figure 2. HSI48 oscillator trimming characteristics

c I C

AN5857 - Rev 1 page 4/23



### 3.1 HSI48 calibration principle

The calibration principle consists in:

- 1. Setting HSI48 as the system's clock source
- 2. Measuring the internal RC oscillator (HSI48) frequency for each trimming value
- 3. Computing the frequency error for each trimming value
- 4. Setting the trimming bits with the optimum value (corresponding to the lowest frequency error)

The internal oscillator frequency is not measured directly but it is computed from the number of clock pulses counted using a timer compared with the requested value. To do this, a very accurate reference frequency must be available such as the LSE frequency provided by the external 32.768 kHz crystal or the 50 Hz/60 Hz of the external signal (refer to Section 3.2.1 Case where LSE is used as the reference frequency to measure HSI48 and Section 3.2.2 Case where another source is used as the reference frequency to measure HSI48).

The figure below shows how the reference signal period is measured in number of timer counts.

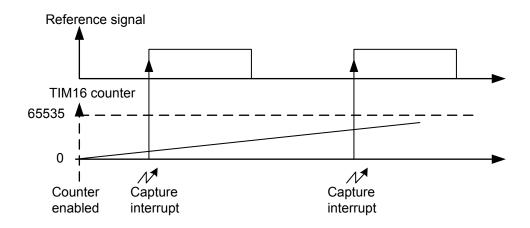


Figure 3. Timing diagram of internal oscillator calibration

After enabling the timer counter, when the first rising edge of the reference signal occurs, the timer counter value is captured and stored in IC1ReadValue1. At the second rising edge, the timer counter is captured again and stored in IC1ReadValue2. The elapsed time between two consecutive rising edges (IC1ReadValue2 - IC1ReadValue1) represents an entire period of the reference signal.

Since the timer counter is clocked by the system clock (internal RC oscillator HSI48), the real frequency generated by the internal RC oscillator versus the reference signal is given by:

Measuredfrequency = (IC1ReadValue2 – IC1ReadValue1) x referencefrequency

The error (in Hz) is computed as the absolute value of the difference between the measured frequency and the typical value.

Hence, the internal oscillator frequency error is expressed as:

• Error(Hz) = Measuredfrequency – requested value

After calculating the error for each trimming value, the algorithm determines the optimum trimming value (that corresponds to the nearest frequency to requested value) to be programmed in the trimming bits (refer to Section 3.3 Description of the internal oscillator calibration firmware).

### 3.2 Hardware implementation

The STM32C0 Series devices have the ability to connect internally and indirectly the internal or external oscillator to the dedicated timers (TIM14, TIM16 and TIM17).

AN5857 - Rev 1 page 5/23



### 3.2.1 Case where LSE is used as the reference frequency to measure HSI48

When LSE is used as the reference frequency to measure HSI48, the TIM16 channel is used. The LSE clock can be used as the reference signal for internal oscillator calibration and no additional hardware connections are required. Only the LSE oscillator should be connected to OSC32\_IN and OSC32\_OUT.

The figure below shows the hardware connections needed for internal oscillators calibration, using LSE as an accurate frequency source for calibration.

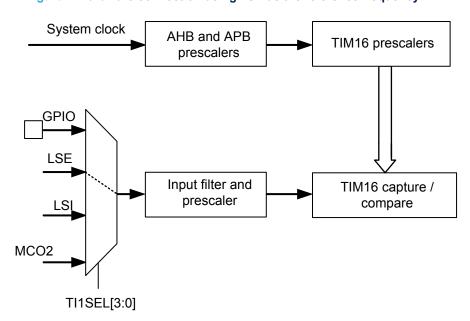


Figure 4. Hardware connection using LSE as the reference frequency

DT57090V1

page 6/23

DT57091V1



#### 3.2.2 Case where another source is used as the reference frequency to measure HSI48

Any signal with an accurate frequency can be used for the internal oscillator calibration, and the external signal is one of the possibilities.

As shown in the figure below, the reference signal should be connected to Timer 16 channel 1.

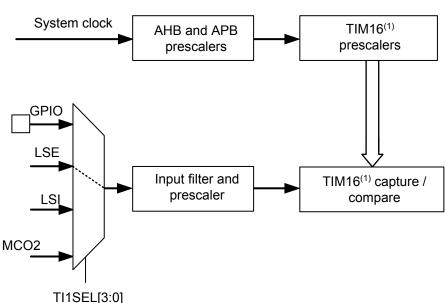


Figure 5. Hardware connection using external reference frequency

(1) When using an external signal as a reference, another timer can be used instead of TIM16. The firmware provided with this application note uses TIM16

# 3.3 Description of the internal oscillator calibration firmware

The internal RC oscillator calibration firmware provided with this application note includes four major functions:

```
uint32_t HSI_CalibrateMinError(void)
ErrorStatus HSI_CalibrateFixedError(uint32_t MaxAllowedError, uint32_t* Freq)
ErrorStatus HSI_CalibrateCurve( uint32_t* Freq );
void HSI_GetCurve( void );
```

#### 3.3.1 Internal oscillator calibration with minimum error

The HSI\_CalibrateMinError() function calibrates the internal oscillator (HSI48) to have the frequency nearest to the typical value. It measures all frequencies for different trimming values and provides the trimming value that corresponds to the frequency with the minimum error. The trimming value is programmed in the trimming bits.

After calibration, the HSI\_CalibrateMinError() function returns the internal oscillator frequency value as an unsigned 32-bit integer (uint32\_t).

#### **Example:**

```
uint32_t InternOscAfterCalib = 0;
{
.....
/* Get the internal oscillator (HSI) value after calibration */
InternOscAfterCalib = HSI_CalibrateMinError();
}
```

The flowchart below provides the algorithm for this function.

AN5857 - Rev 1 page 7/23



Start of calibration Configure peripherals used in calibration: RCC, GPIO, TIMx Set system clock source: HSI48 Trimming value = 0 Set trimming bits with trimming value Measure current frequency Compute the current frequency error Optimum frequency error > frequency error? Yes Save current frequency error: Optimum frequency error = Frequency error Optimum calibration value = Trimming value Increment trimming value Yes Trimming value < Number of steps? **♦** No Set trimming bits with optimum trimming value Return internal oscillator frequency after calibration End of calibration

Figure 6. Internal oscillator calibration: finding the minimum frequency error

① The frequency measurement is detailed in Section « Internal oscillator frequency measurement »

F57092V1

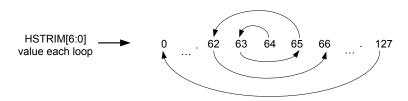


#### 3.3.2 HSI48 calibration with fixed error

HSI\_CalibrateFixedError(uint32\_t MaxAllowedError, uint32\_t Freq) function is provided to calibrate the HSI48 oscillator with a maximum allowed frequency error. It is configured by the user as an absolute value given in Hertz (the first parameter: MaxAllowedError). This function is the same as HSI\_CalibrateMinError(), but it searches for the frequency that has an error (in absolute value) lower than or equal to MaxAllowedError.

- If it finds this frequency, it stops searching and configures the trimming bits HSITRIM[6:0] according to this frequency and returns SUCCESS, meaning that the calibration operation has succeeded.
- Otherwise, it continues searching for it until the HSITRIM bits = 127 (128th frequency). It then sets the trimming bits HSITRIM[6:0] to the default calibration value and returns ERROR, meaning that the calibration has failed and did not find any frequency with an error lower than or equal to MaxAllowedError. The frequency measurement starts with value of HSTRIM equal to 64. The HSITRIM value is computed in loops to find the next value. That is, the HSITRIM value starts from 64, then goes to the next value to the left, then to the next to the right, then to the second to the left and so on until it reaches 127, forming a "spring loop" (as shown in the figure below). This algorithm is based on the fact that the probability of finding the frequency that has the minimum error increases when the HSITRIM[6:0] value tends to 64. This algorithm is implemented so as to minimize the time consumed by the calibration process.

Figure 7. Spring loop



DT493

The second parameter is used to get the frequency (in Hertz) after calibration in the form of an unsigned 32-bit integer (unit32\_t).

The following flowchart provides the algorithm for this function.

AN5857 - Rev 1 page 9/23



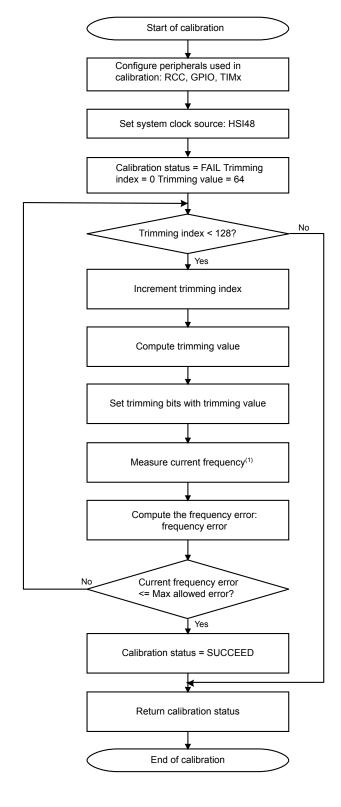


Figure 8. HSI48 calibration flowchart: maximum allowed frequency error

DT57093V1

1. The frequency measurement is detailed in Section 3.3.4 Internal oscillator frequency measurement

AN5857 - Rev 1 page 10/23



#### 3.3.3 Internal oscillator calibration using calibration curve

Both the minimal and fixed error methods can take some time due to the high number of measurements to run (for example 128 measurement for HSI48 with the minimal error method). First, for all trimming values, the difference between the corresponding and the requested frequency is measured and stored in a table. This method uses the same principle as the minimal error method. When it is necessary to calibrate the internal oscillator function HSI\_CalibrateCurve(), only the actual frequency is measured and the appropriate value to compensate the difference is taken from the table.

Start of measurement

Configure peripherals used in calibration: RCC, GPIO, TIMx

Set system clock source: HSI48

Trimming value = 0

Set trimming bits with trimming value

Measure current frequency(1)

Compute the current frequency error

Store the value into the table

Increment trimming value

Yes

Trimming value < Number of steps?

Figure 9. Internal oscillator calibration: measuring the calibration table

1. The frequency measurement is detailed in Section 3.3.4 Internal oscillator frequency measurement.

#### 3.3.4 Internal oscillator frequency measurement

The internal oscillator frequency measurement is performed by Timer 16 capture interrupt. In the timer TIM16 ISR, an entire period of internal oscillator frequency is computed. The number of periods to be measured for each trimming value is configurable by the user in the hsi.h or Isimeasurement.c files, as follows:

#define HSI\_NUMBER\_OF\_LOOPS ((uint32\_t)100)

Isimeasurment.c:

#define LSI\_NUMBER\_OF\_LOOPS ((uint32\_t)10)

The averaging method is used to minimize frequency error measurements. So, if the counter of loops reaches NUMBER\_OF\_LOOPS, the average of all measured frequencies is computed.

AN5857 - Rev 1 page 11/23



Users can easily configure the frequency of the reference source. It is defined in the *InternOscCalibration.h* header file, as follows:

• If the LSE clock is used as the reference frequency, uncomment the line below to make sure the LSE is configured and internally connected to Timer input channel.

```
#define USE_REFERENCE_LSE
```

• If the reference frequency is an external signal equal to 50 Hz, then comment the line above and define the reference frequency as shown below:

```
\#define REFERENCE_FREQUENCY (uint32_t)50 /* The reference frequency value in Hz */
```

The computation of the frequency measurements does not depend on the duty cycle of the source reference signal. It depends on its frequency since the capture 1 interrupt is configured to occur on every rising edge of the reference signal (refer to Figure 12. Timing diagram of an internal RC oscillator measurement).

AN5857 - Rev 1 page 12/23



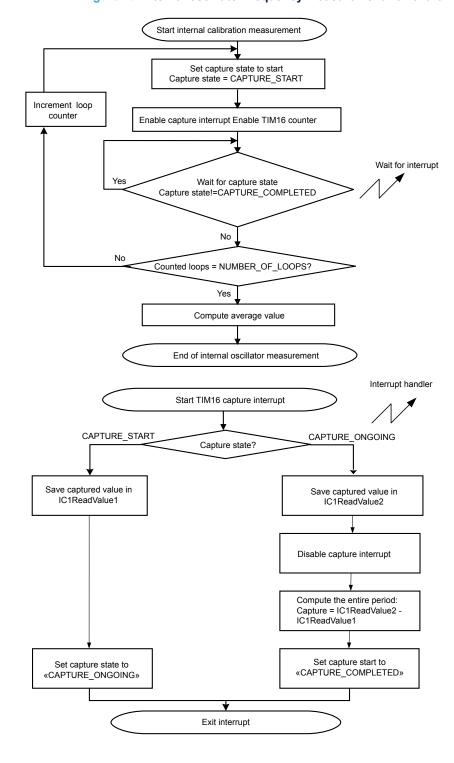


Figure 10. Internal oscillator frequency measurement flowchart

19360V1

AN5857 - Rev 1 page 13/23



# 3.4 Recommendations on the use of the calibration library

- If the external signal frequency is lower than system clock / 65535, the TIM16 counter prescaler should be used to support low frequencies.
- 2. If the external signal frequency is higher than system clock / 100, TIM16 input capture prescaler (divider) should be used to support high frequencies.
- It is recommended to stop all application activities before the calibration process, and to restart them after calling the calibration functions.
   Therefore, the application has to stop the communications, the ADC measurements and any other processes (except when using the ADC for the calibration, refer to Step 6 below).
  - These processes normally use clock configurations that are different from those used in the calibration process. Otherwise, errors might be introduced in the application: errors while reading/sending frames, ADC reading errors since the sampling time has changed, and so on.
- 4. The internal RC oscillator calibration firmware uses the following peripherals: reset and clock control (for trimming internal RC oscillators), Timer 16 (for measuring internal RC oscillators). Therefore, it is recommended to reconfigure these peripherals (if used in the application) after running the calibration routine.
- 5. Real-time calibration versus temperature can be used when the ambient temperature changes noticeably while the application is running. The internal temperature sensor can be used with the ADC watchdog with two thresholds. Each time an ADC watchdog interrupt occurs, a new calibration process has to be performed and the two thresholds are updated according to the current temperature (this feature is not implemented in the firmware provided with this application note):
  Threshold\_High = CurrentTemperatureValue + TemperatureOffset
  Threshold\_Low = CurrentTemperatureValue TemperatureOffset
- 6. It might happen that with change of operation conditions (such as the surrounding temperature) the calibration curve can change. From this reason it is recommended to measure from time to time again (or when the condition change) to keep working with correct values.

AN5857 - Rev 1 page 14/23



### 3.5 Calibration process performance

#### 3.5.1 Duration of the calibration process

The duration of the calibration process depends on:

- 1. The frequency of the reference signal (prescaled value) "REFERENCE\_FREQUENCY"
- 2. The number of measured periods per trimming value "NUMBER OF LOOPS"
- 3. The number of measured frequencies during the calibration process "number of steps".

Once the peripherals are configured and ready (mainly the LSE oscillator), the duration of the calibration process is approximated by:

• Duration = (2 x (NUMBER\_OF\_LOOPS + 1) x number of steps) / REFERENCE\_FREQUENCY

If the calibration process is run with a minimum frequency error for an HSI oscillator

(HSI\_CalibrateMinError()), the number of steps is equal to 128. If the LSE oscillator is used as the reference frequency (REFERENCE\_FREQUENCY = LSE value / Input capture prescaler = 32768/8 = 4096 Hz) and the selected number of measured periods is 10, the calibration consumes approximately:

• Duration = (2 x 51 x 128) / 4096 = 3.19 s

The duration of the calibration process with a maximum allowed error is lower than or equal to the duration of calibration when using the minimum frequency error process.

Note: Multiplying by 2 in the duration formula above is due to the fact that there is no synchronization between the reference signal and the start of counting by the timer.

AN5857 - Rev 1 page 15/23



# 4 Low-speed internal oscillator measurement

The internal LSI RC oscillator is a low-power clock source. In the STM32C0 Series, an internal and indirect connection is provided between the internal RC oscillator LSI and the embedded timer TIM16 to facilitate the measurement procedure.

### 4.1 Measurement principle

The internal RC oscillator measurement procedure consists in running the timer counter using the HSI48 clock, configuring the internal RC oscillator LSI as the source of input capture signal of TIM16.

Below figure shows the configuration used to perform an LSI measurement.

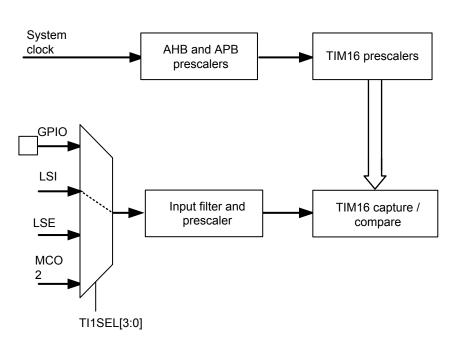


Figure 11. LSI measurement configuration

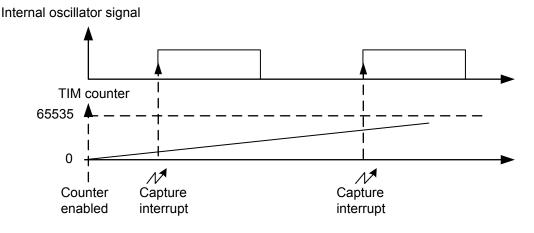
DT57095V1

After enabling the timer counter, when the first rising edge of the internal oscillator signal to be measured occurs, the timer counter value is captured and then stored in IC1ReadValue1. On the second rising edge, the timer counter is captured again and stored in IC1ReadValue2. The elapsed time between two consecutive rising edges of the clock represents an entire period. The figure below shows the timing diagram of an internal RC oscillator measurement.

AN5857 - Rev 1 page 16/23



Figure 12. Timing diagram of an internal RC oscillator measurement



The internal oscillator frequency value is computed as shown by the following formula:

Internal oscillator frequency = HSI Value / Capture

#### Where:

- HSI\_Value is the HSI48 frequency value: typical value is 48 MHz
- Capture represents an entire period of internal RC oscillator LSI: IC1ReadValue2 IC1ReadValue1.

As users can conclude from the above formula, the frequency measurement accuracy depends on the HSI48 frequency accuracy. Consequently, if a reference signal is available, users can run the internal RC oscillator calibration routine described in Internal RC oscillator calibration before performing the internal RC oscillator measurement procedure.

The input capture prescaler can be used for better measurement accuracy so the formula above becomes:

LSI\_Frequency = InputCapturePrescaler \* HSI\_Value / Capture\_Value

Note: If the HSE clock is available it is highly recommended to use it as the reference clock instead of HSI48 in order to benefit from its higher accuracy.

### 4.2 Description of the internal oscillator measurement firmware

The internal oscillator measurement firmware provided with this application note includes one C source file:

LSIMeasurement.c performing LSI frequency measurement using LSI\_FreqMeasure() function

The internal RC oscillator LSI is measured for a predefined number of periods. Then it returns the average value to minimize the error of the measured frequency.

Users can change this parameter (number of LSI periods) in the lsi\_measurement.h file:

#define LSI\_NUMBER\_OF\_LOOPS 10

AN5857 - Rev 1 page 17/23



# 5 Internal oscillator calibration/measurement example description

The example provided with this application note shows the ability of the firmware to calibrate the internal RC oscillator (HSI48) and explains how to use it to measure the internal RC oscillator (LSI) of the STM32C0 Series devices.

In this example:

- After system reset, the HSI48 is selected to be used as the system clock source.
- The HSI48 is calibrated using the LSE oscillator as a reference clock.
- After that, the LSI frequency is measured.
- Finally, the measured frequency of the different oscillators is sent to the NUCLEO-C031C6 board's USART2.

By default, the example uses the minimum error method to calibrate the HSI48 oscillator.

To run the calibration process that provides the frequency with fixed error, users have to comment out the following define in the main.c file:

```
#define CALIBRATION_MIN_ERROR
#define FIXED_ERROR
#define ERROR_CURVE
```

To choose the source of the reference frequency between LSE and GPIO, the user needs to comment or uncomment the following define value in main.h

```
#define USE_REFERENCE_LSE
```

The message below sent to the USART (implemented as virtual serial port on the Nucleo board) when running the demo with results of calibration and measurement.

```
HSI before: 47.930 MHz
HSI after: 48.024 MHz
LSI frequency: 31.758 kHz
```

This message can be displayed by some terminal software (while connecting Nucleo board to a PC) on STLINK virtual serial port. The HSI48 then the LSI is measured.

AN5857 - Rev 1 page 18/23



### 6 Conclusion

Even if the internal RC oscillator is factory-calibrated, the user should calibrate them in the operating environment, when a high-accuracy clock is required in the application.

This application note provides two routines:

- High-speed internal oscillator (HSI48) calibration: how to fine-tune the oscillator to the requested value.
- Low-speed internal oscillator (LSI) measurement: how to get the "exact" LSI frequency value.

Several frequency sources can be used to calibrate the internal RC oscillator (HSI48): LSE crystal, AC line, etc. Whatever the reference frequency source, the internal oscillator calibration principle is the same: a reference signal must be provided to be measured by a timer. The higher the accuracy of the reference signal frequency, the better the accuracy of the internal oscillator frequency measurement. The error is computed as the absolute value of the requested frequency value and the measured one for each trimming value. From this, the calibration value is calculated and then programmed in the trimming bits.

In order to improve the accuracy, the timer prescaler can be used to increase the ratio between the measured frequency and the reference frequency.

The second section of this application note is about the measurement of LSI oscillator. The internal connection between internal oscillators and embedded timers in the STM32C0 Series family is used for this purpose. The timer is clocked using the system clock source and configured in the Input capture mode. The captured time between two consecutive rising edges of internal oscillator represents an entire period.

AN5857 - Rev 1 page 19/23



# **Revision history**

**Table 1. Document revision history** 

Date	Version	Changes
14-Dec-2022	1	Initial release.

AN5857 - Rev 1 page 20/23



# **Contents**

1	Gen	neral information				
2	STM	M32C0 Series system clock3				
3	Internal RC oscillator calibration					
	3.1	HSI48	calibration principle	5		
	3.2	2 Hardware implementation				
		3.2.1	Case where LSE is used as the reference frequency to measure HSI48	6		
		3.2.2	Case where another source is used as the reference frequency to measure HSI48.	7		
	3.3	Descri	ption of the internal oscillator calibration firmware	7		
		3.3.1	Internal oscillator calibration with minimum error	7		
		3.3.2	HSI48 calibration with fixed error	9		
		3.3.3	Internal oscillator calibration using calibration curve	11		
		3.3.4	Internal oscillator frequency measurement	11		
	3.4	Recom	nmendations on the use of the calibration library	14		
	3.5	Calibra	ation process performance	15		
		3.5.1	Duration of the calibration process	15		
4	Low	-speed	internal oscillator measurement	16		
	4.1	Measu	urement principle	16		
	4.2	Descri	ption of the internal oscillator measurement firmware	17		
5	Inte	rnal osc	cillator calibration/measurement example description	18		
6						
Re			·			



# **List of figures**

Figure 1.	Simplified clock tree	. 3
Figure 2.	HSI48 oscillator trimming characteristics	. 4
Figure 3.	Timing diagram of internal oscillator calibration	. 5
Figure 4.	Hardware connection using LSE as the reference frequency	. 6
Figure 5.	Hardware connection using external reference frequency	. 7
Figure 6.	Internal oscillator calibration: finding the minimum frequency error	. 8
Figure 7.	Spring loop	. 9
Figure 8.	HSI48 calibration flowchart: maximum allowed frequency error	10
Figure 9.	Internal oscillator calibration: measuring the calibration table	11
Figure 10.	Internal oscillator frequency measurement flowchart	13
Figure 11.	LSI measurement configuration	16
Figure 12.	Timing diagram of an internal RC oscillator measurement	17

AN5857 - Rev 1 page 22/23



#### **IMPORTANT NOTICE - READ CAREFULLY**

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgment.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. For additional information about ST trademarks, refer to www.st.com/trademarks. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2022 STMicroelectronics - All rights reserved

AN5857 - Rev 1 page 23/23