

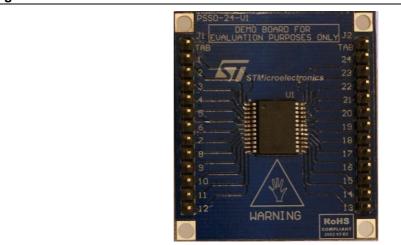
AN4212 Application note

PowerSSO-24 devices evaluation board

Introduction

The EV board concept is intended to provide customers with an easy way to connect ST's surface mounted VIPower[®] drivers into their existing prototype circuitry. The EV boards come pre-assembled with one of ST's VIPower high-side drivers.

Figure 1. PowerSSO-24 evaluation board



These boards provide mounting solutions and some heat sinking capability for prototype development. There are still external components required to make these devices work in any application.

Figure 2. PowerSSO-24 package



This document provides application schematics for the following evaluation boards:

Table 1. Evaluation board

12 V digital devices	12 V analog devices	24 V system device
EV-VND5E050K	EV-VND5E025AK	EV-VND5T035AK
EV-VNQ5E160K	EV-VNQ5E050AK	
EV-VNQ5E050K	EV-VNQ5E160AK	
	EV-VNQ5027AK	

September 2013 Doc ID 023983 Rev 2 1/11

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1 Application schematics

The evaluation boards themselves do not have any components other than the high-side driver that is mounted on it. The schematics provided in this document are recommended for their proper functionality. All designs have different needs and requirements. Whatever design you decide to use, it will still need to be verified in order to meet your application specifications. ST implies no guarantee or warranty (see *Appendix A: Reference documents*).

Note:

When more than one pin is used for an output, all output pins must be tied together for proper operation.

ST has produced two user manuals: the UM1556 (for 12 V devices) and the UM1557 (for 24 V devices) (see *Appendix A: Reference documents*). Those user manuals are VIPower hardware design guides that provide the necessary information to successfully design your circuit using our VIPower drivers.

1.1 Digital feedback designs

These devices have a logic level fault pin that is pulled low when a fault is detected and the status disable (ST_DIS) pin is held low. The feedback is an open Drain configuration and requires a pull-up resistor for proper operation. For this package type (PowerSSO-24) there are dual and quad output devices.

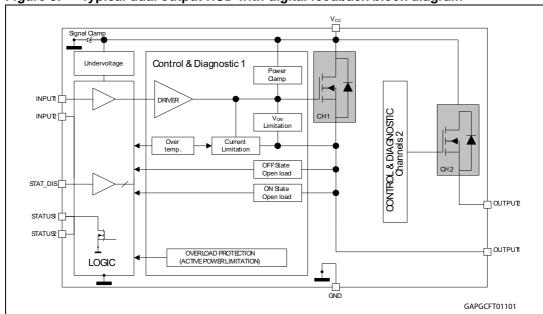


Figure 3. Typical dual output HSD with digital feedback block diagram

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1.1.1 VND5E050K-E application schematic

OUTPUT1 10k ∕√√√ OUTPUT1 N.C. 21_C OUTPUT1 OUTPUT1 **^**✓∧ INPUT1 10k ∕VV√ ОПТРИТ1 STATUS1 OUTPUT2 N.C. (TAB) OUTPUT2 STATUS2 07 OUTPUT2 N.C. STATUS2 √√√ 10k OUTPUT2 INPUT2 OUTPUT2 6 N.C. OUTPUT2 √√√ 10k For Reverse Battery Protection

Figure 4. VND5E050K-E evaluation board application schematic

1.1.2 VNQ5E160K-E and VNQ5E050K-E application schematic

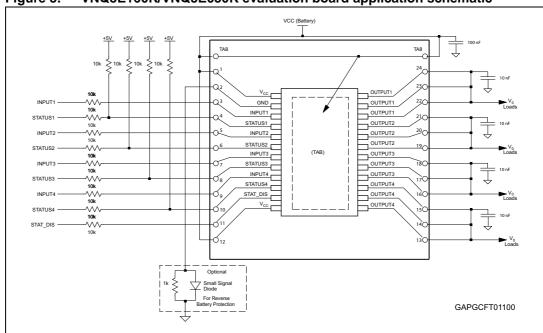


Figure 5. VNQ5E160K/VNQ5E050K evaluation board application schematic

1.2 Analog feedback drivers

Analog feedback devices have a sense resistor that, when properly scaled, provides a reasonable representation of the output current in terms of voltage that can be measured as well as its diagnostic information. How to properly scale that resistor can be found in the user manual UM1556 (see *Appendix A: Reference documents*).

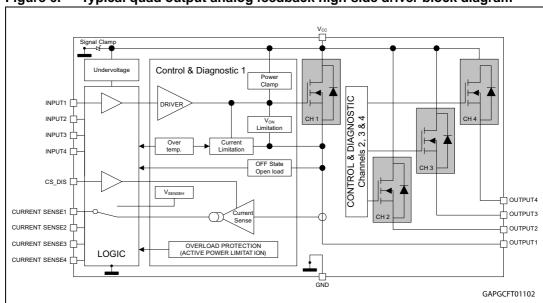


Figure 6. Typical quad output analog feedback high-side driver block diagram

1.2.1 VNQ5027AK-E, VNQ5E050AK and VNQ5E160AK application schematic

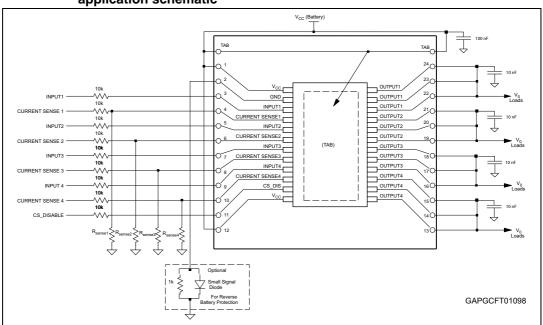
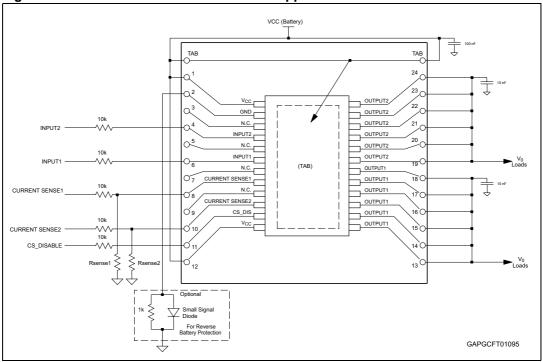


Figure 7. VNQ5027AK, VNQ5E050AK, and VNQ5E160AK evaluation board application schematic

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1.2.2 VND5E025AK application schematic

Figure 8. VND5E025AK evaluation board application schematic



24 V system devices 1.3

1.3.1 **VND5T035AK-E application schematic**

The VND5T035AK is a 24V system device. The maximum voltage rating for this device is elevated from the standard 41V to 58V. 24 V products are equipped with a latch-off feature keeping the device switched-off once power limitation or thermal shutdown is triggered. This latch can be activated by holding the FR_STBY pin high. Resetting the device to retry the load requires pulling the FR_STBY pin low. Leaving the FR_STBY pin low will disable the latch off feature and lets the part automatically retry shorted loads. More details can be found in the user manual UM1557 (see Appendix A: Reference documents).

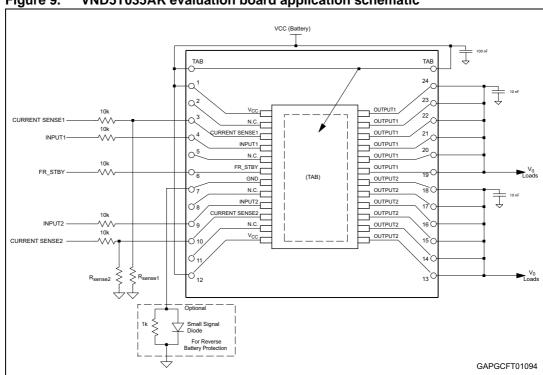


Figure 9. VND5T035AK evaluation board application schematic

AN4212 Reference documents

Appendix A Reference documents

- 1. VND5E050K evaluation board (EV-VND5E050K, DocID 023989)
- 2. VNQ5E160K evaluation board (EV-VNQ5E160K, DocID 023996)
- 3. VNQ5E050K evaluation board (EV-VN5E050AJ, DocID 023995)
- 4. VND5E025AK evaluation board (EV-VND5E050AJ, DocID 023978)
- 5. VNQ5E050AK evaluation board (EV-VNQ5E050AK, DocID 023979)
- 6. VNQ5E160AK evaluation board (EV-VNQ5E160AK, DocID 023980)
- 7. VNQ5027AK evaluation board (EV-VNQ5027AK, DocID 023981)
- 8. VND5T035AK evaluation board (EV-VND5E160J, DocID 023987)
- 9. VND5T100AJ evaluation board (EV-VND5T100AJ, DocID 023967)
- 10. VIPower M0-5 and M0-5Enhanced high-side drivers (UM1556, DocID 023520)
- 11. VIPower MO-5T: high-side switches for 24V systems (UM1557, DocID 023521)
- 12. Evaluation Product Licence Agreement on www.st.com

Revision history AN4212

Revision history

Table 2. Document revision history

Date	Revision	Changes
03-Dec-2012	1	Initial release.
16-Sep-2013	2	Updated disclaimer.

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